



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2126HS-TN  
(H14DSH , AMD EPYC 9755)

SPECspeed®2026\_int\_base = 8.26

SPECspeed®2026\_int\_peak = 8.26

CPU2026 License: 001176

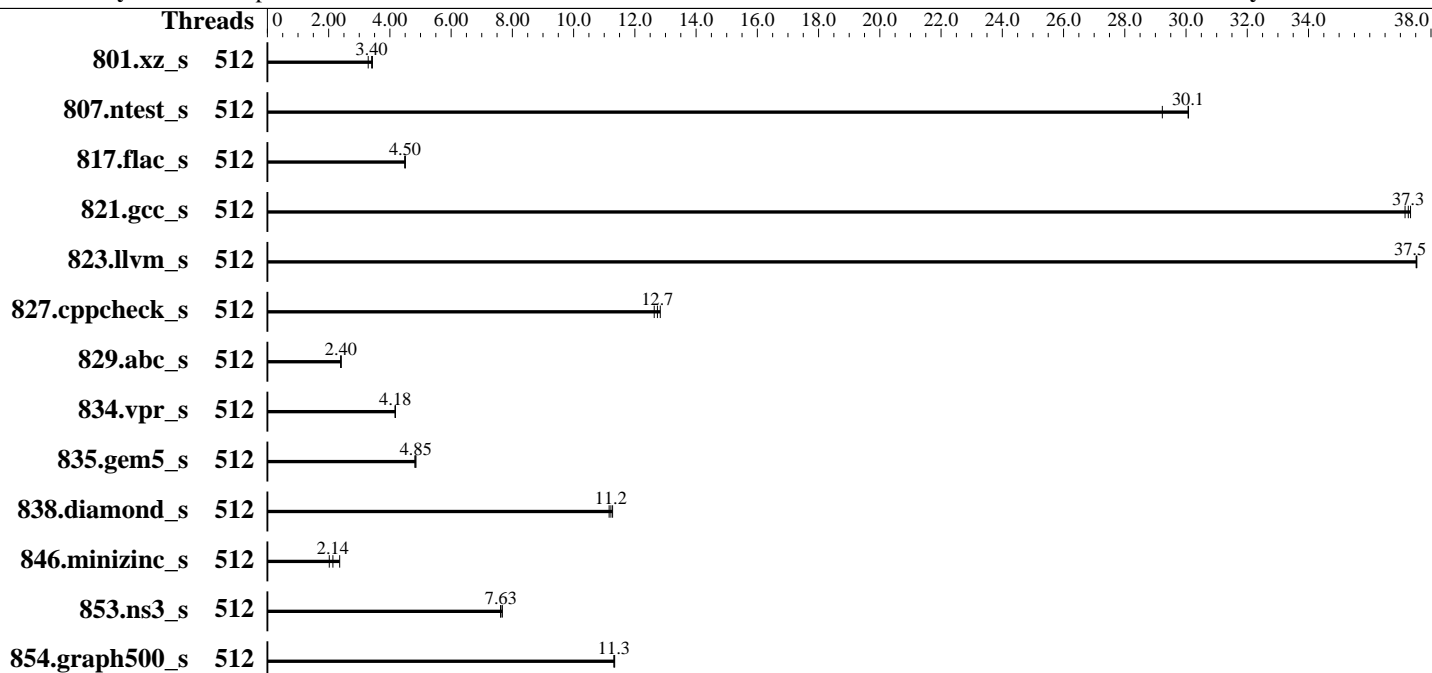
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2026

Hardware Availability: Oct-2024

Software Availability: Jan-2026



### Hardware

CPU Name: AMD EPYC 9755  
 Max MHz: 4100  
 Nominal: 2700  
 Enabled: 256 cores, 2 chips, 2 threads/core  
 Orderable: 2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 2304 GB (24 x 96 GB 2Rx4 PC5-6400B-R)  
 Storage: 1 x 3.84 TB NVMe SSD  
 Cooling: Air  
 Other: None

### Software

OS: Ubuntu 24.04.3 LTS  
 6.8.0-90-generic  
 Compiler: C/C++: Version 5.1.0 of AOCC  
 Fortran: Flang v22  
 Compiler Category: Vendor  
 Firmware: Version 1.5 released May-2025  
 File System: ext4  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
801.xz_s	512	<b>174</b>	<b>3.40</b>	172	3.43	180	3.29	512	<b>174</b>	<b>3.40</b>	172	3.43	180	3.29
807.nctest_s	512	37.9	30.1	<b>37.9</b>	<b>30.1</b>	39.0	29.2	512	37.9	30.1	<b>37.9</b>	<b>30.1</b>	39.0	29.2
817.flac_s	512	386	4.50	387	4.49	<b>386</b>	<b>4.50</b>	512	386	4.50	387	4.49	<b>386</b>	<b>4.50</b>
821.gcc_s	512	55.4	37.3	<b>55.6</b>	<b>37.3</b>	55.7	37.2	512	55.4	37.3	<b>55.6</b>	<b>37.3</b>	55.7	37.2
823.llvm_s	512	<b>37.6</b>	<b>37.5</b>	37.6	37.5	37.6	37.5	512	<b>37.6</b>	<b>37.5</b>	37.6	37.5	37.6	37.5
827.cppcheck_s	512	88.6	12.6	87.2	12.8	<b>87.9</b>	<b>12.7</b>	512	88.6	12.6	87.2	12.8	<b>87.9</b>	<b>12.7</b>
829.abc_s	512	347	2.39	<b>346</b>	<b>2.40</b>	345	2.41	512	347	2.39	<b>346</b>	<b>2.40</b>	345	2.41
834.vpr_s	512	<b>228</b>	<b>4.18</b>	228	4.18	229	4.17	512	<b>228</b>	<b>4.18</b>	228	4.18	229	4.17
835.gem5_s	512	<b>235</b>	<b>4.85</b>	237	4.82	235	4.85	512	<b>235</b>	<b>4.85</b>	237	4.82	235	4.85
838.diamond_s	512	88.8	11.3	<b>89.2</b>	<b>11.2</b>	89.7	11.2	512	88.8	11.3	<b>89.2</b>	<b>11.2</b>	89.7	11.2
846.minizinc_s	512	<b>313</b>	<b>2.14</b>	284	2.36	332	2.02	512	<b>313</b>	<b>2.14</b>	284	2.36	332	2.02
853.ns3_s	512	<b>151</b>	<b>7.63</b>	151	7.61	150	7.67	512	<b>151</b>	<b>7.63</b>	151	7.61	150	7.67
854.graph500_s	512	<b>54.0</b>	<b>11.3</b>	54.0	11.3	53.9	11.3	512	<b>54.0</b>	<b>11.3</b>	54.0	11.3	53.9	11.3

SPECspeed®2026\_int\_base = **8.26**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>  
Flang v22 is available at  
<https://flang.llvm.org/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,

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### Operating System Notes (Continued)

```
'sysctl -w vm.zone_reclaim_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.  
To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

### Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:  
GOMP_CPU_AFFINITY = "0-511"  
LD_LIBRARY_PATH =  
    "/spec/speccpu2026speed/amd_speed_aocc510_flang22_znver5_A_lib/lib:/spec  
    /speccpu2026speed/amd_speed_aocc510_flang22_znver5_A_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

### General Notes

Binaries were compiled on a system with an AMD EPYC 9754 CPU + 768 GiB Memory using Ubuntu 24.04

### Platform Notes

```
BIOS settings:  
SEV Control = Disabled  
SMEE = Disabled  
Memory Target Speed = DDR6400  
Determinism Control = Manual  
Determinism Enable = Power  
TDP control = Manual  
TDP = 500  
Package Power Limit Control = Manual  
Package Power Limit = 500  
TSME = Disabled  
  
Sysinfo program /spec/speccpu2026speed/bin/sysinfo  
Rev: 069f95da7e7f5d81b2ce48a82150e54f  
running on smc9694turin-u24-os Thu Feb 5 15:30:15 2026  
  
SUT (System Under Test) info as seen by some common utilities.  
  
-----  
Table of contents
```

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### Platform Notes (Continued)

- 
1. `uname -srvm`
  2. `w`
  3. Username
  4. `ulimit -a`
  5. `sysinfo process ancestry`
  6. `/proc/cpuinfo`
  7. `lscpu`
  8. `numactl --hardware`
  9. `/proc/meminfo`
  10. `who -r`
  11. Systemd service manager version: `systemd 255 (255.4-lubuntu8.10)`
  12. Services, from `systemctl list-unit-files`
  13. Linux kernel boot-time arguments, from `/proc/cmdline`
  14. `cpupower frequency-info`
  15. `sysctl`
  16. `/sys/kernel/mm/transparent_hugepage`
  17. `/sys/kernel/mm/transparent_hugepage/khugepaged`
  18. OS release
  19. Disk information
  20. `/sys/devices/virtual/dmi/id`
  21. `dmidecode`
  22. BIOS
- 

---

```
1. uname -srvm
Linux 6.8.0-90-generic #91-Ubuntu SMP PREEMPT_DYNAMIC Tue Nov 18 14:14:30 UTC 2025 x86_64
```

---

```
2. w
15:30:15 up 12:40, 3 users, load average: 0.07, 0.02, 0.00
USER      TTY      FROM          LOGIN@      IDLE        JCPU      PCPU      WHAT
root      tty      10.252.48.220 14:28      12:40m     0.00s     0.06s    sshd: root@notty
root      tty      10.252.48.220 14:28      12:40m     0.00s     0.21s    sshd: root@pts/1
root      tty1     -             04:47      10:43m     0.08s     ?        -bash
```

---

```
3. Username
From environment variable $USER: root
```

---

```
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
```

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### Platform Notes (Continued)

```

coredump(blocks)      0
memory(kbytes)        unlimited
locked memory(kbytes) 2097152
process                9286111
nofiles                1024
vmemory(kbytes)       unlimited
locks                  unlimited
rtprio                 0

```

-----  
5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --system --deserialize=115
SCREEN -S cpu
/bin/bash
python3 ./run_amd_speed_aocc510_flang22_znver5_A1.py
/bin/bash ./amd_speed_aocc510_flang22_znver5_A1.sh
runcpu --config amd_speed_aocc510_flang22_znver5_A1.cfg --tune base --reportable --iterations 3 intspeerd
runcpu --configfile amd_speed_aocc510_flang22_znver5_A1.cfg --tune base --reportable --iterations 3
--nopower --runmode speed --tune base --size test:train:refspeerd intspeerd --nopreenv --note-preenv
--logfile $$SPEC/tmp/CPU2026.001/templogs/preenv.intspeerd.001.0.log --lognum 001.0 --from_runcpu 2
specperl $$SPEC/bin/sysinfo
$$SPEC = /spec/speccpu2026speed

```

-----  
6. /proc/cpuinfo

```

model name           : AMD EPYC 9755 128-Core Processor
vendor_id             : AuthenticAMD
cpu family           : 26
model                 : 2
stepping              : 1
microcode             : 0xb002147
bugs                  : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size              : 192 4K pages
cpu cores             : 128
siblings              : 256
2 physical ids (chips)
512 processors (hardware threads)
physical id 0: core ids 0-127
physical id 1: core ids 0-127
physical id 0: apicids 0-255
physical id 1: apicids 256-511

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

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### Platform Notes (Continued)

From lscpu from util-linux 2.39.3:

```

Architecture:                x86_64
CPU op-mode(s):              32-bit, 64-bit
Address sizes:                52 bits physical, 57 bits virtual
Byte Order:                   Little Endian
CPU(s):                       512
On-line CPU(s) list:         0-511
Vendor ID:                    AuthenticAMD
BIOS Vendor ID:              Advanced Micro Devices, Inc.
Model name:                   AMD EPYC 9755 128-Core Processor
BIOS Model name:             AMD EPYC 9755 128-Core Processor           Unknown CPU @ 2.7GHz
BIOS CPU family:             107
CPU family:                   26
Model:                        2
Thread(s) per core:          2
Core(s) per socket:          128
Socket(s):                    2
Stepping:                     1
Frequency boost:              enabled
CPU(s) scaling MHz:          100%
CPU max MHz:                  2700.0000
CPU min MHz:                  1500.0000
BogoMIPS:                     5400.11

```

```

Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaveerptr rdpru wbnoinvd amd_ppin cpc amd_ibpb_ret arat npt lbrv
svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl
vnni avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
L1d cache:                   12 MiB (256 instances)
L1i cache:                   8 MiB (256 instances)
L2 cache:                     256 MiB (256 instances)
L3 cache:                     1 GiB (32 instances)

```

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### Platform Notes (Continued)

```

NUMA node(s):                2
NUMA node0 CPU(s):           0-127,256-383
NUMA node1 CPU(s):           128-255,384-511
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:   Not affected
Vulnerability Lltf:           Not affected
Vulnerability Mds:            Not affected
Vulnerability Meltdown:       Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:       Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:      Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:      Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:          Not affected
Vulnerability Tsx async abort: Not affected
Vulnerability Vmscape:        Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	12M	12	Data	1	64	1	64
L1i	32K	8M	8	Instruction	1	64	1	64
L2	1M	256M	16	Unified	2	1024	1	64
L3	32M	1G	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-127,256-383
node 0 size: 1160619 MB
node 0 free: 1156794 MB
node 1 cpus: 128-255,384-511
node 1 size: 1160986 MB
node 1 free: 1155473 MB
node distances:
node  0  1
  0:  10  32
  1:  32  10

```

9. /proc/meminfo

MemTotal: 2377324240 kB

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### Platform Notes (Continued)

10. who -r  
run-level 5 Feb 5 02:50

-----  
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.10)  
Default Target Status  
graphical running

-----  
12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager apparmor appport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd networkd-dispatcher nvme-fc-boot-connections nvme-autoconnect open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades vgauth
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	console-getty debug-shell iscsid nftables rsync serial-getty@ ssh systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysexit systemd-time-wait-sync upower
indirect	systemd-sysupdate systemd-sysupdate-reboot uidd
masked	cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-6.8.0-90-generic  
root=UUID=365b48f2-b036-433d-970b-b75b18c86cd8  
ro

-----  
14. cpupower frequency-info

analyzing CPU 177:  
current policy: frequency should be within 1.50 GHz and 2.70 GHz.  
The governor "performance" may decide which speed to use within this range.  
  
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 2700MHz

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### Platform Notes (Continued)

-----  
15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

-----  
16. /sys/kernel/mm/transparent\_hugepage

defrag	[always]	defer	defer+madvise	madvise	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

-----  
17. /sys/kernel/mm/transparent\_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

-----  
18. OS release

From	/etc/*-release	/etc/*-version
os-release	Ubuntu 24.04.3 LTS	

-----  
19. Disk information

SPEC is set to: /spec/speccpu2026speed

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### Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme6nlp1	ext4	3.5T	28G	3.5T	1%	/spec

```

20. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: AS -2126HS-TN
Product Family: SMC H14
Serial: S920464X4819694

```

#### 21. dmidecode

Additional information from dmidecode 3.5 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
24x Samsung M321RYGA0PB2-CCPWC 96 GB 2 rank 6400

#### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

```

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.5
BIOS Date: 05/12/2025
BIOS Revision: 5.35

```

### Compiler Version Notes

C | 854.graph500\_s(base)

```

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

```

C++ | 807.ntest\_s(base) 827.cppcheck\_s(base) 853.ns3\_s(base)

```

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin

```

(Continued on next page)



# SPEC CPU®2026 Integer Speed Result

Copyright 2026 Standard Performance Evaluation Corporation

## Supermicro

Hyper A+ Server AS -2126HS-TN  
(H14DSH , AMD EPYC 9755)

SPECspeed®2026\_int\_base = 8.26

SPECspeed®2026\_int\_peak = 8.26

CPU2026 License: 001176  
Test Sponsor: Supermicro  
Tested by: Supermicro

Test Date: Feb-2026  
Hardware Availability: Oct-2024  
Software Availability: Jan-2026

## Compiler Version Notes (Continued)

```

=====
C++, C | 801.xz_s(base) 817.flac_s(base) 821.gcc_s(base) 823.llvm_s(base)
      | 829.abc_s(base) 834.vpr_s(base) 835.gem5_s(base) 838.diamond_s(base)
      | 846.minizinc_s(base)
=====

```

```

AMD clang version 17.0.6 (CLANG: AOCC_5.1.0-Build#1994 2025_12_23)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-5.1.0/bin
=====

```

## Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Benchmarks using both C and C++:  
clang++ clang

## Base Portability Flags

```

801.xz_s: -DSPEC_LP64
807.ntest_s: -DSPEC_LP64
817.flac_s: -DSPEC_LP64
821.gcc_s: -DSPEC_LP64
823.llvm_s: -DSPEC_LP64
827.cppcheck_s: -DSPEC_LP64
829.abc_s: -DSPEC_LP64
834.vpr_s: -fno-finite-math-only -DSPEC_LP64
835.gem5_s: -fno-finite-math-only -DSPEC_LP64
838.diamond_s: -DSPEC_LP64
846.minizinc_s: -DSPEC_LP64
853.ns3_s: -fno-finite-math-only -DSPEC_LP64
854.graph500_s: -DSPEC_LP64

```



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## Base Optimization Flags

C benchmarks:

```
-m64 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3 -flto
-march=znver5 -fveclib=AMDLIBM -ffast-math -zopt -fremap-arrays
-fstrip-mining -fstruct-layout=7 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=50
-fopenmp -DSPEC_OPENMP -lamdalloc -lamdlibm -fopenmp=libomp -lomp
```

C++ benchmarks:

```
-m64 -std=c++17 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -flto -march=znver5
-fveclib=AMDLIBM -ffast-math -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -fopenmp -DSPEC_OPENMP
-fvirtual-function-elimination -fvisibility=hidden -lamdalloc
-lamdlibm -fopenmp=libomp -lomp
```

Benchmarks using both C and C++:

```
-m64 -std=c++17 -std=c18 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -flto -march=znver5
-fveclib=AMDLIBM -ffast-math -zopt -fremap-arrays -fstrip-mining
-fstruct-layout=7 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=50
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-fopenmp -DSPEC_OPENMP -fvirtual-function-elimination
-fvisibility=hidden -lamdalloc -lamdlibm -fopenmp=libomp -lomp
```

## Base Other Flags

C benchmarks:

```
-Wno-return-type
```

Benchmarks using both C and C++:

```
-Wno-return-type
```

## Peak Optimization Flags

C benchmarks:

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# SPEC CPU®2026 Integer Speed Result

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## Supermicro

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SPECspeed®2026\_int\_base = 8.26

SPECspeed®2026\_int\_peak = 8.26

**CPU2026 License:** 001176  
**Test Sponsor:** Supermicro  
**Tested by:** Supermicro

**Test Date:** Feb-2026  
**Hardware Availability:** Oct-2024  
**Software Availability:** Jan-2026

## Peak Optimization Flags (Continued)

854.graph500\_s: basepeak = yes

C++ benchmarks:

807.ntest\_s: basepeak = yes

827.cppcheck\_s: basepeak = yes

853.ns3\_s: basepeak = yes

Benchmarks using both C and C++:

801.xz\_s: basepeak = yes

817.flac\_s: basepeak = yes

821.gcc\_s: basepeak = yes

823.llvm\_s: basepeak = yes

829.abc\_s: basepeak = yes

834.vpr\_s: basepeak = yes

835.gem5\_s: basepeak = yes

838.diamond\_s: basepeak = yes

846.minizinc\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2026/results/flags/aocc-flags.html>

<http://www.spec.org/cpu2026/results/flags/Supermicro-Platform-Settings-V1.2-Turin-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2026/results/flags/aocc-flags.xml>

<http://www.spec.org/cpu2026/results/flags/Supermicro-Platform-Settings-V1.2-Turin-revG.xml>

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