



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL345 Gen12

(3.15 GHz, AMD EPYC 9455P)

SPECspeed®2017_fp_base = 316

SPECspeed®2017_fp_peak = 322

CPU2017 License: 3

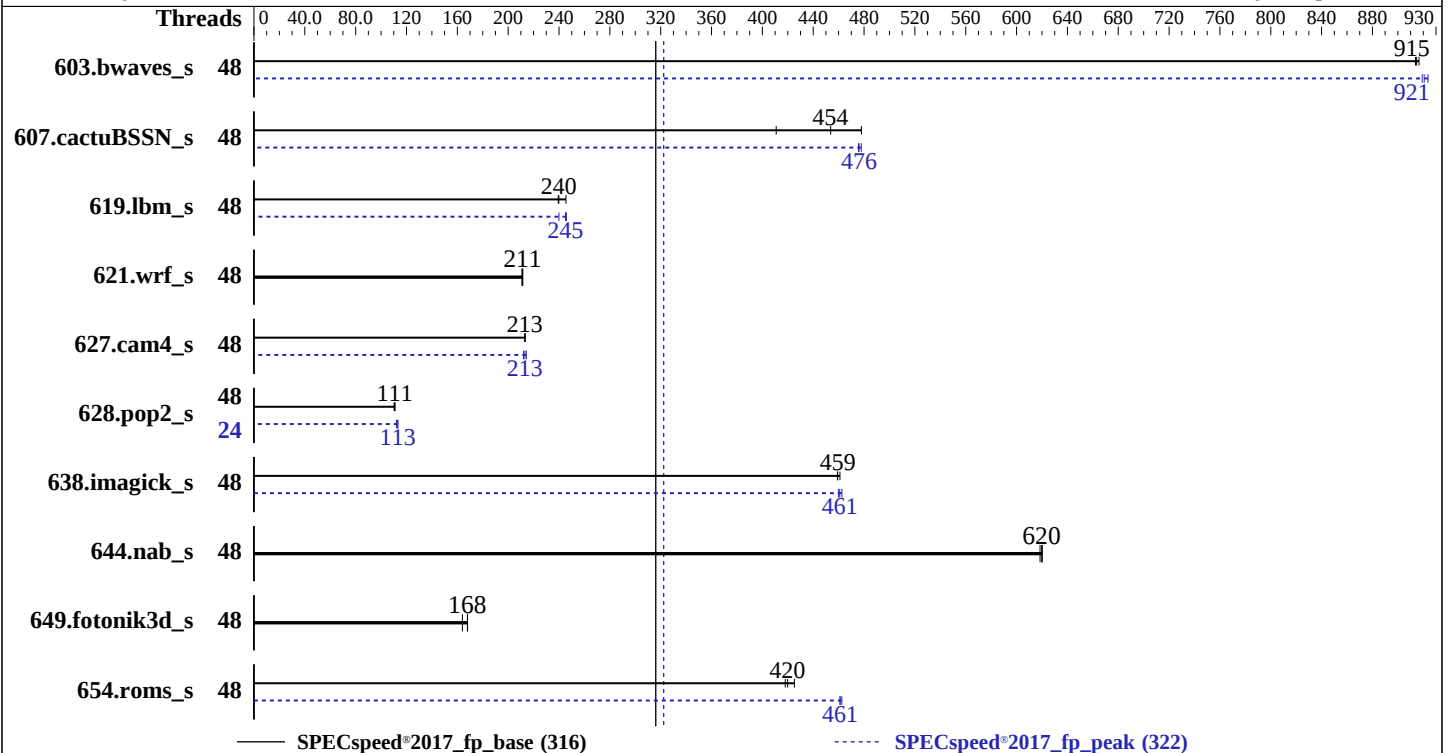
Test Sponsor: HPE

Tested by: HPE

Test Date: Sep-2025

Hardware Availability: Aug-2025

Software Availability: Apr-2025



Hardware

CPU Name: AMD EPYC 9455P
Max MHz: 4400
Nominal: 3150
Enabled: 48 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 6 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage: 1 x 1.92 TB 64G NVME SS
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP7
Kernel 6.4.0-150700.51-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: HPE BIOS Version v1.20 07/11/2025 released Jul-2025
File System: xfs
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	48	64.4	917	<u>64.5</u>	<u>915</u>	64.6	914	48	63.9	924	64.2	919	<u>64.1</u>	<u>921</u>
607.cactuBSSN_s	48	<u>36.7</u>	<u>454</u>	34.9	478	40.6	411	48	35.0	476	34.9	478	<u>35.0</u>	<u>476</u>
619.lbm_s	48	<u>21.8</u>	<u>240</u>	21.9	239	21.3	245	48	21.8	240	21.3	246	<u>21.4</u>	<u>245</u>
621.wrf_s	48	62.5	211	<u>62.6</u>	<u>211</u>	62.7	211	48	62.5	211	<u>62.6</u>	<u>211</u>	62.7	211
627.cam4_s	48	41.6	213	<u>41.6</u>	<u>213</u>	41.5	213	48	41.4	214	41.8	212	<u>41.6</u>	<u>213</u>
628.pop2_s	48	<u>107</u>	<u>111</u>	107	111	108	110	24	106	112	105	113	<u>105</u>	<u>113</u>
638.imagick_s	48	31.4	459	31.3	461	<u>31.4</u>	<u>459</u>	48	<u>31.3</u>	<u>461</u>	31.2	463	31.4	460
644.nab_s	48	28.3	618	28.2	620	<u>28.2</u>	<u>620</u>	48	28.3	618	28.2	620	<u>28.2</u>	<u>620</u>
649.fotonik3d_s	48	55.6	164	<u>54.3</u>	<u>168</u>	54.2	168	48	55.6	164	<u>54.3</u>	<u>168</u>	54.2	168
654.roms_s	48	<u>37.5</u>	<u>420</u>	37.0	425	37.7	418	48	34.0	463	<u>34.1</u>	<u>461</u>	34.1	461

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-95"

LD_LIBRARY_PATH =

"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "96"

Environment variables set by runcpu during the 603.bwaves_s peak run:

GOMP_CPU_AFFINITY = "0-47"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:

GOMP_CPU_AFFINITY = "0-47"

Environment variables set by runcpu during the 619.lbm_s peak run:

GOMP_CPU_AFFINITY = "0-47"

Environment variables set by runcpu during the 627.cam4_s peak run:

GOMP_CPU_AFFINITY = "0-47"

Environment variables set by runcpu during the 628.pop2_s peak run:

GOMP_CPU_AFFINITY = "0-23"

Environment variables set by runcpu during the 638.imagick_s peak run:

GOMP_CPU_AFFINITY = "0-47"

Environment variables set by runcpu during the 654.roms_s peak run:

GOMP_CPU_AFFINITY = "0-47"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below

Workload Profile set to General Peak Frequency Compute

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

ACPI CST C2 Latency set to 18 microseconds

Last-Level Cache (LLC) as NUMA Node set to Enabled

NUMA memory domains per socket set to Four memory domains per socket

Thermal Configuration set to Enhanced CPU Cooling

Workload Profile set to Custom

Power Regulator set to OS Control Mode

(Continued on next page)



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Platform Notes (Continued)

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Sep 8 01:35:28 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost 6.4.0-150700.51-default #1 SMP PREEMPT_DYNAMIC Wed Apr 30 21:35:43 UTC 2025 (6930611)
x86_64 x86_64 x86_64 GNU/Linux

2. w
01:35:28 up 2:37, 3 users, load average: 6.82, 6.48, 3.94
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root pts/1 10.79.30.185:S.0 22:59 2:35m 0.81s 0.11s /bin/bash ./amd_speed_aocc500_znver5_A1.sh

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 3092866
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200

(Continued on next page)



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Platform Notes (Continued)

```
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 3092866
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
```

```
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
```

```
sshd: root [priv]
```

```
sshd: root@pts/0
```

```
-bash
```

```
screen -S cpu
```

```
SCREEN -S cpu
```

```
/bin/bash
```

```
python3 ./run_intspeed.py
```

```
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
```

```
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
```

```
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
```

```
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
```

```
$SPEC/tmp/CPU2017.037/temlogs/preenv.fpspeed.037.0.log --lognum 037.0 --from_runcpu 2
```

```
specperl $SPEC/bin/sysinfo
```

```
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9455P 48-Core Processor
```

```
vendor_id       : AuthenticAMD
```

```
cpu family      : 26
```

```
model           : 2
```

```
stepping        : 1
```

```
microcode       : 0xb002151
```

```
bugs             : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srs0
```

```
TLB size        : 192 4K pages
```

```
cpu cores        : 48
```

```
siblings         : 96
```

```
1 physical ids (chips)
```

```
96 processors (hardware threads)
```

```
physical id 0: core ids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
```

```
physical id 0: apicids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.40.4:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             96
On-line CPU(s) list: 0-95
Vendor ID:          AuthenticAMD
Model name:         AMD EPYC 9455P 48-Core Processor
CPU family:         26
Model:              2
Thread(s) per core: 2
```

(Continued on next page)



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Platform Notes (Continued)

Core(s) per socket: 48
Socket(s): 1
Stepping: 1
Frequency boost: enabled
CPU(s) scaling MHz: 101%
CPU max MHz: 3150.0000
CPU min MHz: 1500.0000
BogoMIPS: 6289.90
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc amd_ibpb_ret arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_lld debug_swap hv_inuse_wr_allowed srso_user_kernel_no amd_lbr_pmc_freeze

Virtualization: AMD-V
L1d cache: 2.3 MiB (48 instances)
L1i cache: 1.5 MiB (48 instances)
L2 cache: 48 MiB (48 instances)
L3 cache: 256 MiB (8 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-5,48-53
NUMA node1 CPU(s): 6-11,54-59
NUMA node2 CPU(s): 12-17,60-65
NUMA node3 CPU(s): 18-23,66-71
NUMA node4 CPU(s): 24-29,72-77
NUMA node5 CPU(s): 30-35,78-83
NUMA node6 CPU(s): 36-41,84-89
NUMA node7 CPU(s): 42-47,90-95
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; IBPB on VMEXIT only
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

(Continued on next page)



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Platform Notes (Continued)

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	1M	48M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-5,48-53

node 0 size: 96376 MB

node 0 free: 95883 MB

node 1 cpus: 6-11,54-59

node 1 size: 96764 MB

node 1 free: 95826 MB

node 2 cpus: 12-17,60-65

node 2 size: 96764 MB

node 2 free: 96215 MB

node 3 cpus: 18-23,66-71

node 3 size: 96764 MB

node 3 free: 96396 MB

node 4 cpus: 24-29,72-77

node 4 size: 96764 MB

node 4 free: 96440 MB

node 5 cpus: 30-35,78-83

node 5 size: 96764 MB

node 5 free: 96479 MB

node 6 cpus: 36-41,84-89

node 6 size: 96764 MB

node 6 free: 95670 MB

node 7 cpus: 42-47,90-95

node 7 size: 96280 MB

node 7 free: 95893 MB

node distances:

node	0	1	2	3	4	5	6	7
0:	10	11	12	12	12	12	12	12
1:	11	10	12	12	12	12	12	12
2:	12	12	10	11	12	12	12	12
3:	12	12	11	10	12	12	12	12
4:	12	12	12	12	10	11	12	12
5:	12	12	12	12	11	10	12	12
6:	12	12	12	12	12	12	10	11
7:	12	12	12	12	12	12	11	10

9. /proc/meminfo

MemTotal: 791802516 kB

10. who -r

run-level 5 Sep 7 22:58

11. Systemd service manager version: systemd 254 (254.24+suse.148.g83b9060b6e)

Default Target Status
graphical running

12. Services, from systemctl list-unit-files

(Continued on next page)



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Platform Notes (Continued)

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings kdump kdump-early kdump-notify klog lvm2-monitor nscd nvme-fc-boot-connections nvme-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant systemd-remount-fs
enabled-runtime	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon amavis autofs autostart-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd clamd clamondac collectd console-getty cups cups-browsed debug-shell dhcpd dhcpd6 dhcrelay dhcrelay6 dnsmasq ebttables exchange-bmc-os-info fancontrol firewallld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lm_sensors lunmask man-db-create memcached multipathd munge named nfs nfs-blkmap nfs-server nfsserver nmb ostree-remount ostree-state-overlay@ pcp-geolocate pmcd pmfind pmie pmie_farm pmlogger pmlogger_farm pmproxy proftpd rpcbind rpmconfigcheck rsyncd rtkit-daemon samba-bgqd serial-getty@ smartd-generate_opts smb snmpd snmptrapd speech-dispatcherd sysstat systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@ vsftpd wpa_supplicant@ pcscd systemd-userdbd wickedd
disabled	
indirect	

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150700.51-default
root=UUID=e9c24f90-4995-4bc8-80c5-b1e0dc1c50dd
splash=silent
resume=/dev/disk/by-uuid/f8d2aff2-72e3-490e-b7aa-5412864f7e46
mitigations=auto
quiet
security=apparmor
crashkernel=374M,high
crashkernel=72M,low
amd_pstate=active
```

14. cpupower frequency-info

```
analyzing CPU 18:
  current policy: frequency should be within 1.50 GHz and 3.15 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
```

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0

(Continued on next page)



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Platform Notes (Continued)

```
vm.nr_overcommit_hugepages      0
vm.swappiness                    1
vm.watermark_boost_factor       15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag                1
max_ptes_none         511
max_ptes_shared       256
max_ptes_swap         64
pages_to_scan         4096
scan_sleep_millisecs 10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP7
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p5 xfs   953G  26G  928G   3% /home
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     HPE ProLiant Compute DL345 Gen12
Product Family: ProLiant
Serial:      2M110725V1
```

```
-----
21. dmidecode
Additional information from dmidecode 3.6 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  9x Hynix HMC694AHBRA480N 64 GB 2 rank 6400, configured at 5200
  3x Micron MTC40F2046S1RC64BD2 QSFF 64 GB 2 rank 6400, configured at 5200
```

```
-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      HPE
BIOS Version:     1.20
BIOS Date:        07/11/2025
BIOS Revision:    1.20
Firmware Revision: 1.16
```



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SPECspeed®2017_fp_peak = 322

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Sep-2025

Hardware Availability: Aug-2025

Software Availability: Apr-2025

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1377 2024_09_24)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-5.0.0/bin
=====

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

(Continued on next page)



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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

-Wl, -mllvm -Wl, -enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl, -mllvm -Wl, -align-all-nofallthru-blocks=6
-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=2000000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

(Continued on next page)



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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlbm -lamdalloc -lflang
```

638.imagick_s: Same as 619.lbm_s

644.nab_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlbm -lamdalloc -lflang
```

649.fotonik3d_s: basepeak = yes

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Peak Optimization Flags (Continued)

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIB -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -Mrecursive
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIB -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -Mrecursive -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIB -ffast-math -fopenmp -flto -DSPEC_OPENMP
-fremap-arrays -fstrip-mining -fstruct-layout=9
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

(Continued on next page)



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Peak Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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