



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

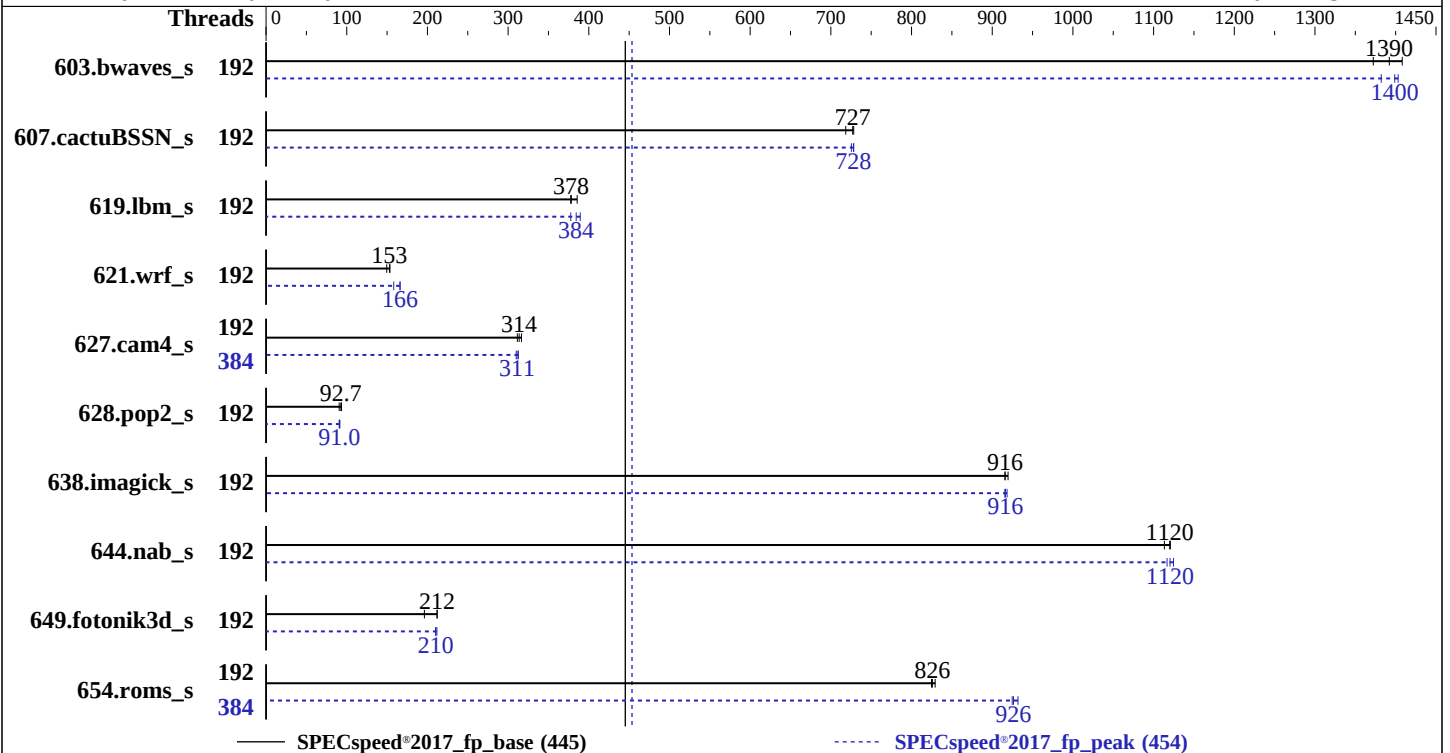
Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025



Hardware

CPU Name: AMD EPYC 9645
Max MHz: 3700
Nominal: 2300
Enabled: 192 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 12 cores
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 1 TB NVMe
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04 LTS
kernel version 6.14.0-29-generic
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: Version ES312AMS.205T5 released Sep-2025
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: OS and BIOS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	192	<u>42.4</u>	<u>1390</u>	41.9	1410	43.0	1370	192	<u>42.2</u>	<u>1400</u>	42.0	1400	42.7	1380
607.cactuBSSN_s	192	23.2	718	22.9	728	<u>22.9</u>	<u>727</u>	192	22.9	728	23.0	725	<u>22.9</u>	<u>728</u>
619.lbm_s	192	<u>13.8</u>	<u>378</u>	13.9	378	13.6	386	192	13.5	389	13.9	378	<u>13.6</u>	<u>384</u>
621.wrf_s	192	86.2	153	<u>86.3</u>	<u>153</u>	88.4	150	192	83.6	158	79.6	166	<u>79.7</u>	<u>166</u>
627.cam4_s	192	28.5	311	<u>28.3</u>	<u>314</u>	28.0	317	384	28.3	313	28.6	310	<u>28.5</u>	<u>311</u>
628.pop2_s	192	131	90.7	<u>128</u>	<u>92.7</u>	127	93.5	192	130	91.5	131	90.6	<u>130</u>	<u>91.0</u>
638.imagick_s	192	<u>15.7</u>	<u>916</u>	15.7	920	15.8	916	192	15.8	916	15.7	918	<u>15.7</u>	<u>916</u>
644.nab_s	192	15.7	1110	15.6	1120	<u>15.6</u>	<u>1120</u>	192	<u>15.6</u>	<u>1120</u>	15.5	1120	15.6	1120
649.fotonik3d_s	192	<u>43.0</u>	<u>212</u>	46.5	196	43.0	212	192	43.1	212	43.4	210	<u>43.3</u>	<u>210</u>
654.roms_s	192	19.0	829	19.1	825	<u>19.1</u>	<u>826</u>	384	16.9	932	<u>17.0</u>	<u>926</u>	17.0	925

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero MDA200A3N-212)
(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-383"

LD_LIBRARY_PATH =

"/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "384"

Environment variables set by runcpu during the 603.bwaves_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 619.lbm_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 621.wrf_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 627.cam4_s peak run:

GOMP_CPU_AFFINITY = "0-383"

Environment variables set by runcpu during the 628.pop2_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 638.imagick_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 644.nab_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

GOMP_CPU_AFFINITY = "0-191"

Environment variables set by runcpu during the 654.roms_s peak run:

GOMP_CPU_AFFINITY = "0-383"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
cTDP: 400

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

Determinism Slider set to Power

ACPI SRAT L3 Cache as NUMA Domain: enabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on amd-benchmark Sat Sep 20 15:08:07 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux amd-benchmark 6.14.0-29-generic #29~24.04.1-Ubuntu SMP PREEMPT_DYNAMIC Thu Aug 14 16:52:50 UTC 2
x86_64 x86_64 x86_64 GNU/Linux

2. w
15:08:07 up 3:22, 1 user, load average: 6.12, 7.24, 4.49
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
amd tty1 - 12:05 3:01m 0.52s 0.51s sudo su

3. Username
From environment variable \$USER: root
From the command 'logname': amd

4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero MDA200A3N-212)
(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

```
locked memory(kbytes) 2097152
process                6189361
nofiles                1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                 0
```

5. sysinfo process ancestry

```
/sbin/init splash
/bin/login -p --
-bash
sudo su
sudo su
su
bash
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9645 96-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 17
stepping        : 0
microcode       : 0xb101025
bugs             : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size        : 192 4K pages
cpu cores       : 96
siblings        : 192
2 physical ids (chips)
384 processors (hardware threads)
physical id 0:  core ids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123
physical id 1:  core ids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123
physical id 0:  apicids 0-23,32-55,64-87,96-119,128-151,160-183,192-215,224-247
physical id 1:  apicids 256-279,288-311,320-343,352-375,384-407,416-439,448-471,480-503
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64	
CPU op-mode(s):	32-bit, 64-bit	
Address sizes:	52 bits physical, 57 bits virtual	
Byte Order:	Little Endian	
CPU(s):	384	
On-line CPU(s) list:	0-383	
Vendor ID:	AuthenticAMD	
BIOS Vendor ID:	Advanced Micro Devices, Inc.	
Model name:	AMD EPYC 9645 96-Core Processor	
BIOS Model name:	AMD EPYC 9645 96-Core Processor	Unknown CPU @

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero MDA200A3N-212)
(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

BIOS CPU family:	2.3GHz
CPU family:	107
Model:	26
Thread(s) per core:	17
Core(s) per socket:	2
Socket(s):	96
Stepping:	2
Frequency boost:	0
CPU(s) scaling MHz:	enabled
CPU max MHz:	51%
CPU min MHz:	3712.0000
BogoMIPS:	600.0000
Flags:	4592.62
	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfnr vaes vpcmlmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_l1d debug_swap amd_lbr_pmc_freeze
Virtualization:	AMD-V
L1d cache:	9 MiB (192 instances)
L1i cache:	6 MiB (192 instances)
L2 cache:	192 MiB (192 instances)
L3 cache:	512 MiB (16 instances)
NUMA node(s):	2
NUMA node0 CPU(s):	0-95,192-287
NUMA node1 CPU(s):	96-191,288-383
Vulnerability Gather data sampling:	Not affected
Vulnerability Ghostwrite:	Not affected
Vulnerability Indirect target selection:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability L1tf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Mitigation; IBPB on VMEXIT only
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	9M	12	Data	1	64	1	64
L1i	32K	6M	8	Instruction	1	64	1	64
L2	1M	192M	16	Unified	2	1024	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-95,192-287

node 0 size: 773573 MB

node 0 free: 770905 MB

node 1 cpus: 96-191,288-383

node 1 size: 773943 MB

node 1 free: 771319 MB

node distances:

node 0 1

0: 10 32

1: 32 10

9. /proc/meminfo

MemTotal: 1584657572 kB

10. who -r

run-level 3 Sep 20 10:54

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.8)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon anacron apparmor apport avahi-daemon bluetooth cloud-config cloud-final cloud-init cloud-init-local collectd console-setup cron cups cups-browsed dmesg e2scrub_reap getty@ gnome-remote-desktop gpu-manager grub-common grub-initrd-fallback kerneloops keyboard-setup networkd-dispatcher openvpn power-profiles-daemon rsyslog secureboot-db setvtrgb snapd ssl-cert sssd switcheroo-control sysstat systemd-oom systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades wpa_supplicant
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	brltty console-getty debug-shell ipmievdt nftables openvpn-client@ openvpn-server@ openvpn@ rsync rtkit-daemon serial-getty@ speech-dispatcherd ssh systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd systemd-networkd-wait-online systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysex systemd-time-wait-sync upower wpa_supplicant-nl80211@ wpa_supplicant-wired@ wpa_supplicant@
generated	openipmi speech-dispatcher
indirect	saned@ spice-vdagentd sssd-autofs sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot uuid
masked	alsa-utils cryptdisks cryptdisks-early hwclock saned sudo x11-common

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/vmlinuz-6.14.0-29-generic
root=UUID=4c4111a1-2027-4689-8cc8-4d6d365932ca
ro
quiet
splash
vt.handoff=7
```

14. cpupower frequency-info

```
analyzing CPU 283:
  current policy: frequency should be within 1.89 GHz and 3.71 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes
```

15. sysctl

```
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
```

16. /sys/kernel/mm/transparent_hugepage

```
defrag      [always] defer defer+madvise madvise never
enabled     [always] madvise never
hpage_pmd_size  2097152
shmem_enabled  always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero MDA200A3N-212)
(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Platform Notes (Continued)

18. OS release

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS

19. Disk information

SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p5 ext4 701G 7.8G 657G 2% /home

20. /sys/devices/virtual/dmi/id

Vendor: Tyrone Systems
Product: Tyrone Camarero MDA200A3N-212
Product Family: Camarero

21. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: ES312AMS.205T5
BIOS Date: 09/02/2025
BIOS Revision: 5.35

Compiler Version Notes

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero MDA200A3N-212)
(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.lbm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64

627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64

628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64

638.imagick_s: -DSPEC_LP64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Base Portability Flags (Continued)

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

638.imagick_s: Same as 619.lbm_s

```
644.nab_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

```
649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm
-lamdalloc -lflang
```

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero MDA200A3N-212)
(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Peak Optimization Flags (Continued)

621.wrf_s (continued):

```
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIB -ffast-math -fopenmp  
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining  
-fstruct-layout=9 -mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIB -ffast-math -fopenmp  
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining  
-fstruct-layout=9 -mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -Mrecursive  
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang
```

628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIB -ffast-math -fopenmp  
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining  
-fstruct-layout=9 -mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -fscalar-transform  
-fvector-transform -Mrecursive -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5  
-fveclib=AMDLIB -ffast-math -fopenmp -flto -DSPEC_OPENMP  
-fremap-arrays -fstrip-mining -fstruct-layout=9  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100  
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

(Tyrone Camarero MDA200A3N-212)

(2.30 GHz, AMD EPYC 9645)

SPECspeed®2017_fp_base = 445

SPECspeed®2017_fp_peak = 454

CPU2017 License: 6042

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Sep-2025

Hardware Availability: Oct-2024

Software Availability: Aug-2025

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Turin-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Turin-revD.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-09-20 05:38:07-0400.

Report generated on 2025-12-03 14:21:21 by CPU2017 PDF formatter v6716.

Originally published on 2025-12-02.