



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

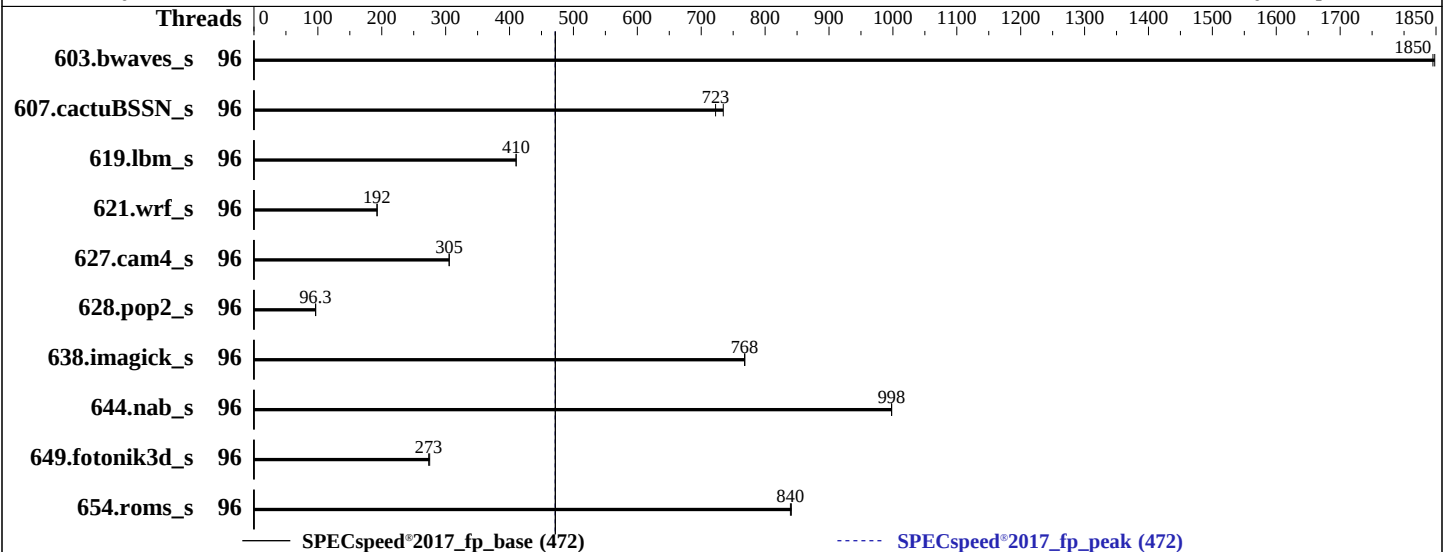
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025



Hardware

CPU Name: AMD EPYC 9455
Max MHz: 4400
Nominal: 3150
Enabled: 96 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 6 cores
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 70 GB on tmpfs
Other: CPU Cooling: DLC

Software

OS: Ubuntu 22.04.5 LTS
5.15.0-136-generic
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: Version 1.0.0 released Mar-2025
File System: tmpfs
System State: Run level 5 (graphical multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	96	31.9	1850	<u>32.0</u>	<u>1850</u>			96	31.9	1850	<u>32.0</u>	<u>1850</u>		
607.cactuBSSN_s	96	<u>23.1</u>	<u>723</u>	22.7	734			96	<u>23.1</u>	<u>723</u>	22.7	734		
619.lbm_s	96	<u>12.8</u>	<u>410</u>	12.8	410			96	<u>12.8</u>	<u>410</u>	12.8	410		
621.wrf_s	96	<u>68.7</u>	<u>192</u>	68.5	193			96	<u>68.7</u>	<u>192</u>	68.5	193		
627.cam4_s	96	29.0	306	<u>29.0</u>	<u>305</u>			96	29.0	306	<u>29.0</u>	<u>305</u>		
628.pop2_s	96	<u>123</u>	<u>96.3</u>	123	96.7			96	<u>123</u>	<u>96.3</u>	123	96.7		
638.imagick_s	96	<u>18.8</u>	<u>768</u>	18.8	768			96	<u>18.8</u>	<u>768</u>	18.8	768		
644.nab_s	96	17.5	998	<u>17.5</u>	<u>998</u>			96	17.5	998	<u>17.5</u>	<u>998</u>		
649.fotonik3d_s	96	33.1	275	<u>33.3</u>	<u>273</u>			96	33.1	275	<u>33.3</u>	<u>273</u>		
654.roms_s	96	<u>18.8</u>	<u>840</u>	18.7	841			96	<u>18.8</u>	<u>840</u>	18.7	841		

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-95"

LD_LIBRARY_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/amd_speed_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/amd_speed_aocc500_znver5_A_lib/lib32:"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"

MALLOC_CONF = "retain:true"

OMP_DYNAMIC = "false"

OMP_SCHEDULE = "static"

OMP_STACKSIZE = "128M"

OMP_THREAD_LIMIT = "96"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

Benchmark run from a 70 GB ramdisk created with the cmd: "mount -t tmpfs -o size=70G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

Logical Processor : Disabled

Virtualization Technology : Disabled

System Profile : Custom

C-States : Disabled

Memory Patrol Scrub : Disabled

PCI ASPM L1 Link Power Management : Disabled

Periodic Directory Rinse Tuning : Blended

Determinism Control : Manual

Determinism Slider : Power Determinism

Algorithm Performance Boost Disable : Enabled

Adaptive Allocation : Enabled

Dram Refresh Delay : Performance

DIMM Self Healing -

on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on 1234567-XE9685L Tue Apr 29 15:01:58 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Platform Notes (Continued)

```
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.15)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS
```

```
1. uname -a
Linux 1234567-XE9685L 5.15.0-136-generic #147-Ubuntu SMP Sat Mar 15 15:53:30 UTC 2025 x86_64 x86_64 x86_64
GNU/Linux
```

```
2. w
15:01:58 up 2:33, 1 user, load average: 0.93, 0.98, 1.00
USER  TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
test  pts/0    100.71.225.4     12:32    2:28m  1.12s  0.01s sshd: test [priv]
```

```
3. Username
From environment variable $USER: test
```

```
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            6189336
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio             0
```

```
5. sysinfo process ancestry
/sbin/init
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: test [priv]
sshd: test@pts/0
-bash
su
bash
/bin/bash /home/DellFiles/bin/DELL_speed.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh speed --define DL-VERS=6.3_T2 --output_format
html,pdf,txt
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2025
Hardware Availability: May-2025
Software Availability: Apr-2025

Platform Notes (Continued)

```
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=1 --define DL-VERS=6.3_T2 --output_format html,pdf,txt fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=1 --define DL-VERS=6.3_T2 --output_format html,pdf,txt --nopower --runmode speed --tune
base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9455 48-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb002147
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 48
siblings        : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
physical id 1: core ids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
physical id 0: apicids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
physical id 1: apicids 128-133,144-149,160-165,176-181,192-197,208-213,224-229,240-245
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:        Little Endian
CPU(s):            96
On-line CPU(s) list: 0-95
Vendor ID:         AuthenticAMD
Model name:        AMD EPYC 9455 48-Core Processor
CPU family:        26
Model:             2
Thread(s) per core: 1
Core(s) per socket: 48
Socket(s):         2
Stepping:          1
Frequency boost:    enabled
CPU max MHz:       4411.2300
CPU min MHz:       1500.0000
BogoMIPS:          6291.58
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1
sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Platform Notes (Continued)

osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd
mba ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1
avx2 smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru
wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic
v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke
avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri movdir64b
overflow_recov succor smca avx512_vp2intersect flush_l1d
AMD-V

Virtualization:
L1d cache: 4.5 MiB (96 instances)
L1i cache: 3 MiB (96 instances)
L2 cache: 96 MiB (96 instances)
L3 cache: 512 MiB (16 instances)

NUMA node(s): 2
NUMA node0 CPU(s): 0-47
NUMA node1 CPU(s): 48-95

Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	1M	96M	16	Unified	2	1024	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0-47
node 0 size: 773354 MB
node 0 free: 768197 MB
node 1 cpus: 48-95
node 1 size: 774089 MB
node 1 free: 773326 MB
node distances:
node 0 1
0: 10 32
1: 32 10

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2025
Hardware Availability: May-2025
Software Availability: Apr-2025

Platform Notes (Continued)

9. /proc/meminfo
MemTotal: 1584582632 kB

10. who -r
run-level 5 Apr 29 12:30

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.15)
Default Target Status
graphical degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync upower
generated apport
indirect uuuid
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-5.15.0-136-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro

15. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 1.50 GHz and 3.15 GHz.
The governor "performance" may decide which speed to use within this range.
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 3150MHz

16. tuned-adm active
Current active profile: latency-performance

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Platform Notes (Continued)

17. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	3
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

18. /sys/kernel/mm/transparent_hugepage

defrag	[always]	defer	defer+madvise	madvise	never
enabled	[always]	madvise	never		
hpage_pmd_size	2097152				
shmem_enabled	always	within_size	advise	[never]	deny force

19. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

20. OS release

From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.5 LTS

21. Disk information

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	70G	3.3G	67G	5%	/mnt/ramdisk

22. /sys/devices/virtual/dmi/id

Vendor:	Dell Inc.
Product:	PowerEdge XE9685L
Product Family:	PowerEdge
Serial:	1234567

23. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x 80AD000080AD HMC94AHBRA485N 64 GB 2 rank 6400

24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.

BIOS Version: 1.0.0

BIOS Date: 03/31/2025

BIOS Revision: 1.0

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.lbm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64

627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64

628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64

638.imagick_s: -DSPEC_LP64

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5

-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto

-fremap-arrays -fstrip-mining -fstruct-layout=7

-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3

-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp

-lamdlibm -lamdalloc -lflang

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Base Optimization Flags (Continued)

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=2000000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Base Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge XE9685L (AMD EPYC 9455 48-Core Processor)

SPECspeed®2017_fp_base = 472

SPECspeed®2017_fp_peak = 472

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Apr-2025

Hardware Availability: May-2025

Software Availability: Apr-2025

Peak Optimization Flags (Continued)

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-04-29 11:01:58-0400.

Report generated on 2025-07-22 10:11:26 by CPU2017 PDF formatter v6716.

Originally published on 2025-07-22.