



SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

SPECrate®2017_fp_base = 967

SPECrate®2017_fp_peak = 1010

PowerEdge R6715 (AMD EPYC 9845 160-Core Processor)

CPU2017 License: 6573

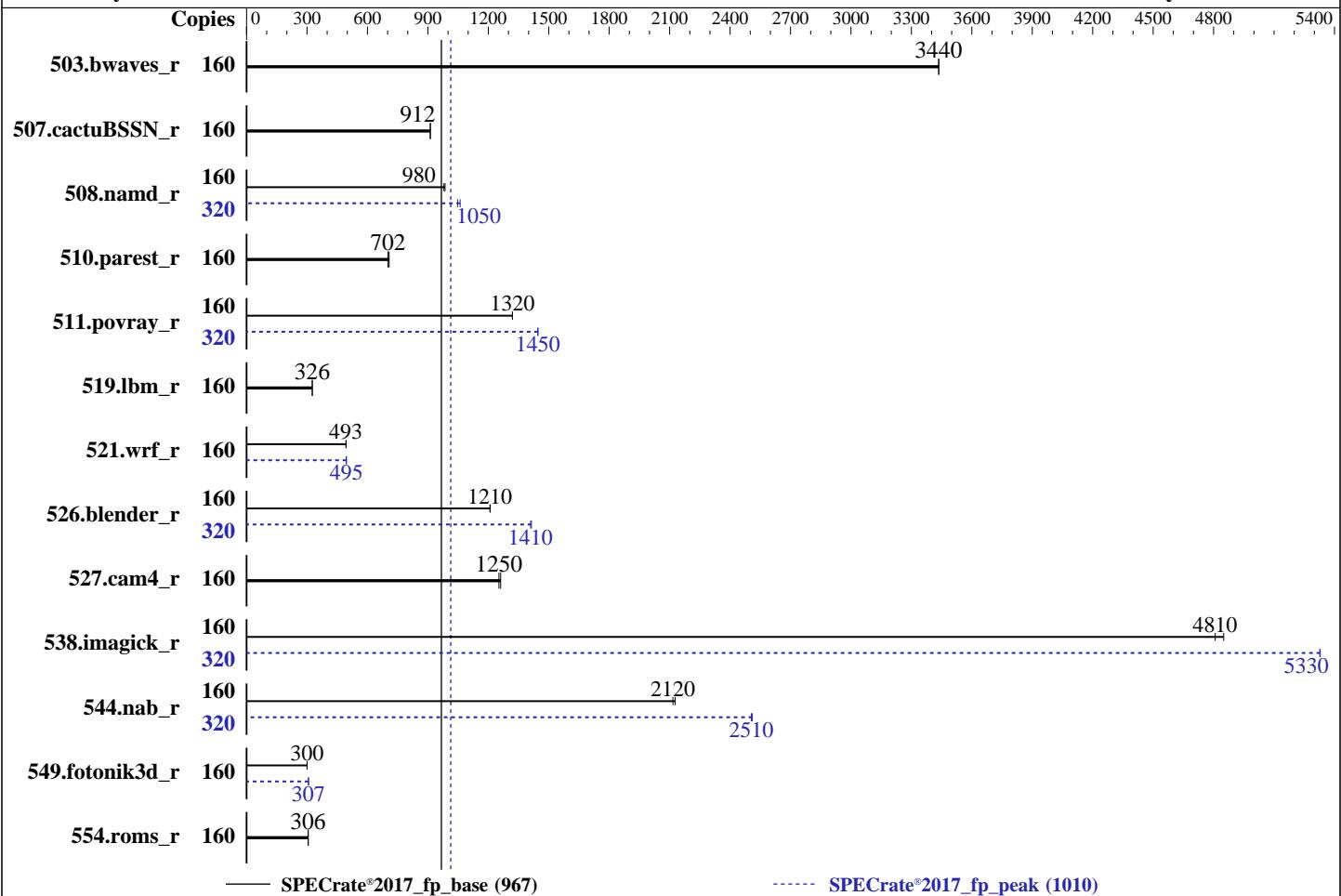
Test Date: Apr-2025

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Mar-2025



Hardware		Software	
CPU Name:	AMD EPYC 9845	OS:	Ubuntu 24.04.2 LTS
Max MHz:	3700	Compiler:	6.8.0-58-generic
Nominal:	2100	Parallel:	C/C++/Fortran: Version 5.0.0 of AOCC
Enabled:	160 cores, 1 chip, 2 threads/core	Firmware:	No
Orderable:	1 chip	File System:	Version 1.1.2 released Feb-2025
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	tmpfs
L2:	1 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	320 MB I+D on chip per chip, 32 MB shared / 16 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)	Power Management:	None
Storage:	160 GB on tmpfs		BIOS set to prefer performance at the cost of additional power usage.
Other:	CPU Cooling: Air		



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	160	467	3440	467	3440			160	467	3440	467	3440		
507.cactusBSSN_r	160	222	912	222	912			160	222	912	222	912		
508.namd_r	160	154	985	155	980			320	290	1050	287	1060		
510.parest_r	160	591	708	596	702			160	591	708	596	702		
511.povray_r	160	283	1320	283	1320			320	517	1450	517	1450		
519.lbm_r	160	517	326	517	326			160	517	326	517	326		
521.wrf_r	160	727	493	725	495			160	723	495	724	495		
526.blender_r	160	202	1210	201	1210			320	346	1410	345	1410		
527.cam4_r	160	222	1260	223	1250			160	222	1260	223	1250		
538.imagick_r	160	82.0	4850	82.8	4810			320	149	5330	149	5330		
544.nab_r	160	127	2130	127	2120			320	215	2510	215	2510		
549.fotonik3d_r	160	2076	300	2074	301			160	2029	307	2028	307		
554.roms_r	160	831	306	832	306			160	831	306	832	306		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017  
    -1.1.9-aocc500-znerv5_A1.3/amd_rate_aocc500_znver5_A_lib/lib32:  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

Benchmark run from a 160 GB ramdisk created with the cmd: "mount -t tmpfs -o size=160G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:

```
Virtualization Technology : Disabled  
NUMA Nodes Per Socket : 4  
  
System Profile : Custom  
C-States : Disabled  
Memory Patrol Scrub : Disabled  
PCI ASPM L1 Link Power Management : Disabled  
Periodic Directory Rinse Tuning : Blended  
Determinism Control : Manual  
Determinism Slider : Power Determinism  
Optimizer Mode : Enabled  
Adaptive Allocation : Enabled  
Dram Refresh Delay : Performance  
DIMM Self Healing -  
on Uncorrectable Memory Error : Disabled
```

```
Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on 1234567-R6715 Wed Apr 30 00:24:06 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo

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Platform Notes (Continued)

```
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.6)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
-----
-----
1. uname -a
Linux 1234567-R6715 6.8.0-58-generic #60-Ubuntu SMP PREEMPT_DYNAMIC Fri Mar 14 18:29:48 UTC 2025 x86_64 x86_64 GNU/Linux
-----
2. w
 00:24:06 up 5:00, 2 users, load average: 159.53, 276.78, 301.30
USER   TTY      FROM          LOGIN@    IDLE   JCPU   PCPU WHAT
root    tty1     -           19:25    4:57m  1.72s  0.55s /bin/bash ./amd_rate_aocc500_znver5_A1.sh
root    tty2     -           19:26    4:57m  0.05s  0.02s -bash
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)        unlimited
data(kbytes)        unlimited
stack(kbytes)       unlimited
coredump(blocks)    0
memory(kbytes)      unlimited
locked memory(kbytes) 2097152
process            3092099
nofiles             1024
vmmemory(kbytes)   unlimited
locks               unlimited
rtprio              0
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -f --
-bash
/bin/bash ./DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.2 --output_format html,pdf,txt
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define DL-BIOS-NPS=2
--define DL-VERS=6.2 --output_format html,pdf,txt fprate
```

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```
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define  
DL-BIOS-NPS=2 --define DL-VERS=6.2 --output_format html,pdf,txt --nopower --runmode rate --tune base:peak  
--size test:train:refrate fprate --nopreenv --note-preenv --logfile  
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3
```

```
-----  
6. /proc/cpuinfo  
model name      : AMD EPYC 9845 160-Core Processor  
vendor_id       : AuthenticAMD  
cpu family     : 26  
model          : 17  
stepping        : 0  
microcode       : 0xb101028  
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass  
TLB size        : 192 4K pages  
cpu cores       : 160  
siblings         : 320  
1 physical ids (chips)  
320 processors (hardware threads)  
physical id 0: core ids 0-159  
physical id 0: apicids 0-319
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

```
From lscpu from util-linux 2.39.3:  
Architecture:           x86_64  
CPU op-mode(s):        32-bit, 64-bit  
Address sizes:         52 bits physical, 57 bits virtual  
Byte Order:            Little Endian  
CPU(s):                320  
On-line CPU(s) list:   0-319  
Vendor ID:             AuthenticAMD  
BIOS Vendor ID:       AMD  
Model name:            AMD EPYC 9845 160-Core Processor  
BIOS Model name:      AMD EPYC 9845 160-Core Processor  
BIOS CPU family:      107  
CPU family:            26  
Model:                 17  
Thread(s) per core:    2  
Core(s) per socket:    160  
Socket(s):             1  
Stepping:               0  
Frequency boost:       enabled  
CPU(s) scaling MHz:   41%  
CPU max MHz:          3718.0659  
CPU min MHz:          1500.0000  
BogoMIPS:              4193.88  
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat  
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb  
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid  
extd_apicid aperfmpfperf rapl pnpi pclmulqdq monitor ssse3 fma cx16 pcid  
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm  
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch  
osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext  
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
```

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Platform Notes (Continued)

ibrs	ibpb	stibp	ibrs_enhanced	vmmcall	fsgsbase	tsc_adjust	bmi1	avx2
smep	bmi2	invpcid	cqm	rdt_a	avx512f	avx512dq	rdseed	adx smap
avx512ifma	clflushopt	clwb	avx512cd	sha_ni	avx512bw	avx512vl	xsaveopt	
xsavec	xgetbv1	xsaves	cqm_llc	cqm_occup_llc	cqm_mbm_total			
cqm_mbm_local	user_shst	avx_vnni	avx512_bf16	clzero	irperf			
xsaveerptr	rdpru	wbnoinvd	amd_ppin	cppc	amd_ibpb_ret	arat	npt	lbrv
svm_lock	nrip_save	tsc_scale	vmcb_clean	flushbyasid	decodeassists			
pausefilter	pfthreshold	avic_v_vmsave_vmlload	vgif_x2avic	v_spec_ctrl				
vnm	avx512vbmi	umip	pku	ospke	avx512_vbmi2	gfni	vaes	vpc1mulqdq
avx512_vnni	avx512_bitalg	avx512_vpopcntdq	la57	rpid	bus_lock_detect			
movdiri	movdir64b	overflow_recov	succor	smca	avx512_vp2intersect			
flush_llid	debug_swap							
L1d cache:								
L1i cache:								
L2 cache:								
L3 cache:								
NUMA node(s):								
NUMA node0 CPU(s):	0-79,160-239							
NUMA node1 CPU(s):	80-159,240-319							
Vulnerability Gather data sampling:	Not affected							
Vulnerability Itlb multihit:	Not affected							
Vulnerability L1tf:	Not affected							
Vulnerability Mds:	Not affected							
Vulnerability Meltdown:	Not affected							
Vulnerability Mmio stale data:	Not affected							
Vulnerability Reg file data sampling:	Not affected							
Vulnerability Retbleed:	Not affected							
Vulnerability Spec rstack overflow:	Not affected							
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl							
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization							
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected							
Vulnerability Srbds:	Not affected							
Vulnerability Tsx async abort:	Not affected							

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	7.5M	12	Data	1	64	1	64
L1i	32K	5M	8	Instruction	1	64	1	64
L2	1M	160M	16	Unified	2	1024	1	64
L3	32M	320M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-79,160-239

node 0 size: 386148 MB

node 0 free: 377871 MB

node 1 cpus: 80-159,240-319

node 1 size: 386950 MB

node 1 free: 380980 MB

node distances:

node 0 1

 0: 10 12

 1: 12 10

9. /proc/meminfo

MemTotal: 791653660 kB

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Platform Notes (Continued)

10. who -r
run-level 3 Apr 29 19:25

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.6)
Default Target Status
multi-user degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured
Legend: LOAD -> Reflects whether the unit definition was properly loaded.
ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.
SUB -> The low-level unit activation state, values depend on unit type.
1 loaded units listed.

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell ipmievd iscsid nftables rsync serial-getty@ ssh
systemd-boot-check-no-failures systemd-confexit systemd-network-generator
systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
generated systemd-time-wait-sync upower
indirect openipmi
masked systemd-sysupdate systemd-sysupdate-reboot uidd
cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-6.8.0-58-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro

15. cpupower frequency-info
analyzing CPU 125:
current policy: frequency should be within 1.50 GHz and 2.10 GHz.
The governor "schedutil" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2100MHz

16. sysctl

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Platform Notes (Continued)

```
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness   20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs    3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs  500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold          500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages    0
vm.swappiness                  1
vm.watermark_boost_factor     15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

-----
17. /sys/kernel/mm/transparent_hugepage
    defrag          [always] defer defer+madvise madvise never
    enabled         [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled   always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                  1
    max_ptes_none          511
    max_ptes_shared        256
    max_ptes_swap          64
    pages_to_scan          4096
    scan_sleep_millisecs   10000

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release Ubuntu 24.04.2 LTS

-----
20. Disk information
    SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.3
    Filesystem      Type  Size  Used Avail Use% Mounted on
    tmpfs          tmpfs  160G  3.3G  157G  3% /mnt/ramdisk

-----
21. /sys/devices/virtual/dmi/id
    Vendor:        Dell Inc.
    Product:       PowerEdge R6715
    Product Family: PowerEdge
    Serial:        1234567

-----
22. dmidecode
    Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
```

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Platform Notes (Continued)

"DMTF SMBIOS" standard.

Memory:

10x 802C0000802C MTC40F2046S1RC64BD2 64 GB 2 rank 6400, configured at 5200
2x 802C0000802C MTC40F2046S1RC64BDY 64 GB 2 rank 6400, configured at 5200

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.
BIOS Version: 1.1.2
BIOS Date: 02/20/2025
BIOS Revision: 1.1

Compiler Version Notes

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====

=====
C++, C, Fortran | 507.cactusBSSN_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
=====

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SPECrate®2017_fp_base = 967

SPECrate®2017_fp_peak = 1010

PowerEdge R6715 (AMD EPYC 9845 160-Core Processor)

CPU2017 License: 6573

Test Date: Apr-2025

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2025

Tested by: Dell Inc.

Software Availability: Mar-2025

Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



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Test Date: Apr-2025

Hardware Availability: Mar-2025

Software Availability: Mar-2025

Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactubSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdaloc
```

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Tested by: Dell Inc.

Software Availability: Mar-2025

Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-lflang -ldl

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

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Tested by: Dell Inc.

SPECrate®2017_fp_base = 967

SPECrate®2017_fp_peak = 1010

Test Date: Apr-2025

Hardware Availability: Mar-2025

Software Availability: Mar-2025

Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags



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Software Availability: Mar-2025

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: basepeak = yes

538.imagick_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

```
510.parest_r: basepeak = yes
```

Fortran benchmarks:

```
503.bwaves_r: basepeak = yes
```

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdaloc -ldl -lflang
```

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Software Availability: Mar-2025

Peak Optimization Flags (Continued)

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fefilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -ldl
```

```
526.blender_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes



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SPECrate®2017_fp_base = 967

SPECrate®2017_fp_peak = 1010

Test Date: Apr-2025

Hardware Availability: Mar-2025

Software Availability: Mar-2025

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

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