



SPEC CPU®2017 Floating Point Rate Result

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Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_fp_base = 927

SPECrate®2017_fp_peak = 756

CPU2017 License: 006802

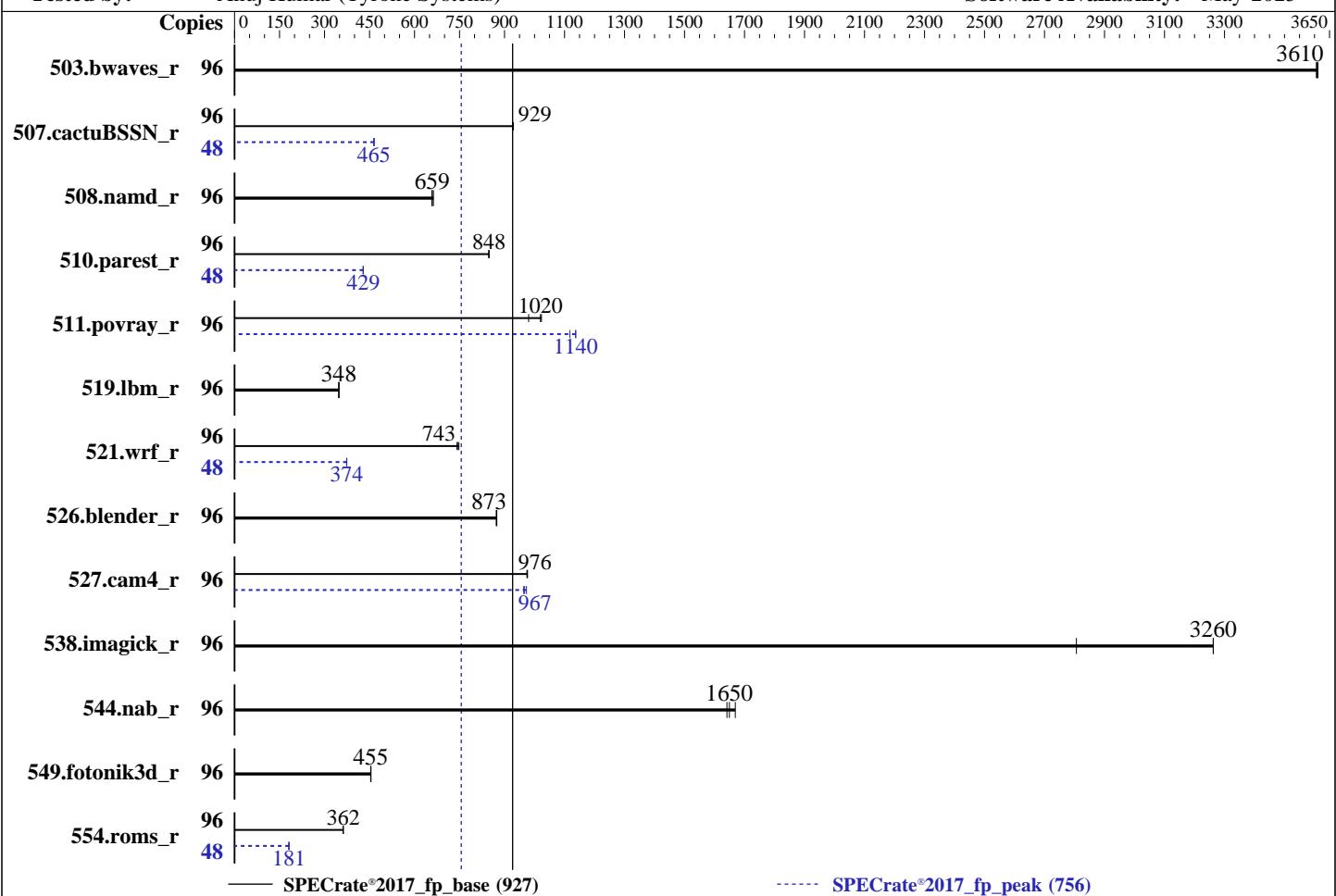
Test Date: May-2025

Test Sponsor: Netweb Technologies India Ltd

Hardware Availability: Jun-2024

Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025



Hardware

CPU Name: Intel Xeon Platinum 8568Y+
Max MHz: 4000
Nominal: 2300
Enabled: 96 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 300 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 960 GB NVMe
Other: CPU Cooling: Air

OS:

Ubuntu 22.04.5 LTS

Compiler:

5.15.0-140-generic

C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;

Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;

No

Firmware: Version 2.4 released Sep-2024

File System: ext4

System State: Run level 3 (multi-user)

Base Pointers: 64-bit

Peak Pointers: 64-bit

Other: jemalloc memory allocator V5.0.1

Parallel:

Power Management: BIOS set to prefer

performance at cost of additional power.

Software

Ubuntu 22.04.5 LTS

5.15.0-140-generic

C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;

Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	267	3610	267	3610	267	3610	96	267	3610	267	3610	267	3610
507.cactusBSSN_r	96	131	929	131	929	131	929	48	131	465	131	465	131	465
508.namd_r	96	138	659	138	663	139	658	96	138	659	138	663	139	658
510.parest_r	96	296	848	296	848	296	848	48	293	429	293	429	293	429
511.povray_r	96	220	1020	219	1020	229	980	96	197	1140	201	1120	197	1140
519.lbm_r	96	291	348	291	348	291	348	96	291	348	291	348	291	348
521.wrf_r	96	288	748	290	742	289	743	48	287	374	288	374	287	374
526.blender_r	96	167	873	167	874	168	873	96	167	873	167	874	168	873
527.cam4_r	96	172	975	172	976	172	976	96	173	973	174	967	174	964
538.imagick_r	96	73.2	3260	85.1	2810	73.2	3260	96	73.2	3260	85.1	2810	73.2	3260
544.nab_r	96	96.8	1670	97.9	1650	98.4	1640	96	96.8	1670	97.9	1650	98.4	1640
549.fotonik3d_r	96	824	454	823	455	823	455	96	824	454	823	455	823	455
554.roms_r	96	421	362	421	362	421	362	48	420	181	421	181	420	181

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
```

```
numactl --interleave=all runcpu <etc>
```

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)



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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading set to Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on benchmark Sun May 18 16:33:04 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux benchmark 5.15.0-140-generic #150-Ubuntu SMP Sat Apr 12 06:00:09 UTC 2025 x86_64 x86_64 x86_64
GNU/Linux

2. w
16:33:04 up 3:52, 3 users, load average: 73.57, 90.00, 92.79
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

(Continued on next page)



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Platform Notes (Continued)

```
intel      tty1      -          12:42    3:50m  0.24s  0.01s -bash
intel      pts/0      -          12:42    3:46m  0.93s  0.23s sudo su
intel      tty2      -          12:49    3:43m  0.01s  0.01s -bash
```

3. Username

```
From environment variable $USER: root
From the command 'logname': intel
```

4. ulimit -a

```
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)        unlimited
stack(kbytes)       unlimited
coredump(blocks)    0
memory(kbytes)      unlimited
locked memory(kbytes) 66002056
process            2062124
nofiles             1024
vmemory(kbytes)     unlimited
locks               unlimited
rtprio              0
```

5. sysinfo process ancestry

```
/sbin/init
/bin/login -p --
-bash
sudo su
sudo su
su
bash
bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 -c
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=48 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=96 --configfile
  ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=48 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
  --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) PLATINUM 8568Y+
vendor_id       : GenuineIntel
cpu family      : 6
model           : 207
stepping         : 2
microcode        : 0x21000291
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrsb bhi
cpu cores        : 48
siblings          : 48
2 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-47
physical id 1: core ids 0-47
```

(Continued on next page)



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Platform Notes (Continued)

```
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72
,74,76,78,80,82,84,86,88,90,92,94
```

```
physical id 1: apicids
128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
80,182,184,186,188,190,192,194,196,198,200,202,204,206,208,210,212,214,216,218,220,222
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	46 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	96
On-line CPU(s) list:	0-95
Vendor ID:	GenuineIntel
Model name:	INTEL(R) XEON(R) PLATINUM 8568Y+
CPU family:	6
Model:	207
Thread(s) per core:	1
Core(s) per socket:	48
Socket(s):	2
Stepping:	2
BogoMIPS:	4600.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nop1 xtTopology nonstop_tsc cpuid aperfmpf perf tsc_known_freq pn1 pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bm1l avx2 smep bm1l erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqmq_llc cqmq_occu_llc cqmq_mbmm_total cqmq_mbmm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts avx512vbmi umip pkv ospte waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdir64b enqcmd fsrm md_clear serialize tsxldtrh pconfig arch_lbr amx_bf16 avx512_fp16 amx_tile amx_int8 flush_ll1d arch_capabilities

Virtualization:

L1d cache: 4.5 MiB (96 instances)

L1i cache: 3 MiB (96 instances)

L2 cache: 192 MiB (96 instances)

L3 cache: 600 MiB (2 instances)

NUMA node(s): 4

NUMA node0 CPU(s): 0-23

NUMA node1 CPU(s): 24-47

NUMA node2 CPU(s): 48-71

NUMA node3 CPU(s): 72-95

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability Lltf: Not affected

(Continued on next page)



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Platform Notes (Continued)

Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	2M	192M	16	Unified	2	2048	1	64
L3	300M	600M	20	Unified	3	245760	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-23
node 0 size: 128641 MB
node 0 free: 113738 MB
node 1 cpus: 24-47
node 1 size: 129017 MB
node 1 free: 118739 MB
node 2 cpus: 48-71
node 2 size: 128970 MB
node 2 free: 118491 MB
node 3 cpus: 72-95
node 3 size: 129010 MB
node 3 free: 118233 MB
node distances:
node 0 1 2 3
 0: 10 12 21 21
 1: 12 10 21 21
 2: 21 21 10 12
 3: 21 21 12 10
```

9. /proc/meminfo

```
MemTotal: 528016472 kB
```

10. who -r
run-level 3 May 18 12:42

11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.12)
Default Target Status
multi-user degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* fwupd-refresh.service loaded failed failed Refresh fwupd metadata and update motd

(Continued on next page)



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Platform Notes (Continued)

* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor atop atopacct binfmt-support blk-availability cloud-config
cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd
getty@ gpu-manager grub-common grub-initrd-fallback irqbalance keyboard-setup lvm2-monitor
lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog
secureboot-db setvtrgb snapd ssh systemd-networkd systemd-networkd-wait-online
systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage
udisks2 ufw unattended-upgrades vgaauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-rootsystemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@
systemd-boot-check-no-failures systemd-network-generator systemd-sysext
systemd-time-wait-sync upower
generated apport
indirect uidd
masked cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-5.15.0-140-generic
root=UUID=d23f4c96-e4ba-4003-a8ac-11587c2fe77c
ro

15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 60
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

16. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvise [madvise] never
enabled always [madvise] never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1

(Continued on next page)



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Platform Notes (Continued)

```
max_ptes_none      511
max_ptes_shared    256
max_ptes_swap      64
pages_to_scan      4096
scan_sleep_millisecs 10000
```

```
-----  
18. OS release  
From /etc/*-release /etc/*-version  
os-release Ubuntu 22.04.5 LTS
```

```
-----  
19. Disk information  
SPEC is set to: /home/cpu2017  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/nvme0n1p5  ext4  701G  115G  551G  18%  /home
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor:          Tyrone Systems  
Product:         SDI200A3N-212  
Product Family: Family  
Serial:          A495115X4412722
```

```
-----  
21. dmidecode  
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.  
Memory:  
16x NO DIMM NO DIMM  
16x Samsung M321R4GA0BB0-CQKET 32 GB 1 rank 4800
```

```
-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor:      American Megatrends International, LLC.  
BIOS Version:     2.4  
BIOS Date:        09/23/2024  
BIOS Revision:    5.32
```

Compiler Version Notes

```
=====| 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

```
=====| 508.namd_r(base, peak) 510.parest_r(base, peak)
```

```
-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

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Compiler Version Notes (Continued)

```
=====
C++, C      | 511.povray_r(base, peak) 526.blender_r(base, peak)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
C++, C, Fortran | 507.cactusBSSN_r(base, peak)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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-----

=====
Fortran      | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
Fortran, C    | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

(Continued on next page)



SPEC CPU®2017 Floating Point Rate Result

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Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)

Tyrone Camarero SDI200A3N-212
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECrate®2017_fp_base = 927

SPECrate®2017_fp_peak = 756

CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactusBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -futo -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -futo -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags



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CPU2017 License: 006802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Anuj Kumar (Tyrone Systems)

Test Date: May-2025

Hardware Availability: Jun-2024

Software Availability: May-2025

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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Tested by: Anuj Kumar (Tyrone Systems)

Software Availability: May-2025

Peak Optimization Flags (Continued)

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -fsto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-EMR-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-EMR-revA.xml>

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