



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

xFusion

FusionServer 2158H V8
(AMD EPYC 9655)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1120

CPU2017 License: 6488

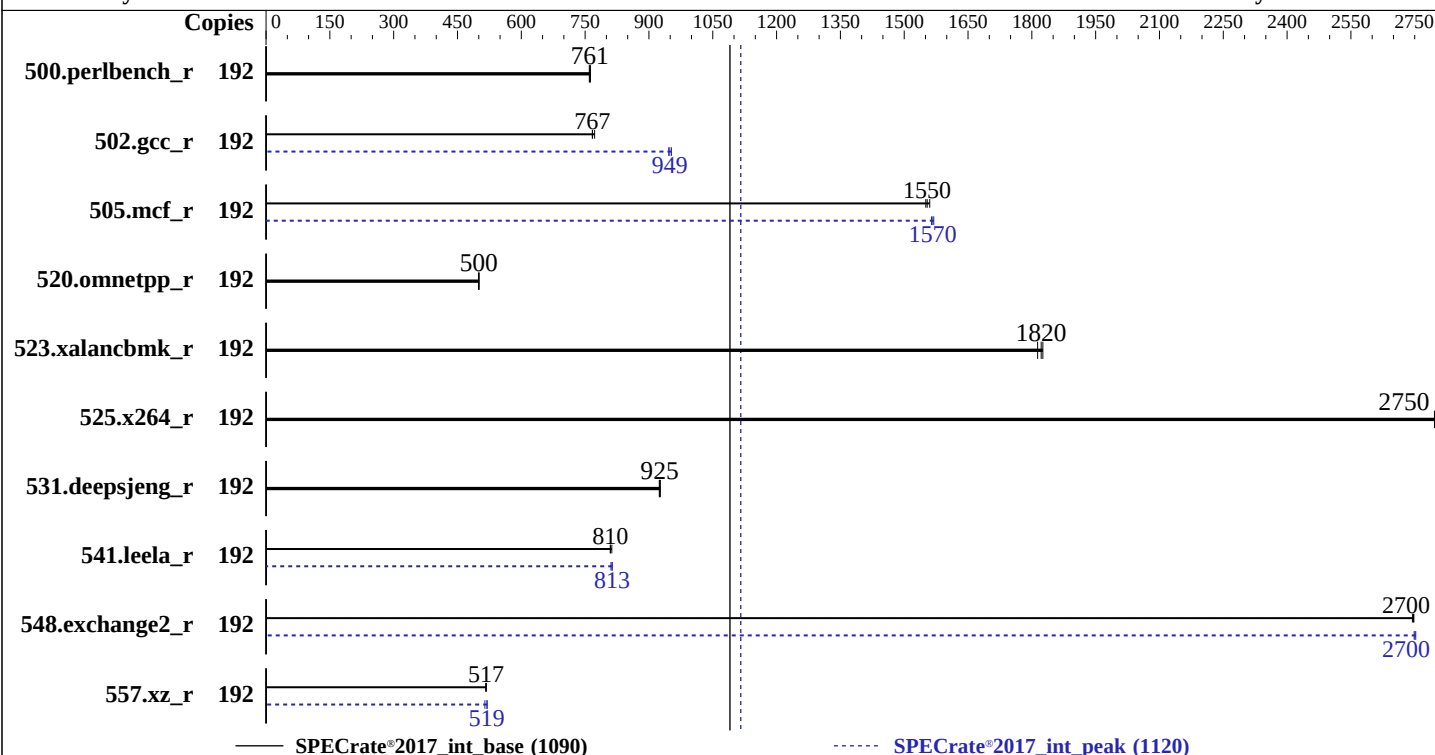
Test Sponsor: xFusion

Tested by: xFusion

Test Date: May-2025

Hardware Availability: Oct-2024

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9655
Max MHz: 4500
Nominal: 2600
Enabled: 96 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 384 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R)
Storage: 1 x 1.6 TB PCIe NVMe SSD
Other: CPU Cooling: Air

Software

OS: Ubuntu 24.04 LTS
6.8.0-38-generic
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: Version 00.13.01.05 released Feb-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	192	402	761	401	762	402	760	192	402	761	401	762	402	760
502.gcc_r	192	354	767	355	767	352	772	192	287	946	286	949	285	953
505.mcf_r	192	200	1550	200	1550	199	1560	192	198	1560	198	1570	198	1570
520.omnetpp_r	192	503	500	503	501	505	499	192	503	500	503	501	505	499
523.xalancbmk_r	192	111	1820	111	1830	112	1810	192	111	1820	111	1830	112	1810
525.x264_r	192	122	2750	122	2750	122	2750	192	122	2750	122	2750	122	2750
531.deepsjeng_r	192	237	927	238	925	238	924	192	237	927	238	925	238	924
541.leela_r	192	391	813	393	809	393	810	192	392	811	391	813	391	814
548.exchange2_r	192	187	2690	186	2700	186	2700	192	186	2700	186	2700	186	2700
557.xz_r	192	400	518	401	517	402	516	192	403	514	399	519	399	520

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit
The kernel stops sending timer ticks to CPUs by using "nohz_full=1-383"
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"

MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

Determinism Control = Manual

Determinism Enable = Power

TDP Control = Manual

TDP = 500

PPT Control = Manual

PPT = 500

NUMA Nodes Per Socket = Auto

ACPI SRAT L3 Cache As NUMA Domain = Enable

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on 2158hv8 Mon May 5 18:35:49 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.1)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release

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Platform Notes (Continued)

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

1. uname -a

Linux 2158hv8 6.8.0-38-generic #38-Ubuntu SMP PREEMPT_DYNAMIC Fri Jun 7 15:25:01 UTC 2024 x86_64 x86_64
x86_64 GNU/Linux

2. w

18:35:49 up 5 min, 1 user, load average: 0.09, 1.27, 0.83

USER	TTY	FROM	LOGIN@	IDLE	JCPU	PCPU	WHAT
root	tty1	-	18:35	13.00s	0.84s	0.01s	tail -100f nohup.out

3. Username

From environment variable \$USER: root

4. ulimit -a

time(seconds)	unlimited
file(blocks)	unlimited
data(kbytes)	unlimited
stack(kbytes)	unlimited
coredump(blocks)	0
memory(kbytes)	unlimited
locked memory(kbytes)	2097152
process	3093750
nofiles	1024
vmemory(kbytes)	unlimited
locks	unlimited
rtprio	0

5. sysinfo process ancestry

/sbin/init

/bin/login -p --

-bash

python3 ./run_amd_rate_aocc500_znver5_A1.py

/bin/bash ./amd_rate_aocc500_znver5_A1.sh

runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intrate

runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower

--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile

\$SPEC/tmp/CPU2017.020/templogs/preenv.intrate.020.0.log --lognum 020.0 --from_runcpu 2

specperl \$SPEC/bin/sysinfo

\$SPEC = /home/cpu2017

6. /proc/cpuinfo

model name	: AMD EPYC 9655 96-Core Processor
vendor_id	: AuthenticAMD
cpu family	: 26
model	: 2
stepping	: 1
microcode	: 0xb00211e
bugs	: sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size	: 192 4K pages

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Platform Notes (Continued)

cpu cores : 96
siblings : 192
1 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-95
physical id 0: apicids 0-191

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	192
On-line CPU(s) list:	0-191
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	Advanced Micro Devices, Inc.
Model name:	AMD EPYC 9655 96-Core Processor
BIOS Model name:	AMD EPYC 9655 96-Core Processor Unknown CPU @ 2.6GHz
BIOS CPU family:	107
CPU family:	26
Model:	2
Thread(s) per core:	2
Core(s) per socket:	96
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	58%
CPU max MHz:	4509.3750
CPU min MHz:	1500.0000
BogoMIPS:	5191.65
Flags:	fpu_vme_de_pse_tsc_msr_pae_mce_cx8_apic_sep_mtrr_pge_mca_cmov_pat pse36_clflush_mmx_fxsr_sse_sse2_ht_syscall_nx_mmxext_fxsr_opt_pdpe1gb rdtscp_lm_constant_tsc_rep_good_amd_lbr_v2_nopl_nonstop_tsc_cpuid extd_apicid_aperfmpperf_rapl_pni_pclmulqdq_monitor_ssse3_fma_cx16_pcid sse4_1_sse4_2_x2apic_movbe_popcnt_aes_xsave_avx_f16c_rdrand_lahf_lm cmp_legacy_extapic_cr8_legacy_abm_sse4a_misalignsse_3dnowprefetch osvw_ibs_skinit_wdt_tce_topoext_perfctr_core_perfctr_nb_bpext perfctr_llc_mwaitx_cpb_cat_l3_cdp_l3_hw_pstate_ssbd_mba_perfmon_v2 ibrs_ibpb_stibp_ibrs_enhanced_vmmcall_fsgsbase_tsc_adjust_bmi1_avx2 smep_bmi2_erms_invpcid_cqm_rdt_a_avx512f_avx512dq_rdseed_adx_smap avx512ifma_clflushopt_clwb_avx512cd_sha_ni_avx512bw_avx512vl_xsaveopt xsavec_xgetbv1_xsaves_cqm_llc_cqm_occup_llc_cqm_mbm_total cqm_mbm_local_user_shstk_avx_vnni_avx512_bf16_clzero_irperf xsaveerptr_rdprr_wbnoinvd_amd_ppin_pppc_arat_npt_lbrv_svm_lock nrip_save_tsc_scale_vmc_b_clean_flushbyasid_decodeassists_pausefilter pfthreshold_avic_v_vmsave_vmload_vgif_x2avic_v_spec_ctrl_vnmi avx512vbmi_umip_pku_ospke_avx512_vbmi2_gfni_vaes_vpclmulqdq avx512_vnni_avx512_bitalg_avx512_vpopcnddq_la57_rdpid_bus_lock_detect movdiri_movdir64b_overflow_recov_succor_smca_fsrn_avx512_vp2intersect flush_l1d_debug_swap
L1d cache:	4.5 MiB (96 instances)
L1i cache:	3 MiB (96 instances)
L2 cache:	96 MiB (96 instances)
L3 cache:	384 MiB (12 instances)
NUMA node(s):	12

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Platform Notes (Continued)

NUMA node0 CPU(s): 0-7,96-103
NUMA node1 CPU(s): 8-15,104-111
NUMA node2 CPU(s): 16-23,112-119
NUMA node3 CPU(s): 24-31,120-127
NUMA node4 CPU(s): 32-39,128-135
NUMA node5 CPU(s): 40-47,136-143
NUMA node6 CPU(s): 48-55,144-151
NUMA node7 CPU(s): 56-63,152-159
NUMA node8 CPU(s): 64-71,160-167
NUMA node9 CPU(s): 72-79,168-175
NUMA node10 CPU(s): 80-87,176-183
NUMA node11 CPU(s): 88-95,184-191
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	1M	96M	16	Unified	2	1024	1	64
L3	32M	384M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 12 nodes (0-11)
node 0 cpus: 0-7,96-103
node 0 size: 63999 MB
node 0 free: 63684 MB
node 1 cpus: 8-15,104-111
node 1 size: 64506 MB
node 1 free: 64300 MB
node 2 cpus: 16-23,112-119
node 2 size: 64506 MB
node 2 free: 64235 MB
node 3 cpus: 24-31,120-127
node 3 size: 64506 MB
node 3 free: 64300 MB
node 4 cpus: 32-39,128-135
node 4 size: 64506 MB
node 4 free: 64306 MB
node 5 cpus: 40-47,136-143
node 5 size: 64506 MB
node 5 free: 64315 MB
node 6 cpus: 48-55,144-151
node 6 size: 64506 MB
node 6 free: 64293 MB

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Platform Notes (Continued)

```
node 7 cpus: 56-63,152-159
node 7 size: 64506 MB
node 7 free: 64142 MB
node 8 cpus: 64-71,160-167
node 8 size: 64506 MB
node 8 free: 64290 MB
node 9 cpus: 72-79,168-175
node 9 size: 64506 MB
node 9 free: 64272 MB
node 10 cpus: 80-87,176-183
node 10 size: 64506 MB
node 10 free: 64276 MB
node 11 cpus: 88-95,184-191
node 11 size: 64444 MB
node 11 free: 64193 MB
node distances:
node 0 1 2 3 4 5 6 7 8 9 10 11
0: 10 11 11 12 12 12 12 12 12 12 12 12
1: 11 10 11 12 12 12 12 12 12 12 12 12
2: 11 11 10 12 12 12 12 12 12 12 12 12
3: 12 12 12 10 11 11 12 12 12 12 12 12
4: 12 12 12 11 10 11 12 12 12 12 12 12
5: 12 12 12 11 11 10 12 12 12 12 12 12
6: 12 12 12 12 12 12 10 11 11 12 12 12
7: 12 12 12 12 12 12 12 11 10 11 12 12
8: 12 12 12 12 12 12 11 11 10 12 12 12
9: 12 12 12 12 12 12 12 12 12 10 11 11
10: 12 12 12 12 12 12 12 12 12 11 10 11
11: 12 12 12 12 12 12 12 12 12 11 11 10
```

9. /proc/meminfo
MemTotal: 792072780 kB

10. who -r
run-level 5 May 5 18:32

11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.1)
Default Target Status
graphical degraded

12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* systemd-networkd-wait-online.service loaded failed failed Wait for Network to be Configured
Legend: LOAD -> Reflects whether the unit definition was properly loaded.
ACTIVE -> The high-level unit activation state, i.e. generalization of SUB.
SUB -> The low-level unit activation state, values depend on unit type.
1 loaded units listed.

13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online apparmor
apport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup
cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback
keyboard-setup lvm2-monitor multipathd networkd-dispatcher networking open-iscsi
open-vm-tools pollinate rsyslog secureboot-db setvtrgb snapd sysstat systemd-networkd

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Platform Notes (Continued)

enabled-runtime disabled systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw unattended-upgrades vgauth wpa_supplicant netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs console-getty debug-shell ifupdown-wait-online iscsid nftables rsync serial-getty@ ssh systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysexit systemd-time-wait-sync upower wpa_supplicant-nl80211@ wpa_supplicant-wired@ wpa_supplicant@ indirect systemd-sysupdate systemd-sysupdate-reboot uuidd masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-6.8.0-38-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro
nohz_full=1-383

15. cpupower frequency-info
analyzing CPU 20:
current policy: frequency should be within 1.50 GHz and 2.60 GHz.
The governor "performance" may decide which speed to use within this range.
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 800MHz

16. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1

17. /sys/kernel/mm/transparent_hugepage
defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152

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Platform Notes (Continued)

shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
```

19. OS release

```
From /etc/*-release /etc/*-version
os-release Ubuntu 24.04 LTS
```

20. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/ubuntu--vg-ubuntu--lv	ext4	196G	17G	170G	9%	/

21. /sys/devices/virtual/dmi/id

```
Product: 2158H V8
Product Family: Turin
```

22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x SK Hynix HMC94AHBRA485N 64 GB 2 rank 6400

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 00.13.01.05
BIOS Date: 02/10/2025
BIOS Revision: 1.5
```

Compiler Version Notes

C | 502.gcc_r(peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)

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Compiler Version Notes (Continued)

| 557.xz_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
C | 502.gcc_r(peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 548.exchange2_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

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xFusion

FusionServer 2158H V8
(AMD EPYC 9655)

SPECrate®2017_int_base = 1090

SPECrate®2017_int_peak = 1120

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: May-2025
Hardware Availability: Oct-2024
Software Availability: Oct-2024

Base Compiler Invocation (Continued)

Fortran benchmarks:
flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

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Hardware Availability: Oct-2024

Software Availability: Oct-2024

Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-Wl, -mllvm -Wl, -reduce-array-computations=3
-Wl, -mllvm -Wl, -inline-recursion=4 -Wl, -mllvm -Wl, -lsr-in-nested-loop
-Wl, -mllvm -Wl, -enable-iv-split -z muldefs -O3 -march=znver5
-fvecLib=AMDLIBM -ffast-math -fltO
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdalloc -ldl

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64

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Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: basepeak = yes

502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc

505.mcf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

525.x264_r: basepeak = yes

557.xz_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

C++ benchmarks:

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Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -fno-PIE
-no-pie -fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang
-lamdalloc-ext -ldl

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -flto -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc -ldl

Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v119/aocc5/1316/amd_rate_aocc500_znver5_A_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.5.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.5.xml>

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