



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(3.30 GHz, AMD EPYC 9575F)

SPECrate®2017_fp_base = 1730

SPECrate®2017_fp_peak = 1750

CPU2017 License: 9017

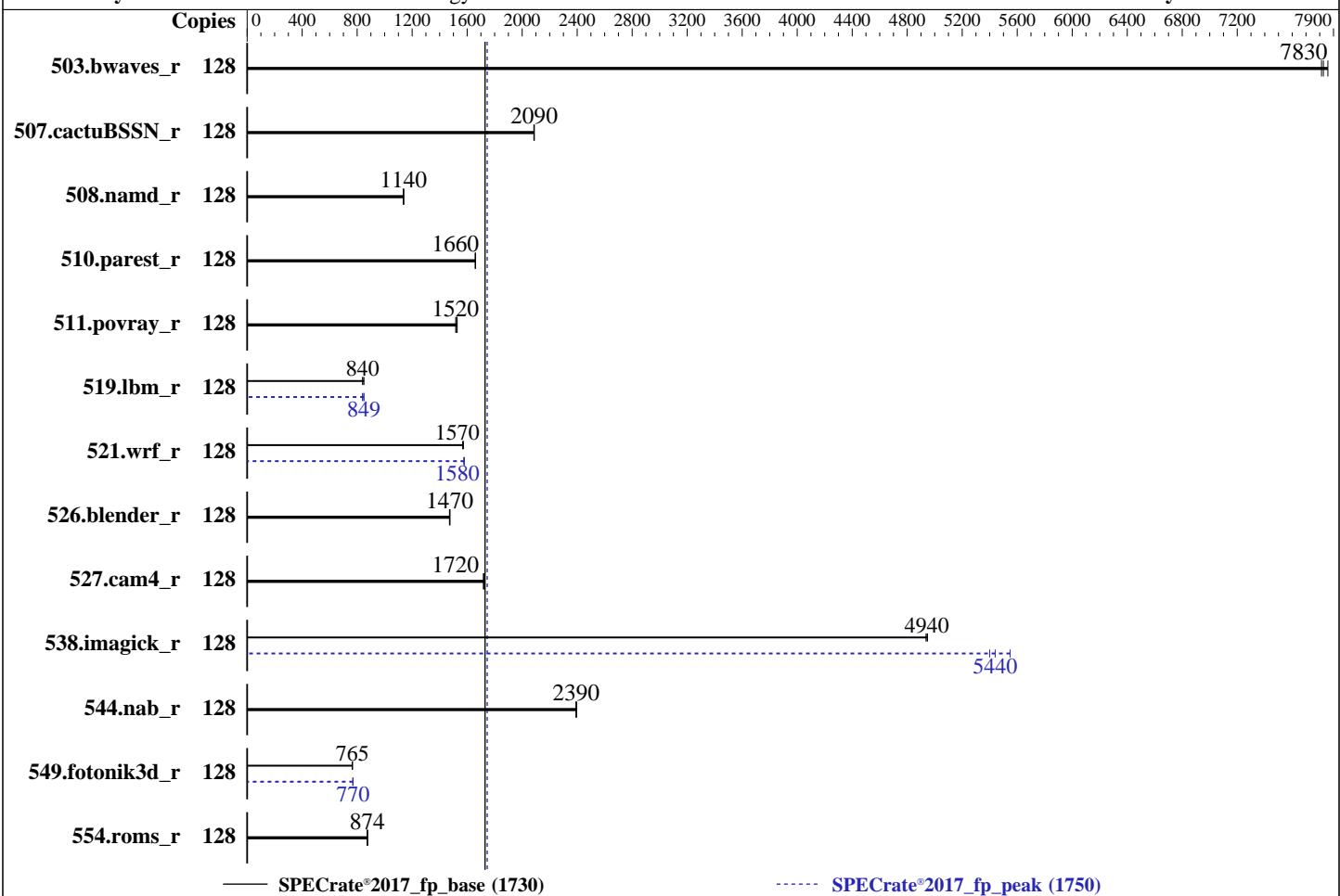
Test Date: May-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9575F
Max MHz: 5000
Nominal: 3300
Enabled: 128 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip,
32 MB shared / 8 cores
Other: None
Memory: 768 GB (24 x 32 GB 2Rx8 PC5-6400B-R)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: CLC

Software

OS: SUSE Linux Enterprise Server 15 SP6
Compiler: Kernel 6.4.0-150600.21-default
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
Firmware: No
File System: Lenovo BIOS Version KAE137G 5.60 released Apr-2025
System State: xfs
Base Pointers: Run level 3 (multi-user)
Peak Pointers: 64-bit
Other: 64-bit
Power Management: None
BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|-----------------|--------|---------|-------|-------------|-------------|-------------|-------------|--------|---------|-------|------------|-------------|-------------|-------------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 128 | 163 | 7860 | 164 | 7830 | 164 | 7810 | 128 | 163 | 7860 | 164 | 7830 | 164 | 7810 | | |
| 507.cactubSSN_r | 128 | 77.7 | 2090 | 77.6 | 2090 | 77.6 | 2090 | 128 | 77.7 | 2090 | 77.6 | 2090 | 77.6 | 2090 | | |
| 508.namd_r | 128 | 107 | 1140 | 107 | 1140 | 107 | 1140 | 128 | 107 | 1140 | 107 | 1140 | 107 | 1140 | | |
| 510.parest_r | 128 | 202 | 1660 | 202 | 1660 | 202 | 1660 | 128 | 202 | 1660 | 202 | 1660 | 202 | 1660 | | |
| 511.povray_r | 128 | 196 | 1530 | 197 | 1520 | 196 | 1520 | 128 | 196 | 1530 | 197 | 1520 | 196 | 1520 | | |
| 519.lbm_r | 128 | 159 | 850 | 161 | 840 | 161 | 840 | 128 | 159 | 849 | 159 | 849 | 161 | 838 | | |
| 521.wrf_r | 128 | 182 | 1570 | 183 | 1570 | 183 | 1570 | 128 | 182 | 1580 | 182 | 1580 | 182 | 1580 | | |
| 526.blender_r | 128 | 133 | 1470 | 132 | 1470 | 132 | 1470 | 128 | 133 | 1470 | 132 | 1470 | 132 | 1470 | | |
| 527.cam4_r | 128 | 130 | 1720 | 130 | 1720 | 130 | 1720 | 128 | 130 | 1720 | 130 | 1720 | 130 | 1720 | | |
| 538.imagick_r | 128 | 64.3 | 4950 | 64.4 | 4940 | 64.5 | 4940 | 128 | 57.4 | 5550 | 59.0 | 5400 | 58.5 | 5440 | | |
| 544.nab_r | 128 | 89.9 | 2400 | 90.1 | 2390 | 90.0 | 2390 | 128 | 89.9 | 2400 | 90.1 | 2390 | 90.0 | 2390 | | |
| 549.fotonik3d_r | 128 | 652 | 766 | 652 | 765 | 652 | 765 | 128 | 649 | 768 | 648 | 770 | 648 | 770 | | |
| 554.roms_r | 128 | 233 | 874 | 233 | 874 | 232 | 876 | 128 | 233 | 874 | 233 | 874 | 232 | 876 | | |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

cpupower set to performance mode

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Operating System Notes (Continued)

```
cpupower frequency-set -r -g performance
To enable Transparent Hugepages (THP) for all allocations:
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd
    -aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

P-State set to Enabled

NUMA Nodes per Socket set to NPS4

L1 Stride Prefetcher set to Disabled

SMT Mode set to Disabled

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sat May 17 15:03:54 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files

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Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
15:03:54 up 4 min, 1 user, load average: 0.14, 0.30, 0.15
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 3094094
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 3094094
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECcpu_1.02.sh
/bin/bash ./Run026-compliant-amd-ratefp.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.543/templogs/preenv.fprate.543.0.log --lognum 543.0 --from_runcpu 2

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Platform Notes (Continued)

```
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2

-----
6. /proc/cpuinfo
    model name      : AMD EPYC 9575F 64-Core Processor
    vendor_id       : AuthenticAMD
    cpu family     : 26
    model          : 2
    stepping        : 1
    microcode       : 0xb002147
    bugs            : sysret_ss_atrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 192 4K pages
    cpu cores       : 64
    siblings         : 64
    2 physical ids (chips)
    128 processors (hardware threads)
    physical id 0: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
    physical id 1: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
    physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119
    physical id 1: apicids 128-135,144-151,160-167,176-183,192-199,208-215,224-231,240-247
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----
7. lscpu
```

From lscpu from util-linux 2.39.3:

| | |
|----------------------|---|
| Architecture: | x86_64 |
| CPU op-mode(s): | 32-bit, 64-bit |
| Address sizes: | 52 bits physical, 57 bits virtual |
| Byte Order: | Little Endian |
| CPU(s): | 128 |
| On-line CPU(s) list: | 0-127 |
| Vendor ID: | AuthenticAMD |
| BIOS Vendor ID: | Advanced Micro Devices, Inc. |
| Model name: | AMD EPYC 9575F 64-Core Processor |
| BIOS Model name: | AMD EPYC 9575F 64-Core Processor |
| BIOS CPU family: | Unknown CPU @ 3.3GHz |
| CPU family: | 107 |
| Model: | 26 |
| Thread(s) per core: | 2 |
| Core(s) per socket: | 64 |
| Socket(s): | 2 |
| Stepping: | 1 |
| Frequency boost: | enabled |
| CPU(s) scaling MHz: | 102% |
| CPU max MHz: | 3300.0000 |
| CPU min MHz: | 1500.0000 |
| BogoMIPS: | 6589.95 |
| Flags: | fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap |

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Platform Notes (Continued)

```
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total
cqmq_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaverptr rdpru wbnoinvd amd_ppin cppo arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmlload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
```

Virtualization:

AMD-V

L1d cache:

6 MiB (128 instances)

L1i cache:

4 MiB (128 instances)

L2 cache:

128 MiB (128 instances)

L3 cache:

512 MiB (16 instances)

NUMA node(s):

8

NUMA node0 CPU(s):

0-15

NUMA node1 CPU(s):

16-31

NUMA node2 CPU(s):

32-47

NUMA node3 CPU(s):

48-63

NUMA node4 CPU(s):

64-79

NUMA node5 CPU(s):

80-95

NUMA node6 CPU(s):

96-111

NUMA node7 CPU(s):

112-127

Vulnerability Gather data sampling:

Not affected

Vulnerability Itlb multihit:

Not affected

Vulnerability Llft:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Reg file data sampling:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec rstack overflow:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE | LEVEL | SETS | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d | 48K | 6M | 12 | Data | 1 | 64 | 1 | 64 |
| L1i | 32K | 4M | 8 | Instruction | 1 | 64 | 1 | 64 |
| L2 | 1M | 128M | 16 | Unified | 2 | 1024 | 1 | 64 |
| L3 | 32M | 512M | 16 | Unified | 3 | 32768 | 1 | 64 |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-15

node 0 size: 96341 MB

node 0 free: 95986 MB

node 1 cpus: 16-31

node 1 size: 96763 MB

node 1 free: 96343 MB

node 2 cpus: 32-47

node 2 size: 96763 MB

node 2 free: 96218 MB

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Platform Notes (Continued)

```
node 3 cpus: 48-63
node 3 size: 96763 MB
node 3 free: 96419 MB
node 4 cpus: 64-79
node 4 size: 96763 MB
node 4 free: 96442 MB
node 5 cpus: 80-95
node 5 size: 96763 MB
node 5 free: 96422 MB
node 6 cpus: 96-111
node 6 size: 96763 MB
node 6 free: 96512 MB
node 7 cpus: 112-127
node 7 size: 96626 MB
node 7 free: 96310 MB
node distances:
node  0   1   2   3   4   5   6   7
  0: 10  12  12  12  32  32  32  32
  1: 12  10  12  12  32  32  32  32
  2: 12  12  10  12  32  32  32  32
  3: 12  12  12  10  32  32  32  32
  4: 32  32  32  32  10  12  12  12
  5: 32  32  32  32  12  10  12  12
  6: 32  32  32  32  12  12  10  12
  7: 32  32  32  32  12  12  12  10
```

9. /proc/meminfo

```
MemTotal: 792115184 kB
```

10. who -r

```
run-level 3 May 17 15:00
```

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```
Default Target      Status
multi-user          running
```

12. Services, from systemctl list-unit-files

| STATE | UNIT FILES |
|-----------------|---|
| enabled | YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator kbdsettlements klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog sapconf smartd sshd sysctl-logger systemd-pstore tuned wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny |
| enabled-runtime | systemd-remount-fs |
| disabled | autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewallld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd sysstat systemd-boot-check-no-failures systemd-confexit systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd |
| generated | ntp_sync |
| indirect | systemd-userdbd uuidd wickedd |

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
```

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Platform Notes (Continued)

```
root=UUID=36ce2ea9-ca92-4f5a-b22a-9bea62fad028
splash=silent
mitigations=auto
quiet
security=apparmor

-----
14. cpupower frequency-info
analyzing CPU 58:
    current policy: frequency should be within 1.50 GHz and 3.30 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
        Supported: yes
        Active: yes

-----
15. tuned-adm active
Current active profile: virtual-host

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       5
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

-----
17. /sys/kernel/mm/transparent_hugepage
defrag           [always] defer defer+madvise madvise never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag              1
max_ptes_none      511
max_ptes_shared    256
max_ptes_swap      64
pages_to_scan      4096
scan_sleep_millisecs 10000
```

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Platform Notes (Continued)

19. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

20. Disk information

```
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   442G  88G  354G  20%  /
```

21. /sys/devices/virtual/dmi/id

```
Vendor:          Lenovo
Product:         ThinkSystem SR645 V3
Product Family: ThinkSystem
Serial:          1234567890
```

22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
1x SK Hynix HMCG88AHBRA284N 32 GB 2 rank 6400
16x SK Hynix HMCG88AHBRA290N 32 GB 2 rank 6400
3x SK Hynix HMCG88AHBRA292N 32 GB 2 rank 6400
3x SK Hynix HMCG88AHBRA471N 32 GB 2 rank 6400
1x SK Hynix HMCG88AHBRA478N 32 GB 2 rank 6400
```

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

```
BIOS Vendor:      Lenovo
BIOS Version:     KAE137G-5.60
BIOS Date:        04/25/2025
BIOS Revision:    5.60
Firmware Revision: 55.40
```

Compiler Version Notes

```
=====
C           | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====
```

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====
```

```
=====
C++          | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====
```

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
=====
```

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Hardware Availability: Jul-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Compiler Version Notes (Continued)

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
C++, C, Fortran | 507.cactusBSSN_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

(Continued on next page)



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Hardware Availability: Jul-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Base Compiler Invocation (Continued)

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining

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Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc  
-lflang -ldl
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdaloc  
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdaloc  
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdaloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdaloc -lflang
```

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Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

-ldl

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

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Peak Compiler Invocation (Continued)

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fsto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdaloc -ldl
```

538.imagick_r: Same as 519.lbm_r

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: basepeak = yes

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Peak Optimization Flags (Continued)

Fortran benchmarks:

503.bwaves_r: basepeak = yes

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

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Peak Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-F.html>
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-F.xml>
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2025-05-17 03:03:54-0400.

Report generated on 2025-06-03 15:44:55 by CPU2017 PDF formatter v6716.

Originally published on 2025-06-03.