# SPEC CPU®2017 Integer Rate Result



## Esconet Technologies Ltd.

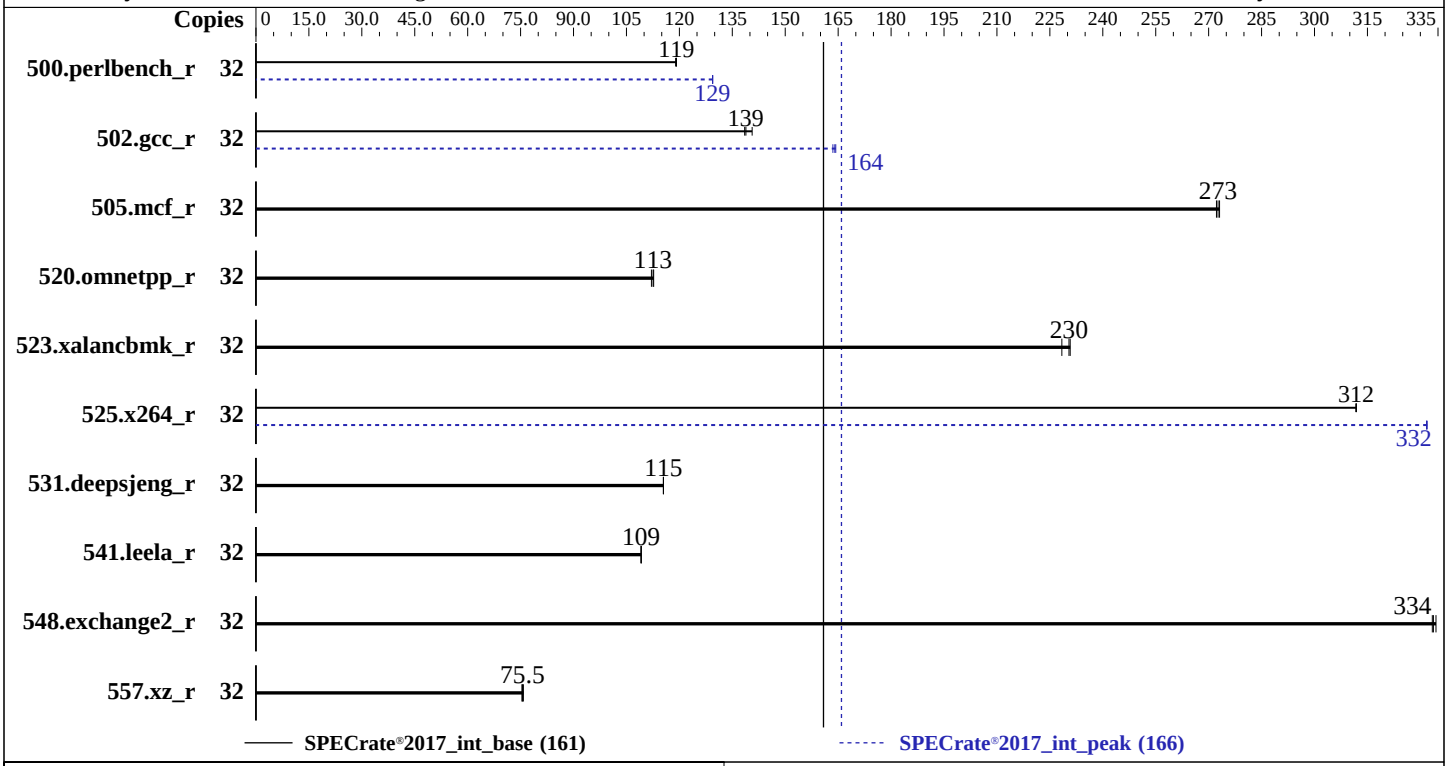
Hexadata HD-RS4100 Ver: SPR-001
(Intel Xeon Gold 6426Y, 2.50 GHz)

SPECrate®2017_int_base = 161

SPECrate®2017_int_peak = 166

**CPU2017 License:** 6523
**Test Sponsor:** Esconet Technologies Ltd.
**Tested by:** Esconet Technologies Ltd.

**Test Date:** Apr-2025
**Hardware Availability:** Apr-2023
**Software Availability:** Jun-2024




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Gold 6426Y |
| Max MHz: | 4100 |
| Nominal: | 2500 |
| Enabled: | 16 cores, 1 chip, 2 threads/core |
| Orderable: | 1 Chip |
| Cache L1: | 32 KB I + 48 KB D on chip per core |
| L2: | 2 MB I+D on chip per core |
| L3: | 37.5 MB I+D on chip per chip |
| Other: | None |
| Memory: | 256 GB (8 x 32 GB 1Rx4 PC5-5600B-R, running at 4800) |
| Storage: | 125 GB on tmpfs |
| Other: | CPU Cooling: Air |

## Software

| | |
|---|---|
| OS: | SUSE Linux Enterprise Server 15 SP6<br>6.4.0-150600.21-default |
| Compiler: | C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;<br>Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux; |
| Parallel: | No |
| Firmware: | Version 1.26 released Apr-2023 |
| File System: | tmpfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other: | jemalloc memory allocator V5.0.1 |
| Power Management: | OS set to prefer performance<br>at the cost of additional power usage. |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 32 | 428 | 119 | 427 | 119 | **428** | **119** | 32 | **394** | **129** | 394 | 129 | 394 | 129 |
| 502.gcc_r | 32 | 322 | 141 | **326** | **139** | 327 | 139 | 32 | **276** | **164** | 277 | 164 | 276 | 164 |
| 505.mcf_r | 32 | 190 | 272 | 189 | 273 | **190** | **273** | 32 | 190 | 272 | 189 | 273 | **190** | **273** |
| 520.omnetpp_r | 32 | 375 | 112 | 372 | 113 | **373** | **113** | 32 | 375 | 112 | 372 | 113 | **373** | **113** |
| 523.xalancbmk_r | 32 | 146 | 231 | 148 | 228 | **147** | **230** | 32 | 146 | 231 | 148 | 228 | **147** | **230** |
| 525.x264_r | 32 | 180 | 312 | **180** | **312** | 180 | 312 | 32 | 169 | 332 | 169 | 332 | **169** | **332** |
| 531.deepsjeng_r | 32 | 318 | 115 | **318** | **115** | 318 | 115 | 32 | 318 | 115 | **318** | **115** | 318 | 115 |
| 541.leela_r | 32 | **485** | **109** | 485 | 109 | 485 | 109 | 32 | **485** | **109** | 485 | 109 | 485 | 109 |
| 548.exchange2_r | 32 | **251** | **334** | 251 | 333 | 251 | 334 | 32 | **251** | **334** | 251 | 333 | 251 | 334 |
| 557.xz_r | 32 | 456 | 75.8 | **458** | **75.5** | 459 | 75.4 | 32 | 456 | 75.8 | **458** | **75.5** | 459 | 75.4 |

**SPECrate®2017_int_base = 161**

**SPECrate®2017_int_peak = 166**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu17/lib/intel64:/mnt/ramdisk/cpu17/lib/ia32:/mnt/ramdisk/cpu17/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

## General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
```

**(Continued on next page)**

## General Notes (Continued)

```
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"
```

## Platform Notes

```
BIOS settings: Default

Sysinfo program /mnt/ramdisk/cpu17/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Apr 29 23:18:09 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   23:18:09 up 14 min,  1 user,  load average: 0.03, 0.12, 0.07
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                23:08    3.00s  0.80s  0.00s sh
  reportable-ic2023.2.3-lin-sapphirerapids-rate-smt-on-20231121.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
```

(Continued on next page)

## Platform Notes (Continued)

```
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 1027773
  max locked memory       (kbytes, -l) 8192
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 1027773
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=31
 login -- root
 -bash
 sh reportable-ic2023.2.3-lin-sapphirerapids-rate-smt-on-20231121.sh
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
   ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
   --define no-numa --tune base,peak -o all --define drop_caches intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
   ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
   --define no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
   base:peak --size refrate intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu17

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) Gold 6426Y
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 143
    stepping        : 8
    microcode       : 0x2b000590
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
    cpu cores       : 16
    siblings        : 32
    1 physical ids (chips)
    32 processors (hardware threads)
    physical id 0: core ids 0-15
    physical id 0: apicids 0-31
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
```

(Continued on next page)

## Platform Notes (Continued)

```
Byte Order:                      Little Endian
CPU(s):                          32
On-line CPU(s) list:             0-31
Vendor ID:                       GenuineIntel
BIOS Vendor ID:                  Intel(R) Corporation
Model name:                      Intel(R) Xeon(R) Gold 6426Y
BIOS Model name:                 Intel(R) Xeon(R) Gold 6426Y  CPU @ 2.5GHz
BIOS CPU family:                 179
CPU family:                      6
Model:                           143
Thread(s) per core:              2
Core(s) per socket:              16
Socket(s):                       1
Stepping:                        8
CPU(s) scaling MHz:              45%
CPU max MHz:                     4100.0000
CPU min MHz:                     800.0000
BogoMIPS:                        5000.00
Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                 pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                 pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                 nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                 pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                                 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                                 tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                                 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                                 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                                 vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
                                 rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                                 clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                                 xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                 split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                 arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req hfi vnmi
                                 avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                                 avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
                                 bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                                 serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                                 amx_int8 flush_l1d arch_capabilities
Virtualization:                  VT-x
L1d cache:                       768 KiB (16 instances)
L1i cache:                       512 KiB (16 instances)
L2 cache:                        32 MiB (16 instances)
L3 cache:                        37.5 MiB (1 instance)
NUMA node(s):                    1
NUMA node0 CPU(s):               0-31
Vulnerability Gather data sampling:   Not affected
Vulnerability Itlb multihit:          Not affected
Vulnerability L1tf:                   Not affected
Vulnerability Mds:                    Not affected
Vulnerability Meltdown:               Not affected
Vulnerability Mmio stale data:        Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:               Not affected
Vulnerability Spec rstack overflow:   Not affected
Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                 PBRSB-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds:                  Not affected
Vulnerability Tsx async abort:        Not affected
```

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## Platform Notes (Continued)

```
From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     768K   12 Data            1    64        1             64
     L1i       32K     512K    8 Instruction     1    64        1             64
     L2         2M      32M   16 Unified         2  2048        1             64
     L3      37.5M    37.5M   15 Unified         3 40960        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-31
  node 0 size: 256969 MB
  node 0 free: 248216 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       263136984 kB

------------------------------------------------------------
10. who -r
  run-level 3 Apr 29 23:03

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
                   kdump-early kdump-notify nvmefc-boot-connections nvmf-autoconnect postfix purge-kernels
                   rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
                   wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebtables fsidd
                   grub2-once haveged issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind
                   rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-confext
                   systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
  indirect         systemd-userdbd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=e40d4a6f-b67a-430a-86f1-3d3ea26a7eb1
  splash=silent
  resume=/dev/disk/by-uuid/767864c6-130c-4bba-bf53-bf715ee2aa3c
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=355M,high
  crashkernel=72M,low

------------------------------------------------------------
14. cpupower frequency-info
```

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## Platform Notes (Continued)

```
  analyzing CPU 4:
    current policy: frequency should be within 800 MHz and 4.10 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu17
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs  125G  4.1G  121G   4% /mnt/ramdisk

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
```

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## Platform Notes (Continued)

```
     Vendor:         HEXADATA
     Product:        HD-RS4100

 ------------------------------------------------------------
 21. dmidecode
   Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     8x Hynix TR532G56S446 32 GB 1 rank 5600, configured at 4800


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       American Megatrends International, LLC.
    BIOS Version:      1.26
    BIOS Date:         04/17/2023
    BIOS Revision:     5.29
```

## Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
```

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## Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------------------------------------
==============================================================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

## Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

## Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math

**(Continued on next page)**

## Base Optimization Flags (Continued)

C++ benchmarks (continued):
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

## Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

## Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

## Peak Optimization Flags

C benchmarks:

**(Continued on next page)**

# Peak Optimization Flags (Continued)

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html
http://www.spec.org/cpu2017/flags/Hexadata-Platform-Flags-Intel-rev1.7.html

## Esconet Technologies Ltd.

Hexadata HD-RS4100 Ver: SPR-001
(Intel Xeon Gold 6426Y, 2.50 GHz)

SPECrate®2017_int_base = 161

SPECrate®2017_int_peak = 166

**CPU2017 License:** 6523
**Test Sponsor:** Esconet Technologies Ltd.
**Tested by:** Esconet Technologies Ltd.

**Test Date:** Apr-2025
**Hardware Availability:** Apr-2023
**Software Availability:** Jun-2024

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml
http://www.spec.org/cpu2017/flags/Hexadata-Platform-Flags-Intel-rev1.7.xml



For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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