



# SPEC CPU®2017 Floating Point Speed Result

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## New H3C Technologies Co., Ltd.

H3C UniServer R4900 G6 (Intel Xeon Gold 6530)

**SPECspeed®2017\_fp\_base = 251**

**SPECspeed®2017\_fp\_peak = Not Run**

**CPU2017 License:** 9066

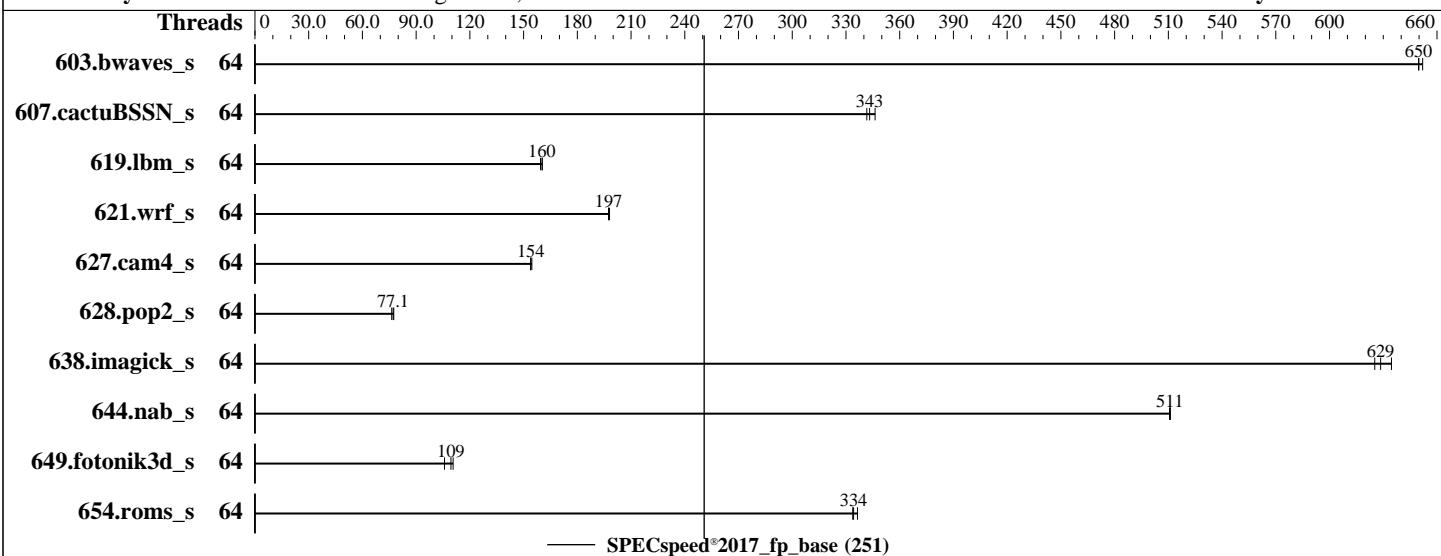
**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Test Date:** Apr-2025

**Hardware Availability:** Oct-2023

**Software Availability:** Mar-2024



### Hardware

CPU Name: Intel Xeon Gold 6530  
 Max MHz: 4000  
 Nominal: 2100  
 Enabled: 64 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 160 MB I+D on chip per chip  
 Other: None  
 Memory: 512 GB (8 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
 Storage: 1 x 960 GB SATA SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default  
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
 Parallel: Yes  
 Firmware: Version 6.10.43 released Jul-2024 BIOS  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	64	<b>90.8</b>	<b>650</b>	90.8	650	90.5	652							
607.cactuBSSN_s	64	48.1	346	<b>48.6</b>	<b>343</b>	48.8	342							
619.lbm_s	64	32.6	160	<b>32.7</b>	<b>160</b>	32.9	159							
621.wrf_s	64	66.8	198	<b>67.0</b>	<b>197</b>	67.0	197							
627.cam4_s	64	<b>57.5</b>	<b>154</b>	57.3	155	57.6	154							
628.pop2_s	64	153	77.5	156	76.4	<b>154</b>	<b>77.1</b>							
638.imagick_s	64	23.1	625	22.7	635	<b>23.0</b>	<b>629</b>							
644.nab_s	64	34.2	511	34.2	511	<b>34.2</b>	<b>511</b>							
649.fotonik3d_s	64	82.4	111	<b>83.3</b>	<b>109</b>	86.1	106							
654.roms_s	64	46.8	336	<b>47.1</b>	<b>334</b>	47.2	334							
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"

MALLOC\_CONF = "retain:true"

OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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## Platform Notes

### BIOS Settings:

Enable LP [Global] = Single LP

SNC = Disabled

Package C State = C0/C1 state

LLC Prefetch = Enabled

### BMC Settings:

Fan mode = powerful mode

```
Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Apr 10 14:35:57 2025
```

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage
18. /sys/kernel/mm/transparent\_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

```
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
14:35:57 up 25 min, 1 user, load average: 0.00, 4.30, 13.22
USER      TTY      FROM          LOGIN@    IDLE    JCPU    PCPU WHAT
root      tty1          -          14:11   12.00s  1.56s  0.01s sh speed.sh
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
```

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## Platform Notes (Continued)

```

data seg size          (kbytes, -d) unlimited
scheduling priority   (-e) 0
file size             (blocks, -f) unlimited
pending signals        (-i) 2062539
max locked memory     (kbytes, -l) 64
max memory size       (kbytes, -m) unlimited
open files             (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 2062539
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited

```

---

5. sysinfo process ancestry  
`/usr/lib/systemd/systemd --switched-root --system --deserialize 29  
login -- root  
-bash  
sh speed.sh  
runcpu --nobuild --action validate --define default-platform-flags -c  
  ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=64 --tune base -o all --define drop_caches  
  fpspeed  
runcpu --nobuild --action validate --define default-platform-flags --configfile  
  ic2024.1-lin-sapphirerapids-speed-20240308.cfg --define cores=64 --tune base --output_format all --define  
  drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv --note-preenv  
  --logfile $SPEC/tmp/CPU2017.011/templogs/preenv.fpspeed.011.0.log --lognum 011.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/speccpu`

---

6. /proc/cpuinfo  
model name : INTEL(R) XEON(R) GOLD 6530
vendor\_id : GenuineIntel
cpu family : 6
model : 207
stepping : 2
microcode : 0x21000200
bugs : spectre\_v1 spectre\_v2 spec\_store\_bypass swapgs eibrss\_pbrss
cpu cores : 32
siblings : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62
physical id 1: apicids
128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
80,182,184,186,188,190
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

---

7. lscpu

From lscpu from util-linux 2.37.4:  
Architecture: x86\_64

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## Platform Notes (Continued)

```

CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) GOLD 6530
CPU family: 6
Model: 207
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 2
Stepping: 2
CPU max MHz: 4000.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
       x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
       abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
       cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmil hle
       avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsaved xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
       cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp
       hwp_act_window hwp_epp hwp_pkg_req avx512vmbi umip pku ospke waitpkg
       avx512_vmbi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
       avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
       enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr avx512_fp16
       amx_tile flush_lld arch_capabilities
L1d cache: 3 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 128 MiB (64 instances)
L3 cache: 320 MiB (2 instances)
NUMA node(s): 2
NUMA node0 CPU(s): 0-31
NUMA node1 CPU(s): 32-63
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
      NAME  ONE-SIZE  ALL-SIZE  WAYS  TYPE      LEVEL    SETS  PHY-LINE  COHERENCY-SIZE
      L1d     48K      3M     12 Data          1      64        1          64
      L1i     32K      2M      8 Instruction   1      64        1          64
      L2      2M     128M     16 Unified        2     2048        1          64
      L3     160M     320M     20 Unified        3   131072        1          64

```

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## Platform Notes (Continued)

8. numactl --hardware  
NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)  
node 0 cpus: 0-31  
node 0 size: 257638 MB  
node 0 free: 255947 MB  
node 1 cpus: 32-63  
node 1 size: 258019 MB  
node 1 free: 255385 MB  
node distances:  
node 0 1  
0: 10 21  
1: 21 10

9. /proc/meminfo  
MemTotal: 528033196 kB

10. who -r  
run-level 3 Apr 10 14:11

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings postfix  
purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4  
wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed  
debug-shell ebttables grub2-once haveged haveged-switch-root issue-add-ssh-keys kexec-load  
lunmask rpmconfigcheck serial-getty@ systemd-boot-check-no-failures  
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned  
indirect pcscd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=5b1bbac0-75ae-47d9-95db-594b94b8ab43  
splash=silent  
mitigations=auto  
quiet  
security=apparmor

14. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 800 MHz and 4.00 GHz.  
The governor "powersave" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

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## Platform Notes (Continued)

-----  
15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.  
Preset profile: throughput-performance

-----  
16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

-----  
17. /sys/kernel/mm/transparent\_hugepage

defrag	always defer defer+madvise [madvise] never
enabled	[always] madvise never
hpae_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

-----  
19. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP5

-----  
20. Disk information

SPEC is set to: /home/speccpu

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	btrfs	892G	115G	776G	13%	/home

-----  
21. /sys/devices/virtual/dmi/id

Vendor:	New H3C Technologies Co., Ltd.
Product:	H3C UniServer R4900 G6
Product Family:	Rack

(Continued on next page)



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## Platform Notes (Continued)

Serial: 210235A4HEH242000028

-----  
22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Hynix HMCG94AGBRA181N 64 GB 2 rank 5600, configured at 4800

-----  
23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.

BIOS Version: 6.10.43

BIOS Date: 07/27/2024

BIOS Revision: 5.32

## Compiler Version Notes

=====

C | 619.lbm\_s(base) 638.imagick\_s(base) 644.nab\_s(base)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C++, C, Fortran | 607.cactuBSSN\_s(base)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran | 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran, C | 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)

-----  
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
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## Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC\_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC\_OPENMP -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math

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SPECspeed®2017\_fp\_base = 251

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9066

Test Date: Apr-2025

Test Sponsor: New H3C Technologies Co., Ltd.

Hardware Availability: Oct-2023

Tested by: New H3C Technologies Co., Ltd.

Software Availability: Mar-2024

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-intel-RevB.html](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-intel-RevB.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

[http://www.spec.org/cpu2017/flags/New\\_H3C-Platform-Settings-intel-RevB.xml](http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-intel-RevB.xml)

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