



# SPEC CPU®2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL320 Gen12

(2.30 GHz, Intel Xeon 6515P)

SPECrate®2017\_int\_base = 190

SPECrate®2017\_int\_peak = 195

CPU2017 License: 3

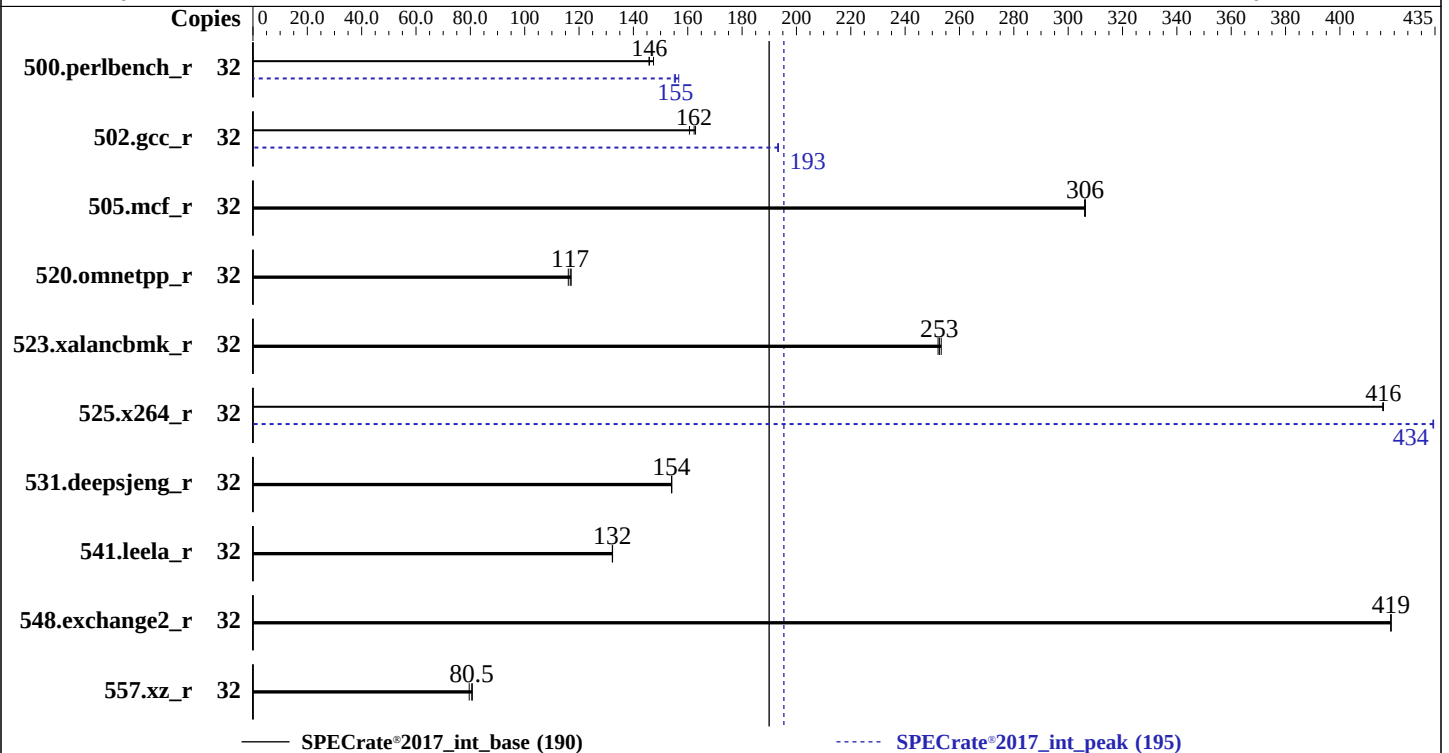
Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024



## Hardware

CPU Name: Intel Xeon 6515P  
Max MHz: 3800  
Nominal: 2300  
Enabled: 16 cores, 1 chip, 2 threads/core  
Orderable: 1 Chip  
Cache L1: 64 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 72 MB I+D on chip per chip  
Other: None  
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-6400B-R)  
Storage: 1 x 1.2 TB NVMe SSD  
Other: CPU Cooling: Air

## Software

OS: SUSE Linux Enterprise Server 15 SP6  
Kernel 6.4.0-150600.21-default  
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: HPE BIOS Version v1.20 02/14/2025 released Feb-2025  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage



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## Results Table

| Benchmark       | Base   |                   |                    |                   |                   |                   |                   | Peak   |                   |                    |                   |                   |                   |                   |
|-----------------|--------|-------------------|--------------------|-------------------|-------------------|-------------------|-------------------|--------|-------------------|--------------------|-------------------|-------------------|-------------------|-------------------|
|                 | Copies | Seconds           | Ratio              | Seconds           | Ratio             | Seconds           | Ratio             | Copies | Seconds           | Ratio              | Seconds           | Ratio             | Seconds           | Ratio             |
| 500.perlbench_r | 32     | 346               | 147                | 350               | 146               | <b><u>349</u></b> | <b><u>146</u></b> | 32     | <b><u>328</u></b> | <b><u>155</u></b>  | 329               | 155               | 325               | 157               |
| 502.gcc_r       | 32     | <b><u>279</u></b> | <b><u>162</u></b>  | 282               | 161               | 278               | 163               | 32     | 234               | 193                | 235               | 193               | <b><u>234</u></b> | <b><u>193</u></b> |
| 505.mcf_r       | 32     | <b><u>169</u></b> | <b><u>306</u></b>  | 169               | 306               | 169               | 306               | 32     | <b><u>169</u></b> | <b><u>306</u></b>  | 169               | 306               | 169               | 306               |
| 520.omnetpp_r   | 32     | 362               | 116                | <b><u>360</u></b> | <b><u>117</u></b> | 359               | 117               | 32     | 362               | 116                | <b><u>360</u></b> | <b><u>117</u></b> | 359               | 117               |
| 523.xalancbmk_r | 32     | 134               | 252                | 133               | 253               | <b><u>134</u></b> | <b><u>253</u></b> | 32     | 134               | 252                | 133               | 253               | <b><u>134</u></b> | <b><u>253</u></b> |
| 525.x264_r      | 32     | <b><u>135</u></b> | <b><u>416</u></b>  | 135               | 416               | 135               | 416               | 32     | 129               | 434                | <b><u>129</u></b> | <b><u>434</u></b> | 129               | 434               |
| 531.deepsjeng_r | 32     | 238               | 154                | <b><u>238</u></b> | <b><u>154</u></b> | 238               | 154               | 32     | 238               | 154                | <b><u>238</u></b> | <b><u>154</u></b> | 238               | 154               |
| 541.leela_r     | 32     | 401               | 132                | 400               | 132               | <b><u>401</u></b> | <b><u>132</u></b> | 32     | 401               | 132                | 400               | 132               | <b><u>401</u></b> | <b><u>132</u></b> |
| 548.exchange2_r | 32     | 200               | 419                | <b><u>200</u></b> | <b><u>419</u></b> | 200               | 419               | 32     | 200               | 419                | <b><u>200</u></b> | <b><u>419</u></b> | 200               | 419               |
| 557.xz_r        | 32     | <b><u>429</u></b> | <b><u>80.5</u></b> | 428               | 80.7              | 434               | 79.6              | 32     | <b><u>429</u></b> | <b><u>80.5</u></b> | 428               | 80.7              | 434               | 79.6              |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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## Platform Notes

### BIOS Configuration:

Workload Profile set to General Throughput Compute  
Enhanced Processor Performance Profile set to Aggressive  
Thermal Configuration set to Maximum Cooling  
Memory Patrol Scrubbing set to Disabled  
Last Level Cache (LLC) Prefetch set to Enabled  
XPT Prefetch set to Disabled  
Workload Profile set to Custom  
DCU Stream Prefetcher set to Disabled  
Adjacent Sector Prefetch set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Mar 25 10:50:41 2025

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  12. Services, from systemctl list-unit-files
  13. Linux kernel boot-time arguments, from /proc/cmdline
  14. cpupower frequency-info
  15. sysctl
  16. /sys/kernel/mm/transparent\_hugepage
  17. /sys/kernel/mm/transparent\_hugepage/khugepaged
  18. OS release
  19. Disk information
  20. /sys/devices/virtual/dmi/id
  21. dmidecode
  22. BIOS
- 
1. uname -a  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT\_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)  
x86\_64 x86\_64 x86\_64 GNU/Linux
- 
2. w  
10:50:41 up 4 min, 3 users, load average: 0.08, 0.03, 0.01  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
- 
3. Username  
From environment variable \$USER: root
- 
4. ulimit -a

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## Platform Notes (Continued)

```
core file size      (blocks, -c) unlimited
data seg size      (kbytes, -d) unlimited
scheduling priority (-e) 0
file size          (blocks, -f) unlimited
pending signals    (-i) 1030006
max locked memory  (kbytes, -l) 8192
max memory size    (kbytes, -m) unlimited
open files         (-n) 1024
pipe size          (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size         (kbytes, -s) unlimited
cpu time           (seconds, -t) unlimited
max user processes (-u) 1030006
virtual memory     (kbytes, -v) unlimited
file locks         (-x) unlimited
```

### 5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/intratespron.sh
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define no-numa --tune base,peak -o all --define drop_caches intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=16 --define physicalfirst
--define no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.031/templogs/preenv.intrate.031.0.log --lognum 031.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

### 6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) 6515P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 173
stepping        : 1
microcode       : 0x1000380
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores       : 16
siblings        : 32
1 physical ids (chips)
32 processors (hardware threads)
physical id 0:  core ids 0-15
physical id 0:  apicids 0-31
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      46 bits physical, 57 bits virtual
```

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## Platform Notes (Continued)

Byte Order: Little Endian  
CPU(s): 32  
On-line CPU(s) list: 0-31  
Vendor ID: GenuineIntel  
BIOS Vendor ID: Intel(R) Corporation  
Model name: Intel(R) Xeon(R) 6515P  
BIOS Model name: Intel(R) Xeon(R) 6515P CPU @ 2.3GHz  
BIOS CPU family: 179  
CPU family: 6  
Model: 173  
Thread(s) per core: 2  
Core(s) per socket: 16  
Socket(s): 1  
Stepping: 1  
BogoMIPS: 4600.00  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat  
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx  
pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good  
nopl xtopology nonstop\_tsc cpuid aperfmperf tsc\_known\_freq pni  
pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16  
xtpr pdcm pcid dca sse4\_1 sse4\_2 x2apic movbe popcnt  
tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm  
3dnowprefetch cpuid\_fault epb cat\_l3 cat\_l2 cdp\_l3 intel\_ppin cdp\_l2  
ssbd mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow flexpriority ept  
vpid ept\_ad fsgsbase tsc\_adjust bmi1 hle avx2 smep bmi2 erms invpcid  
rtm cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt  
clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec  
xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local  
split\_lock\_detect user\_shstk avx\_vnni avx512\_bf16 wbnoinvd dtherm ida  
arat pln pts vnmi avx512vbmi umip pku ospke waitpkg avx512\_vbmi2 gfni  
vaes vpclmulqdq avx512\_vnni avx512\_bitalg tme avx512\_vpopcntdq la57  
rdpid bus\_lock\_detect cldemote movdiri movdir64b enqcmd fsrm md\_clear  
serialize tsxldtrk pconfig arch\_lbr ibt amx\_bf16 avx512\_fp16 amx\_tile  
amx\_int8 flush\_l1d arch\_capabilities  
Virtualization: VT-x  
L1d cache: 768 KiB (16 instances)  
L1i cache: 1 MiB (16 instances)  
L2 cache: 32 MiB (16 instances)  
L3 cache: 72 MiB (1 instance)  
NUMA node(s): 1  
NUMA node0 CPU(s): 0-31  
Vulnerability Gather data sampling: Not affected  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Reg file data sampling: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec rstack overflow: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;  
PBRSE-eIBRS Not affected; BHI BHI\_DIS\_S  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE | LEVEL | SETS | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|------|-------|------|----------|----------------|
| L1d  | 48K      | 768K     | 12   | Data | 1     | 64   | 1        | 64             |

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## Platform Notes (Continued)

|     |     |     |                |   |       |   |    |
|-----|-----|-----|----------------|---|-------|---|----|
| L1i | 64K | 1M  | 16 Instruction | 1 | 64    | 1 | 64 |
| L2  | 2M  | 32M | 16 Unified     | 2 | 2048  | 1 | 64 |
| L3  | 72M | 72M | 16 Unified     | 3 | 73728 | 1 | 64 |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)

node 0 cpus: 0-31

node 0 size: 257526 MB

node 0 free: 256502 MB

node distances:

node 0

0: 10

9. /proc/meminfo

MemTotal: 263707016 kB

10. who -r

run-level 3 Apr 22 17:30

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE UNIT FILES

enabled apparmor auditd cron getty@ irqbalance issue-generator kbdsettings nvme-fc-boot-connections  
nvme-fc-autoconnect postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4  
wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs

disabled boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once  
haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load rpmconfigcheck serial-getty@  
systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysex

systemd-time-wait-sync systemd-timesyncd

indirect systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default

root=UUID=84964d71-b738-4181-8365-490a27ae412d

splash=silent

resume=/dev/disk/by-uuid/d32c8d7d-0388-4dc2-a4d2-6ac3b24bfd85

mitigations=auto

quiet

security=apparmor

14. cpupower frequency-info

analyzing CPU 26:

Unable to determine current policy

boost state support:

Supported: yes

Active: yes

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## Platform Notes (Continued)

### 15. sysctl

|                              |       |
|------------------------------|-------|
| kernel.numa_balancing        | 0     |
| kernel.randomize_va_space    | 2     |
| vm.compaction_proactiveness  | 20    |
| vm.dirty_background_bytes    | 0     |
| vm.dirty_background_ratio    | 10    |
| vm.dirty_bytes               | 0     |
| vm.dirty_expire_centisecs    | 3000  |
| vm.dirty_ratio               | 20    |
| vm.dirty_writeback_centisecs | 500   |
| vm.dirtytime_expire_seconds  | 43200 |
| vm.extfrag_threshold         | 500   |
| vm.min_unmapped_ratio        | 1     |
| vm.nr_hugepages              | 0     |
| vm.nr_hugepages_mempolicy    | 0     |
| vm.nr_overcommit_hugepages   | 0     |
| vm.swappiness                | 60    |
| vm.watermark_boost_factor    | 15000 |
| vm.watermark_scale_factor    | 10    |
| vm.zone_reclaim_mode         | 0     |

### 16. /sys/kernel/mm/transparent\_hugepage

|                |          |             |               |           |            |
|----------------|----------|-------------|---------------|-----------|------------|
| defrag         | always   | defer       | defer+madvise | [madvise] | never      |
| enabled        | [always] | madvise     | never         |           |            |
| hpage_pmd_size | 2097152  |             |               |           |            |
| shmem_enabled  | always   | within_size | advise        | [never]   | deny force |

### 17. /sys/kernel/mm/transparent\_hugepage/khugepaged

|                       |       |
|-----------------------|-------|
| alloc_sleep_millisecs | 60000 |
| defrag                | 1     |
| max_ptes_none         | 511   |
| max_ptes_shared       | 256   |
| max_ptes_swap         | 64    |
| pages_to_scan         | 4096  |
| scan_sleep_millisecs  | 10000 |

### 18. OS release

From /etc/\*-release /etc/\*-version  
os-release SUSE Linux Enterprise Server 15 SP6

### 19. Disk information

SPEC is set to: /home/cpu2017

| Filesystem     | Type | Size | Used | Avail | Use% | Mounted on |
|----------------|------|------|------|-------|------|------------|
| /dev/nvme0n1p4 | xfs  | 1.2T | 257G | 940G  | 22%  | /home      |

### 20. /sys/devices/virtual/dmi/id

|                 |                                  |
|-----------------|----------------------------------|
| Vendor:         | HPE                              |
| Product:        | HPE ProLiant Compute DL320 Gen12 |
| Product Family: | ProLiant                         |
| Serial:         | S84PQDRU0T                       |

### 21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

(Continued on next page)





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## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Micron MTC20F2085S1RC64BD2 QSFF 32 GB 2 rank 6400

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE  
BIOS Version: 1.20  
BIOS Date: 02/14/2025  
BIOS Revision: 1.20  
Firmware Revision: 1.11

## Compiler Version Notes

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308  
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran | 548.exchange2\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308

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ProLiant Compute DL320 Gen12

(2.30 GHz, Intel Xeon 6515P)

SPECrate®2017\_int\_base = 190

SPECrate®2017\_int\_peak = 195

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

## Compiler Version Notes (Continued)

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## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -DSPEC\_LP64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto

-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

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# SPEC CPU®2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL320 Gen12

(2.30 GHz, Intel Xeon 6515P)

SPECrate®2017\_int\_base = 190

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## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-nostandard-realloc-lhs -align array32byte -auto  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)  
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse  
-funroll-loops -qopt-mem-layout-trans=4  
-fno-strict-overflow  
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

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# SPEC CPU®2017 Integer Rate Result

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SPECrate®2017\_int\_peak = 195

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

## Peak Optimization Flags (Continued)

```
502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapfirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmallo
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.2.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.2.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2025-03-25 01:20:40-0400.

Report generated on 2025-05-08 10:01:21 by CPU2017 PDF formatter v6716.

Originally published on 2025-05-06.