



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SD535 V3  
(4.10 GHz, AMD EPYC 9275F)

SPECrate®2017\_int\_base = 395

SPECrate®2017\_int\_peak = 403

CPU2017 License: 9017

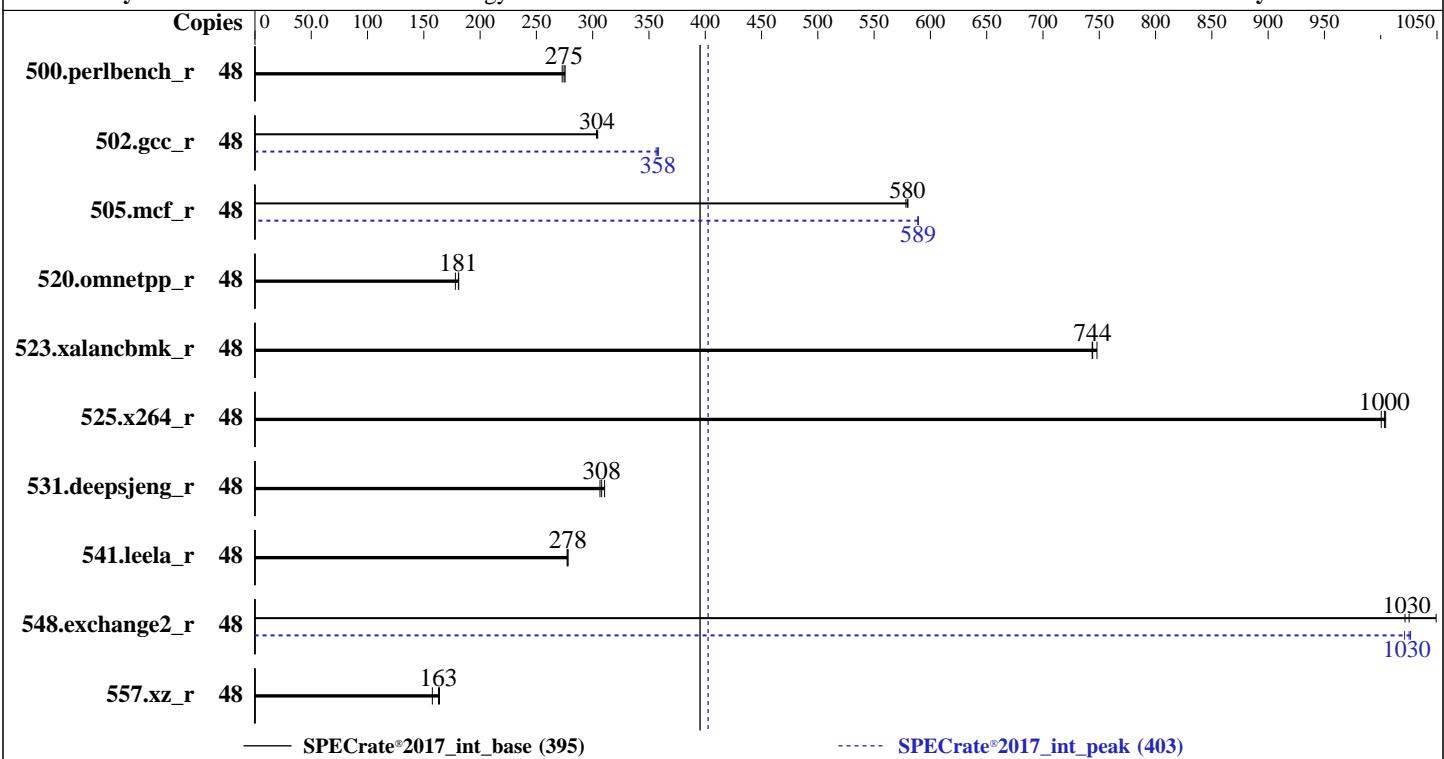
Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9275F  
Max MHz: 4800  
Nominal: 4100  
Enabled: 24 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 256 MB I+D on chip per chip,  
32 MB shared / 3 cores  
Other: None  
Memory: 384 GB (12 x 32 GB 2Rx8 PC5-6400B-R, running at 6000)  
Storage: 1 x 960 GB M.2 SATA SSD  
Other: CPU Cooling: Air

### Software

OS: Red Hat Enterprise Linux 9.4 (Plow)  
Compiler: Kernel 5.14.0-427.13.1.el9\_4.x86\_64  
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC  
Firmware: No  
File System: Lenovo BIOS Version GPE113I 4.10 released Dec-2024  
System State: xfs  
Base Pointers: Run level 3 (multi-user)  
Peak Pointers: 64-bit  
Other: 32/64-bit  
Power Management: None  
BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	48	<b>278</b>	<b>275</b>	280	273	277	276	48	<b>278</b>	<b>275</b>	280	273	277	276		
502.gcc_r	48	<b>224</b>	<b>304</b>	223	304	224	303	48	191	356	190	358	<b>190</b>	<b>358</b>		
505.mcf_r	48	134	580	134	578	<b>134</b>	<b>580</b>	48	132	589	<b>132</b>	<b>589</b>	132	589		
520.omnetpp_r	48	354	178	348	181	<b>348</b>	<b>181</b>	48	354	178	348	181	<b>348</b>	<b>181</b>		
523.xalancbmk_r	48	67.8	748	<b>68.1</b>	<b>744</b>	68.2	744	48	67.8	748	<b>68.1</b>	<b>744</b>	68.2	744		
525.x264_r	48	83.7	1000	<b>83.8</b>	<b>1000</b>	84.0	1000	48	83.7	1000	<b>83.8</b>	<b>1000</b>	84.0	1000		
531.deepsjeng_r	48	179	306	177	311	<b>179</b>	<b>308</b>	48	179	306	177	311	<b>179</b>	<b>308</b>		
541.leela_r	48	287	277	286	278	<b>286</b>	<b>278</b>	48	287	277	286	278	<b>286</b>	<b>278</b>		
548.exchange2_r	48	120	1050	123	1020	<b>123</b>	<b>1030</b>	48	123	1030	<b>123</b>	<b>1030</b>	123	1020		
557.xz_r	48	316	164	<b>318</b>	<b>163</b>	329	158	48	316	164	<b>318</b>	<b>163</b>	329	158		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

cpupower set to performance mode  
cpupower frequency-set -r -g performance  
To enable Transparent Hugepages (THP) for all allocations:  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-
    -aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance and then set it to Custom Mode

NUMA Nodes per Socket set to NPS4

P-State set to Enabled

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Mon Feb 24 23:07:49 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 252 (252-32.el9\_4)
  12. Services, from systemctl list-unit-files
  13. Linux kernel boot-time arguments, from /proc/cmdline
  14. cpupower frequency-info
  15. sysctl
  16. /sys/kernel/mm/transparent\_hugepage
  17. /sys/kernel/mm/transparent\_hugepage/khugepaged
  18. OS release
  19. Disk information
  20. /sys/devices/virtual/dmi/id
  21. dmidecode
  22. BIOS
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## Platform Notes (Continued)

1. uname -a  
Linux localhost.localdomain 5.14.0-427.13.1.el9\_4.x86\_64 #1 SMP PREEMPT\_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
23:07:49 up 3 min, 0 users, load average: 0.09, 0.05, 0.01  
USER TTY LOGIN@ IDLE PCPU WHAT

3. Username  
From environment variable \$USER: root

4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (i) 1546030  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 1546030  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 31  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@notty  
/bin/bash ./02.remote\_local\_SPECCpu\_1.01.sh  
/bin/bash ./Run025-compliant-amd-rateint.sh  
python3 ./run\_amd\_rate\_aocc500\_znver5\_A1.py  
/bin/bash ./amd\_rate\_aocc500\_znver5\_A1.sh  
runcpu --config amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 intrate  
runcpu --configfile amd\_rate\_aocc500\_znver5\_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile  
\$SPEC/tmp/CPU2017.132/templogs/preenv.intrate.132.0.log --lognum 132.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1.2

6. /proc/cpuinfo  
model name : AMD EPYC 9275F 24-Core Processor  
vendor\_id : AuthenticAMD  
cpu family : 26  
model : 2  
stepping : 1  
microcode : 0xb00211a

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## Platform Notes (Continued)

```

bugs          : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 192 4K pages
cpu cores     : 24
siblings       : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-2,8-10,16-18,24-26,32-34,40-42,48-50,56-58
               physical id 0: apicids 0-5,16-21,32-37,48-53,64-69,80-85,96-101,112-117
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
-----
```

### 7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 48
On-line CPU(s) list:   0-47
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9275F 24-Core Processor
BIOS Model name:        AMD EPYC 9275F 24-Core Processor
CPU family:             26
Model:                  2
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):              1
Stepping:               1
Frequency boost:        enabled
CPU(s) scaling MHz:   100%
CPU max MHz:            4100.0000
CPU min MHz:            1500.0000
BogoMIPS:                8186.90
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                       clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                       lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
                       aperfmpfper rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
                       x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
                       extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit
                       wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
                       cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
                       ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2 smep bmi2 erms
                       invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                       clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                       xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
                       avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin
                       cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
                       decodeassist pausefilter pfthreshold avic v_vmsave_vmlload vgif x2avic
                       v_spec_ctrl vnni avx512vbmi umip pku ospke avx512_vbmi2 gfn1 vaes
                       vpclmulqdq avx512_vnni avx512_bitlg avx512_vpopcntdq la57 rdpid
                       bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm
                       avx512_vp2intersect flush_lld debug_swap
Virtualization:          AMD-V
L1d cache:                1.1 MiB (24 instances)
L1i cache:                768 KiB (24 instances)
L2 cache:                 24 MiB (24 instances)
L3 cache:                 256 MiB (8 instances)
```

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## Platform Notes (Continued)

```
NUMA node(s): 4
NUMA node0 CPU(s): 0-5,24-29
NUMA node1 CPU(s): 6-11,30-35
NUMA node2 CPU(s): 12-17,36-41
NUMA node3 CPU(s): 18-23,42-47
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Llrf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, STIBP
always-on, RSB filling, PBRSB-eIBRS Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-5,24-29
node 0 size: 96349 MB
node 0 free: 95864 MB
node 1 cpus: 6-11,30-35
node 1 size: 96723 MB
node 1 free: 96327 MB
node 2 cpus: 12-17,36-41
node 2 size: 96764 MB
node 2 free: 96369 MB
node 3 cpus: 18-23,42-47
node 3 size: 96709 MB
node 3 free: 96354 MB
node distances:
node 0 1 2 3
 0: 10 12 12 12
 1: 12 10 12 12
 2: 12 12 10 12
 3: 12 12 12 10
```

-----  
9. /proc/meminfo

```
MemTotal: 395823768 kB
```

-----  
10. who -r  
run-level 3 Feb 24 23:04

-----  
11. Systemd service manager version: systemd 252 (252-32.el9\_4)

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## Platform Notes (Continued)

Default Target Status  
multi-user running

-----  
12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited chronyd crond  
dbus-broker getty@ insights-client-boot irqbalance kdump low-memory-monitor mdmonitor  
microcode nis-domainname nvmefc-boot-connections rhsmcertd rsyslog rtkit-daemon  
selinux-autorelabel-mark sshd sssd systemd-boot-update systemd-network-generator udisks2  
upower  
enabled-runtime systemd-remount-fs  
disabled canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot  
chrony-wait chronyd-restricted console-getty cpupower debug-shell dnf-system-upgrade  
firewalld kvm\_stat man-db-restart-cache-update nftables nvmmf-autoconnect pesign rdisc rhcd  
rhsm rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@  
systemd-boot-check-no-failures systemd-pstore systemd-sysext  
generated ntp\_sync  
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate  
systemd-sysupdate-reboot

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd3,gpt2)/boot/vmlinuz-5.14.0-427.13.1.el9\_4.x86\_64  
root=UUID=609e1264-f568-475c-b502-9e0795850886  
ro  
resume=UUID=7f8ee8bf-b24d-4aea-9bd8-e670d4778be8

-----  
14. cpupower frequency-info  
analyzing CPU 28:  
current policy: frequency should be within 1.50 GHz and 4.10 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 3466MHz

-----  
15. sysctl  
kernel.numa\_balancing 1  
kernel.randomize\_va\_space 0  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 1  
vm.watermark\_boost\_factor 15000

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## Platform Notes (Continued)

```
vm.watermark_scale_factor          10
vm.zone_reclaim_mode              1

-----
16. /sys/kernel/mm/transparent_hugepage
    defrag           [always] defer defer+madvise madvise never
    enabled          [always] madvise never
    hpage_pmd_size  2097152
    shmem_enabled   always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs  60000
    defrag                 1
    max_ptes_none         511
    max_ptes_shared       256
    max_ptes_swap         64
    pages_to_scan         4096
    scan_sleep_millisecs 10000

-----
18. OS release
    From /etc/*-release /etc/*-version
    os-release      Red Hat Enterprise Linux 9.4 (Plow)
    redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
    system-release  Red Hat Enterprise Linux release 9.4 (Plow)

-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0nlp4  xfs   820G  39G  782G  5%  /home

-----
20. /sys/devices/virtual/dmi/id
Vendor:        Lenovo
Product:       ThinkSystem SD535 V3
Product Family: ThinkSystem
Serial:        1234567890

-----
21. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
12x Samsung M321R4GA3PB2-CCPEC 32 GB 2 rank 6400, configured at 6000

-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:    Lenovo
BIOS Version:   GPE113I-4.10
BIOS Date:      12/12/2024
BIOS Revision:  4.10
Firmware Revision: 5.10
```



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## Compiler Version Notes

```
=====
C      | 502.gcc_r(peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C      | 502.gcc_r(peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
      | 541.leela_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

```
=====
Fortran | 548.exchange2_r(base, peak)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SD535 V3  
(4.10 GHz, AMD EPYC 9275F)

SPECrate®2017\_int\_base = 395

SPECrate®2017\_int\_peak = 403

CPU2017 License: 9017

Test Date: Feb-2025

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather  
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang  
-lamdalloc-ext -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie

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## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-fvirtual-function-elimination -fvisibility=hidden  
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext  
-ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto  
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost  
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500  
-lamdlibm -lflang -lamdalloc -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



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Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64  
502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LINUX -DSPEC\_LP64  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench\_r: basepeak = yes  
  
502.gcc\_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner  
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM  
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline  
-lamdalloc  
  
505.mcf\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lflang -lamdalloc-ext -ldl

525.x264\_r: basepeak = yes

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

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## Peak Optimization Flags (Continued)

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver5 -fveclib=AMDLIBM
-ffast-math -ftlo -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdaloc -ldl
```

## Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc\_r: -L/usr/lib32 -Wno-unused-command-line-argument
-L/home/work/cpu2017/v119/aocc5/1316/amd\_rate\_aocc500\_znver5\_A\_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.html>
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.xml>
<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>



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Tested with SPEC CPU®2017 v1.1.9 on 2025-02-24 10:07:49-0500.

Report generated on 2025-03-26 10:36:50 by CPU2017 PDF formatter v6716.

Originally published on 2025-03-25.