



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECspeed®2017_fp_base = 176

SPECspeed®2017_fp_peak = 181

CPU2017 License: 001176

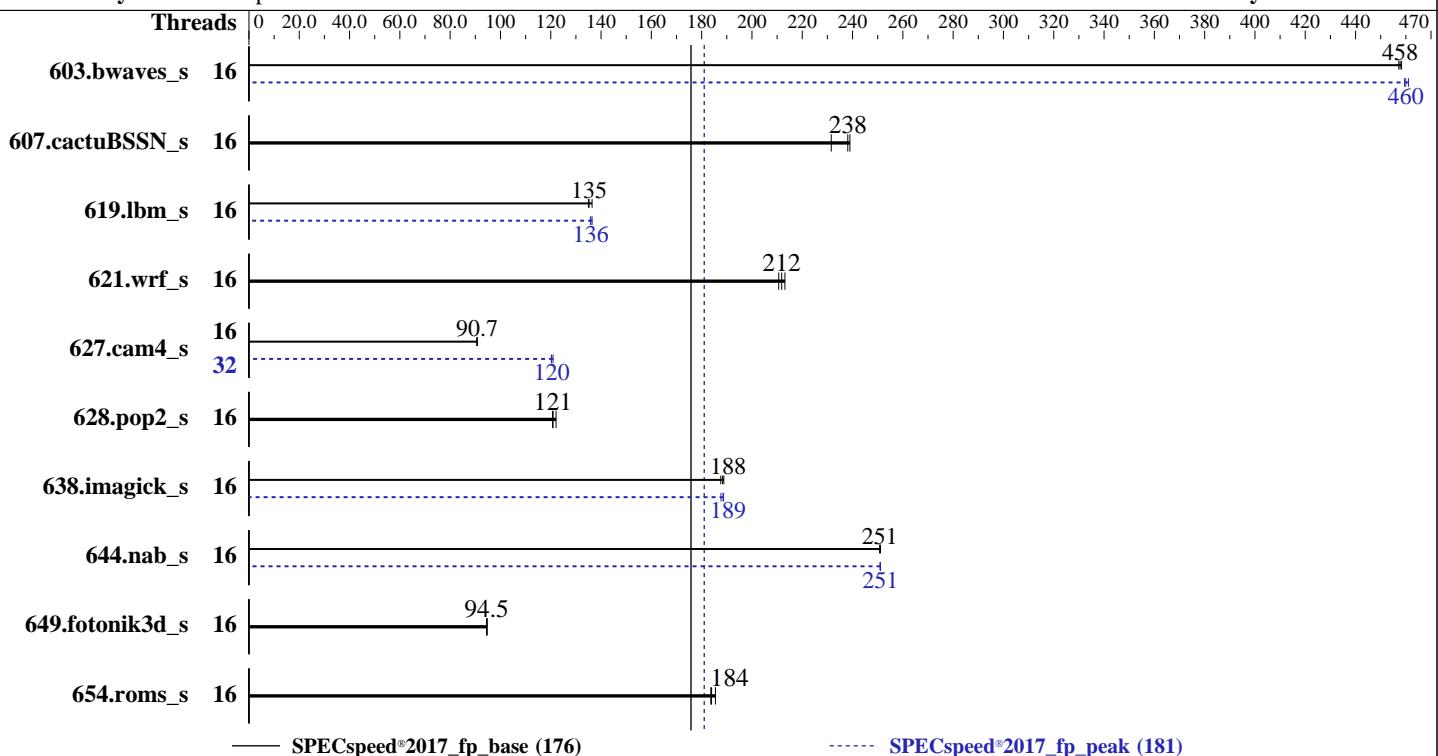
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2025

Hardware Availability: Oct-2024

Software Availability: Feb-2025



Hardware		Software	
CPU Name:	AMD EPYC 9115	OS:	Ubuntu 24.04.2 LTS
Max MHz:	4100	Compiler:	Kernel 6.8.0-54-generic
Nominal:	2600	Parallel:	C/C++/Fortran: Version 5.0.0 of AOCC
Enabled:	16 cores, 1 chip, 2 threads/core	Firmware:	Yes
Orderable:	1 chip	File System:	Version 3.4 released Feb-2025
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	ext4
L2:	1 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	64 MB I+D on chip per chip, 32 MB shared / 8 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	256 GB (8 x 32 GB 2Rx4 PC5-5600B-R)	Power Management:	None
Storage:	1 x 960 GB NVMe SSD	BIOS and OS set to prefer performance at the cost of additional power usage.	
Other:	CPU Cooling: Air		



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	129	458	129	457	129	458	16	128	460	128	459	128	461
607.cactuBSSN_s	16	69.8	239	72.0	232	70.0	238	16	69.8	239	72.0	232	70.0	238
619.lbm_s	16	38.7	135	38.8	135	38.4	136	16	38.4	136	38.6	136	38.5	136
621.wrf_s	16	62.1	213	62.4	212	62.8	211	16	62.1	213	62.4	212	62.8	211
627.cam4_s	16	97.7	90.7	97.8	90.7	97.7	90.7	32	73.6	120	73.7	120	73.3	121
628.pop2_s	16	97.2	122	98.3	121	98.2	121	16	97.2	122	98.3	121	98.2	121
638.imagick_s	16	76.9	188	76.6	188	76.4	189	16	76.4	189	76.5	189	76.9	188
644.nab_s	16	69.6	251	69.6	251	69.7	251	16	69.6	251	69.6	251	69.6	251
649.fotonik3d_s	16	96.4	94.5	96.5	94.5	96.2	94.7	16	96.4	94.5	96.5	94.5	96.2	94.7
654.roms_s	16	85.6	184	84.9	185	85.7	184	16	85.6	184	84.9	185	85.7	184
SPECSpeed®2017_fp_base = 176														
SPECSpeed®2017_fp_peak = 181														

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

SPECspeed®2017_fp_base = 176

SPECspeed®2017_fp_peak = 181

Test Date: Feb-2025

Hardware Availability: Oct-2024

Software Availability: Feb-2025

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-31"
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "32"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

Environment variables set by runcpu during the 619.lbm_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

Environment variables set by runcpu during the 627.cam4_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 638.imagick_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

Environment variables set by runcpu during the 644.nab_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
NUMA Nodes Per Socket = NPS2
Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 155
Package Power Limit Control = Manual
Package Power Limit = 155
TSME = Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on as-1115cs-tnr-9115 Wed Feb 26 22:55:30 2025
```

SUT (System Under Test) info as seen by some common utilities.

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Platform Notes (Continued)

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.5)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. sysctl
- 17. /sys/kernel/mm/transparent_hugepage
- 18. /sys/kernel/mm/transparent_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

1. uname -a
Linux as-1115cs-tnr-9115 6.8.0-54-generic #56-Ubuntu SMP PREEMPT_DYNAMIC Sat Feb 8 00:37:57 UTC 2025
x86_64 x86_64 x86_64 GNU/Linux

2. w
22:55:30 up 12:05, 1 user, load average: 6.64, 6.00, 3.69
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
smc ttysl - 10:52 12:02m 1.14s 1.09s sudo su -

3. Username
From environment variable \$USER: root
From the command 'logname': smc

4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited
locked memory(kbytes) 2097152
process 1029812
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtsprio 0

5. sysinfo process ancestry

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Platform Notes (Continued)

```
/sbin/init
/bin/login -p --
-bash
sudo su -
sudo su -
su -
-bash
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
6. /proc/cpuinfo
model name      : AMD EPYC 9115 16-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 26
model          : 2
stepping        : 1
microcode       : 0xb00211e
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 16
siblings         : 32
1 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-15
physical id 0: apicids 0-31
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
7. lscpu
```

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	32
On-line CPU(s) list:	0-31
Vendor ID:	AuthenticAMD
BIOS Vendor ID:	Advanced Micro Devices, Inc.
Model name:	AMD EPYC 9115 16-Core Processor
BIOS Model name:	AMD EPYC 9115 16-Core Processor
BIOS CPU family:	107
CPU family:	26
Model:	2
Thread(s) per core:	2
Core(s) per socket:	16
Socket(s):	1
Stepping:	1
Frequency boost:	enabled
CPU(s) scaling MHz:	71%
CPU max MHz:	4115.8198
CPU min MHz:	1500.0000

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECspeed®2017_fp_base = 176

SPECspeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Platform Notes (Continued)

BogoMIPS:

5199.80

Flags:

```
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2
smep bmi2 erms invpcid cq_m rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cq_m_llc cq_m_occup_llc cq_m_mbm_total
cq_m_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
xsaverptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassist pausefilter
pfthreshold avic v_vmsave_vmlload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pkru ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
```

Virtualization:

AMD-V

L1d cache:

768 KiB (16 instances)

L1i cache:

512 KiB (16 instances)

L2 cache:

16 MiB (16 instances)

L3 cache:

64 MiB (2 instances)

NUMA node(s):

2

NUMA node0 CPU(s):

0-7,16-23

NUMA node1 CPU(s):

8-15,24-31

Vulnerability Gather data sampling:

Not affected

Vulnerability Itlb multihit:

Not affected

Vulnerability L1tf:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Reg file data sampling:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec rstack overflow:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP

always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	1M	16M	16	Unified	2	1024	1	64
L3	32M	64M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-7,16-23

node 0 size: 128555 MB

node 0 free: 127781 MB

node 1 cpus: 8-15,24-31

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Platform Notes (Continued)

```
node 1 size: 128972 MB
node 1 free: 128413 MB
node distances:
node 0 1
 0: 10 12
 1: 12 10

-----
9. /proc/meminfo
MemTotal:      263708144 kB

-----
10. who -r
run-level 3 Feb 26 10:50

-----
11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.5)
Default Target  Status
multi-user      running

-----
12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        ModemManager apparmor apport blk-availability cloud-config cloud-final cloud-init
                cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
                grub-common grub-initrd-fallback keyboard-setup lvm2-monitor multipathd
                networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb
                snapd sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore
                systemd-resolved systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2
                ufw unattended-upgrades vgaauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
disabled       console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                systemd-boot-check-no-failures systemd-context systemd-network-generator
                systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                systemd-time-wait-sync upower
indirect        systemd-sysupdate systemd-sysupdate-reboot uuidd
masked         cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.8.0-54-generic
root=UUID=afd77683-fe04-45a7-a0c2-997c380461e9
ro

-----
14. cpupower frequency-info
analyzing CPU 24:
    current policy: frequency should be within 1.50 GHz and 2.60 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 2600MHz
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Platform Notes (Continued)

15. tuned-adm active

Current active profile: throughput-performance

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

17. /sys/kernel/mm/transparent_hugepage

defrag	[always]	defer defer+madvise madvise never
enabled	[always]	madvise never
hpage_pmd_size	2097152	
shmem_enabled	always	within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1
max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

From /etc/*-release /etc/*-version
os-release Ubuntu 24.04.2 LTS

20. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme0n1p2	ext4	879G	16G	818G	2%	/

21. /sys/devices/virtual/dmi/id

Vendor:	Supermicro
Product:	AS -1115CS-TNR
Product Family:	SMC H13
Serial:	FruPS12150934

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Platform Notes (Continued)

22. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

8x Micron Technology MTC20F2085S1RC56BD1 32 GB 2 rank 5600

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends International, LLC.

BIOS Version: 3.4

BIOS Date: 02/13/2025

BIOS Revision: 5.35

Compiler Version Notes

=====

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

=====

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

=====

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

=====

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECspeed®2017_fp_base = 176

SPECspeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Compiler Version Notes (Continued)

```
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

Base Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -floop
-freemap-arrays -fstrip-mining -fstruct-layout=7
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Base Optimization Flags (Continued)

C benchmarks (continued):

```
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdaloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIB -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdaloc
-lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIB -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdaloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIB -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Base Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIB -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECspeed®2017_fp_base = 176

SPECspeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Oct-2024

Tested by: Supermicro

Software Availability: Feb-2025

Peak Optimization Flags (Continued)

638.imagick_s: Same as 619.lbm_s

```
644.nab_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

```
627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -Mrecursive
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

CloudDC A+ Server AS -1115CS-TNR
(H13SSW , AMD EPYC 9115)

SPECSpeed®2017_fp_base = 176

SPECSpeed®2017_fp_peak = 181

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2025

Hardware Availability: Oct-2024

Software Availability: Feb-2025

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Turin-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Turin-revD.xml>

SPEC CPU and SPECSpeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-02-26 17:55:30-0500.

Report generated on 2025-03-26 10:33:29 by CPU2017 PDF formatter v6716.

Originally published on 2025-03-25.