



SPEC CPU®2017 Floating Point Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen11

(2.70 GHz, Intel Xeon 6353P)

SPECspeed®2017_fp_base = 86.4

SPECspeed®2017_fp_peak = 86.4

CPU2017 License: 3

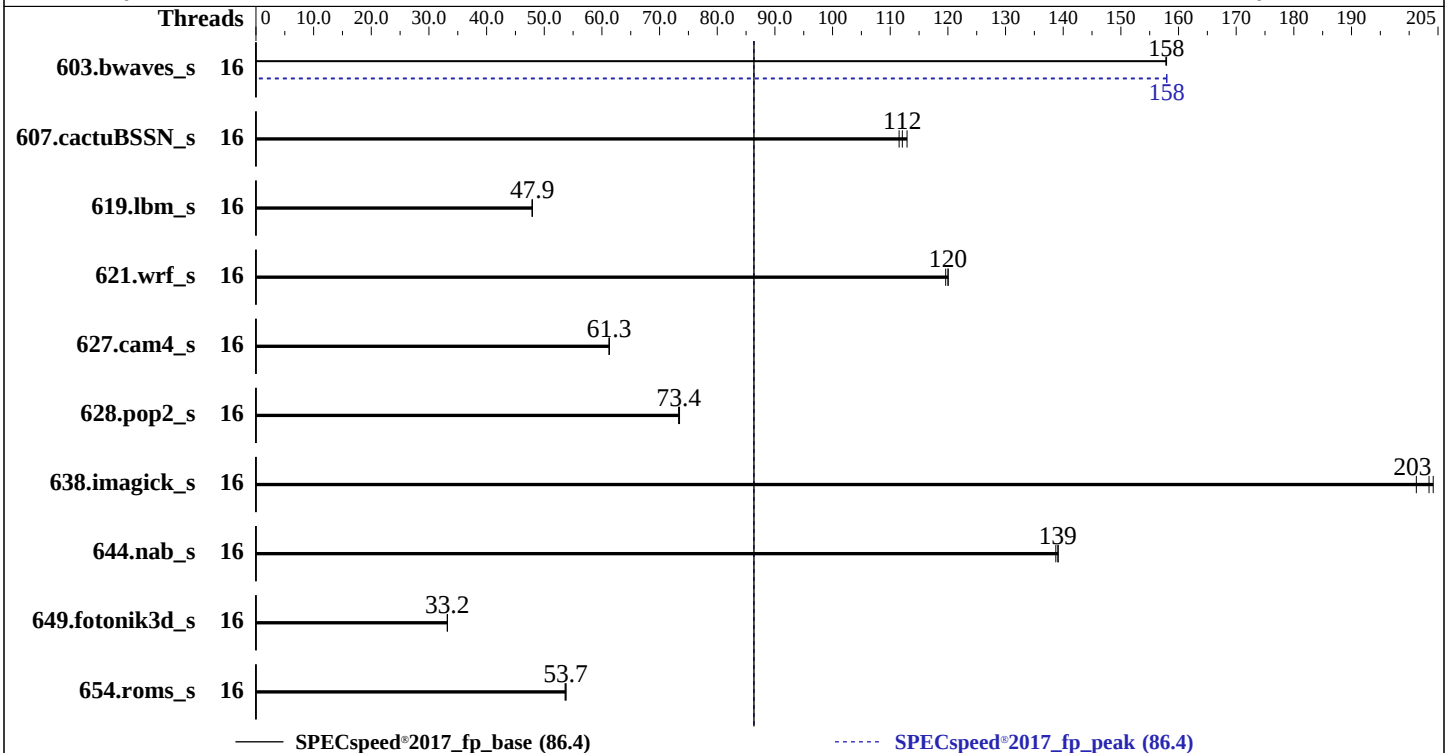
Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6353P
Max MHz: 5400
Nominal: 2700
Enabled: 8 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 24 MB I+D on chip per chip
Other: None
Memory: 64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400 , orderable using HPE part# P64339-B21)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
Kernel 6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: HPE BIOS Version v2.10 12/06/2024 released Dec-2024
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	374	158	374	158	374	158	16	373	158	374	158	374	158
607.cactuBSSN_s	16	149	112	149	112	148	113	16	149	112	149	112	148	113
619.lbm_s	16	109	47.9	109	47.9	109	47.9	16	109	47.9	109	47.9	109	47.9
621.wrf_s	16	110	120	110	120	111	120	16	110	120	110	120	111	120
627.cam4_s	16	145	61.3	145	61.2	145	61.3	16	145	61.3	145	61.2	145	61.3
628.pop2_s	16	162	73.4	162	73.3	162	73.5	16	162	73.4	162	73.3	162	73.5
638.imagick_s	16	70.9	203	71.7	201	70.7	204	16	70.9	203	71.7	201	70.7	204
644.nab_s	16	126	139	126	139	126	139	16	126	139	126	139	126	139
649.fotonik3d_s	16	275	33.2	275	33.2	275	33.2	16	275	33.2	275	33.2	275	33.2
654.roms_s	16	293	53.7	293	53.8	294	53.6	16	293	53.7	293	53.8	294	53.6

SPECspeed®2017_fp_base = **86.4**

SPECspeed®2017_fp_peak = **86.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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Platform Notes

BIOS Configuration:

Workload Profile set to General Peak Frequency Compute

Thermal Configuration set to Maximum Cooling

Enhanced Processor Performance Profile set to Enabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Tue Feb 18 21:13:44 2025

SUT (System Under Test) info as seen by some common utilities.

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15. sysctl
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19. Disk information
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1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
21:13:44 up 3 min, 3 users, load average: 0.01, 0.01, 0.00
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 256709
max locked memory (kbytes, -l) 8192

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Platform Notes (Continued)

max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 256709
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/fpspeed.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2024.1-lin-core-avx2-speed-20240308.cfg --define cores=16 --tune base,peak -o all --define drop_caches
fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2024.1-lin-core-avx2-speed-20240308.cfg --define cores=16 --tune base,peak --output_format all --define
drop_caches --nopower --runmode speed --tune base:peak --size refspeak fpspeed --nopreenv --note-preenv
--logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6353P
vendor_id       : GenuineIntel
cpu family      : 6
model           : 183
stepping        : 1
microcode       : 0x12c
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb bhi
cpu cores       : 8
siblings        : 16
1 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 0: apicids 0-15
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

7. lscpu

From lscpu from util-linux 2.39.3:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	46 bits physical, 48 bits virtual
Byte Order:	Little Endian
CPU(s):	16
On-line CPU(s) list:	0-15
Vendor ID:	GenuineIntel
BIOS Vendor ID:	Intel(R) Corporation
Model name:	Intel(R) Xeon(R) 6353P
BIOS Model name:	Intel(R) Xeon(R) 6353P To Be Filled By O.E.M. CPU @ 2.7GHz

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Platform Notes (Continued)

BIOS CPU family: 179
CPU family: 6
Model: 183
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 1
Stepping: 1
BogoMIPS: 5376.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect user_shstk avx_vnni dtherm ida arat pln pts hfi vnmi umip pku ospke waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize pconfig arch_lbr ibt flush_l1d arch_capabilities
Virtualization: VT-x
L1d cache: 384 KiB (8 instances)
L1i cache: 256 KiB (8 instances)
L2 cache: 16 MiB (8 instances)
L3 cache: 24 MiB (1 instance)
NUMA node(s): 1
NUMA node0 CPU(s): 0-15
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSE-eIBRS SW sequence; BHI BHI_DIS_S
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	384K	12	Data	1	64	1	64
L1i	32K	256K	8	Instruction	1	64	1	64
L2	2M	16M	16	Unified	2	2048	1	64
L3	24M	24M	12	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)
node 0 cpus: 0-15
node 0 size: 64202 MB
node 0 free: 63564 MB
node distances:

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Platform Notes (Continued)

node 0
0: 10

9. /proc/meminfo
MemTotal: 65743156 kB

10. who -r
run-level 3 Feb 18 21:11

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron getty@ irqbalance issue-generator kbdsettings lvm2-monitor postfix purge-kernels rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell grub2-once haveged hwloc-dump-hwdata issue-add-ssh-keys kexec-load lunmask rpmconfigcheck serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd
indirect	systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=1601d4e3-aed0-470e-8bd5-82680fe83224
splash=silent
resume=/dev/disk/by-uuid/e928ac5a-0714-4827-a56e-3fd30f6224d5
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 1:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes

15. sysctl

kernel.numa_balancing	0
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500

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Platform Notes (Continued)

```
vm.min_unmapped_ratio      1
vm.nr_hugepages             0
vm.nr_hugepages_mempolicy   0
vm.nr_overcommit_hugepages  0
vm.swappiness               60
vm.watermark_boost_factor   15000
vm.watermark_scale_factor   10
vm.zone_reclaim_mode        0
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size  2097152
shmem_enabled    always within_size advise [never] deny force
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem    Type  Size  Used Avail Use% Mounted on
/dev/sdb4     xfs   342G   66G  276G  20% /home
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     ProLiant ML30 Gen11
Product Family: ProLiant
Serial:      LXVBT01BVHZ03N
```

```
-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  2x Hynix HMC688AGBEA084N 32 GB 2 rank 5600, configured at 4400
```

```
-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      HPE
BIOS Version:     2.10
BIOS Date:        12/06/2024
```

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Platform Notes (Continued)

BIOS Revision: 2.10

Firmware Revision: 1.62

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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=====

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

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=====

Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



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Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc



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Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -w -m64 -wl, -z, muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast
-ffast-math -flt0 -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML30 Gen11

(2.70 GHz, Intel Xeon 6353P)

SPECspeed®2017_fp_base = 86.4

SPECspeed®2017_fp_peak = 86.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

Peak Optimization Flags (Continued)

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-CatlowRefresh-rev1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-CatlowRefresh-rev1.0.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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