



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR675 V3  
(2.60 GHz, AMD EPYC 9655)

SPECrate®2017\_fp\_base = 1960

SPECrate®2017\_fp\_peak = 1960

CPU2017 License: 9017

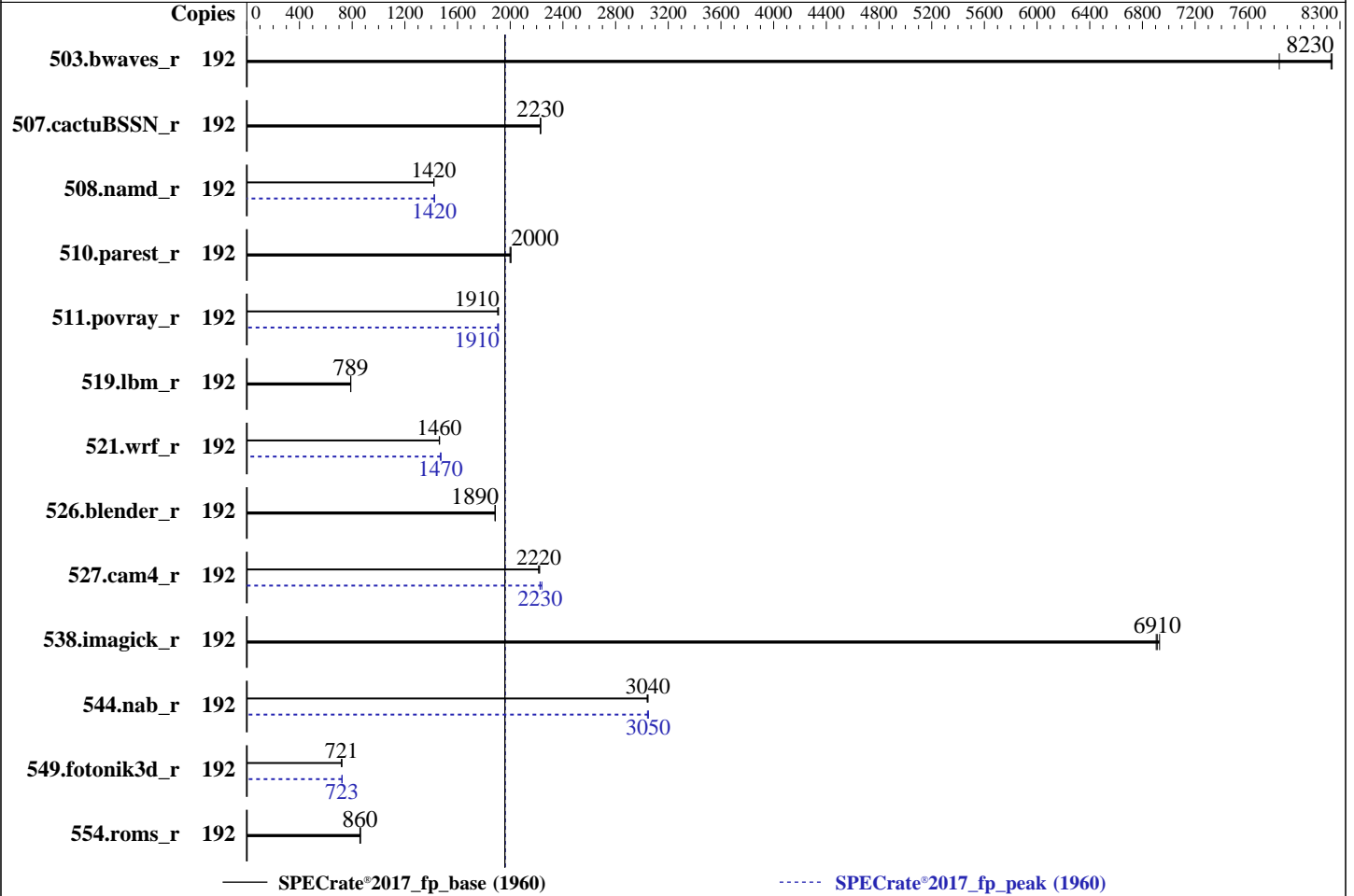
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Sep-2024

Hardware Availability: Nov-2024

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9655  
 Max MHz: 4500  
 Nominal: 2600  
 Enabled: 192 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 384 MB I+D on chip per chip,  
 32 MB shared / 8 cores  
 Other: None  
 Memory: 768 GB (24 x 32 GB 2Rx8 PC5-6400B-R, running at 6000)  
 Storage: 1 x 1.92 TB M.2 NVME SSD  
 Other: CPU Cooling: Air

### Software

OS: SUSE Linux Enterprise Server 15 SP6  
 Kernel 6.4.0-150600.21-default  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Lenovo BIOS Version QGE131M 6.10 released Aug-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

| Benchmark       | Base   |            |             |             |             |             |             | Peak   |             |             |            |             |             |             |
|-----------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|------------|-------------|-------------|-------------|
|                 | Copies | Seconds    | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Copies | Seconds     | Ratio       | Seconds    | Ratio       | Seconds     | Ratio       |
| 503.bwaves_r    | 192    | 234        | 8240        | 246         | 7840        | <b>234</b>  | <b>8230</b> | 192    | 234         | 8240        | 246        | 7840        | <b>234</b>  | <b>8230</b> |
| 507.cactuBSSN_r | 192    | 109        | 2230        | 109         | 2230        | <b>109</b>  | <b>2230</b> | 192    | 109         | 2230        | 109        | 2230        | <b>109</b>  | <b>2230</b> |
| 508.namd_r      | 192    | <b>128</b> | <b>1420</b> | 128         | 1420        | 129         | 1420        | 192    | 128         | 1430        | <b>128</b> | <b>1420</b> | 128         | 1420        |
| 510.parest_r    | 192    | 251        | 2000        | <b>251</b>  | <b>2000</b> | 251         | 2000        | 192    | 251         | 2000        | <b>251</b> | <b>2000</b> | 251         | 2000        |
| 511.povray_r    | 192    | 235        | 1910        | 235         | 1900        | <b>235</b>  | <b>1910</b> | 192    | 235         | 1900        | 235        | 1910        | <b>235</b>  | <b>1910</b> |
| 519.lbm_r       | 192    | 256        | 790         | 257         | 788         | <b>257</b>  | <b>789</b>  | 192    | 256         | 790         | 257        | 788         | <b>257</b>  | <b>789</b>  |
| 521.wrf_r       | 192    | 294        | 1460        | 294         | 1460        | <b>294</b>  | <b>1460</b> | 192    | 292         | 1470        | 292        | 1470        | <b>292</b>  | <b>1470</b> |
| 526.blender_r   | 192    | <b>155</b> | <b>1890</b> | 155         | 1890        | 155         | 1890        | 192    | <b>155</b>  | <b>1890</b> | 155        | 1890        | 155         | 1890        |
| 527.cam4_r      | 192    | <b>151</b> | <b>2220</b> | 152         | 2220        | 151         | 2220        | 192    | 151         | 2230        | 150        | 2240        | <b>151</b>  | <b>2230</b> |
| 538.imagick_r   | 192    | 69.2       | 6900        | 68.9        | 6930        | <b>69.1</b> | <b>6910</b> | 192    | 69.2        | 6900        | 68.9       | 6930        | <b>69.1</b> | <b>6910</b> |
| 544.nab_r       | 192    | 106        | 3050        | 106         | 3040        | <b>106</b>  | <b>3040</b> | 192    | 106         | 3050        | 106        | 3040        | <b>106</b>  | <b>3050</b> |
| 549.fotonik3d_r | 192    | 1041       | 719         | <b>1038</b> | <b>721</b>  | 1038        | 721         | 192    | <b>1035</b> | <b>723</b>  | 1036       | 723         | 1034        | 723         |
| 554.roms_r      | 192    | 353        | 864         | 355         | 859         | <b>355</b>  | <b>860</b>  | 192    | 353         | 864         | 355        | 859         | <b>355</b>  | <b>860</b>  |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



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## Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.9-amd-aocc500_znver5_A1/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-a
occ500_znver5_A1/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

NUMA Nodes per Socket set to NPS4

SMT Mode set to Disabled

L1 Stride Prefetcher set to Disabled

Sysinfo program /home/cpu2017-1.1.9-amd-aocc500\_znver5\_A1/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Wed Sep 18 01:24:40 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info

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### Platform Notes (Continued)

- 15. sysctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
-----
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36cle09)
x86_64 x86_64 x86_64 GNU/Linux
-----
```

```
-----
2. w
01:24:40 up 4 min, 1 user, load average: 0.22, 0.11, 0.04
USER      TTY      FROM          LOGIN@      IDLE       JCPU      PCPU      WHAT
-----
```

```
-----
3. Username
From environment variable $USER: root
-----
```

```
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 3093971
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 3093971
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
-----
```

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECcpu_1.01.sh
/bin/bash ./Run026-compliant-amd-ratefp.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.005/templogs/preenv.fprate.005.0.log --lognum 005.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1
-----
```

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### Platform Notes (Continued)

6. /proc/cpuinfo

```

model name      : AMD EPYC 9655 96-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb00210e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 192 4K pages
cpu cores      : 96
siblings       : 96
2 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 1: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
physical id 1: apicids
256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 192
On-line CPU(s) list:   0-191
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:             AMD EPYC 9655 96-Core Processor
BIOS Model name:       AMD EPYC 9655 96-Core Processor
BIOS CPU family:        107
CPU family:             26
Model:                  2
Thread(s) per core:    1
Core(s) per socket:    96
Socket(s):              2
Stepping:               1
BogoMIPS:               5192.45
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                        extd_apicid aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                        cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                        osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                        perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                        ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                        smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                        xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                        nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                        pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi

```

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### Platform Notes (Continued)

```

Virtualization:
L1d cache:
L1i cache:
L2 cache:
L3 cache:
NUMA node(s):
NUMA node0 CPU(s):
NUMA node1 CPU(s):
NUMA node2 CPU(s):
NUMA node3 CPU(s):
NUMA node4 CPU(s):
NUMA node5 CPU(s):
NUMA node6 CPU(s):
NUMA node7 CPU(s):
Vulnerability Gather data sampling:
Vulnerability Itlb multihit:
Vulnerability L1tf:
Vulnerability Mds:
Vulnerability Meltdown:
Vulnerability Mmio stale data:
Vulnerability Reg file data sampling:
Vulnerability Retbleed:
Vulnerability Spec rstack overflow:
Vulnerability Spec store bypass:
Vulnerability Spectre v1:
Vulnerability Spectre v2:
Vulnerability Srbds:
Vulnerability Tsx async abort:

```

avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq  
avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect  
movdiri movdir64b overflow\_recov succor smca fsrm avx512\_vp2intersect  
flush\_llid debug\_swap  
AMD-V  
9 MiB (192 instances)  
6 MiB (192 instances)  
192 MiB (192 instances)  
768 MiB (24 instances)  
8  
0-23  
24-47  
48-71  
72-95  
96-119  
120-143  
144-167  
168-191  
Not affected  
Not affected  
Not affected  
Not affected  
Not affected  
Not affected  
Not affected  
Not affected  
Not affected  
Not affected  
Mitigation; Speculative Store Bypass disabled via prctl  
Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP  
disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected  
Not affected  
Not affected

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 9M       | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 6M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 192M     | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 768M     | 16   | Unified     | 3     | 32768 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0-23
node 0 size: 96334 MB
node 0 free: 95847 MB
node 1 cpus: 24-47
node 1 size: 96761 MB
node 1 free: 96225 MB
node 2 cpus: 48-71
node 2 size: 96761 MB
node 2 free: 96327 MB
node 3 cpus: 72-95
node 3 size: 96761 MB
node 3 free: 96339 MB
node 4 cpus: 96-119
node 4 size: 96761 MB
node 4 free: 96330 MB

```

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### Platform Notes (Continued)

```

node 5 cpus: 120-143
node 5 size: 96723 MB
node 5 free: 96258 MB
node 6 cpus: 144-167
node 6 size: 96761 MB
node 6 free: 95860 MB
node 7 cpus: 168-191
node 7 size: 96654 MB
node 7 free: 96175 MB
node distances:
node  0  1  2  3  4  5  6  7
0:  10 12 12 12 32 32 32 32
1:  12 10 12 12 32 32 32 32
2:  12 12 10 12 32 32 32 32
3:  12 12 12 10 32 32 32 32
4:  32 32 32 32 10 12 12 12
5:  32 32 32 32 12 10 12 12
6:  32 32 32 32 12 12 10 12
7:  32 32 32 32 12 12 12 10

```

```

9. /proc/meminfo
   MemTotal:      792083768 kB

```

```

10. who -r
    run-level 3 Sep 18 01:21

```

```

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
    Default Target   Status
    multi-user       running

```

```

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-autoconnect postfix
purge-kernels rollback rsyslog sapconf smartd sshd sysctl-logger systemd-pstore wicked
wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny

enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd issue-add-ssh-keys
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd
serial-getty@ smartd-generate_opts snmpd snmptrapd sysstat systemd-boot-check-no-failures
systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync
systemd-timesyncd

generated ntp_sync
indirect  systemd-userdbd uidd wickedd

```

```

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=240b751b-cc96-4bc0-bc79-208919660c0a
splash=silent
mitigations=auto
quiet
security=apparmor

```

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### Platform Notes (Continued)

```

-----
14. cpupower frequency-info
analyzing CPU 145:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
-----

15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
-----

16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer+madvise madvise never
enabled         [always] madvise never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force
-----

17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
-----

18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
-----

19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1
Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/nvme1nlp3 xfs   1.8T  111G  1.7T   7% /
-----

20. /sys/devices/virtual/dmi/id

```

(Continued on next page)





# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

SPECrate®2017\_fp\_base = 1960

ThinkSystem SR675 V3  
(2.60 GHz, AMD EPYC 9655)

SPECrate®2017\_fp\_peak = 1960

CPU2017 License: 9017

Test Date: Sep-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2024

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

### Platform Notes (Continued)

Vendor: Lenovo  
Product: ThinkSystem SR675 V3 System Board  
Product Family: ThinkSystem  
Serial: None

#### 21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

##### Memory:

1x SK Hynix HMC88AHB8A284N 32 GB 2 rank 6400, configured at 6000  
16x SK Hynix HMC88AHB8A290N 32 GB 2 rank 6400, configured at 6000  
3x SK Hynix HMC88AHB8A292N 32 GB 2 rank 6400, configured at 6000  
3x SK Hynix HMC88AHB8A471N 32 GB 2 rank 6400, configured at 6000  
1x SK Hynix HMC88AHB8A478N 32 GB 2 rank 6400, configured at 6000

#### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: QGE131M-6.10  
BIOS Date: 08/11/2024  
BIOS Revision: 6.10  
Firmware Revision: 8.10

### Compiler Version Notes

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

(Continued on next page)



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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Sep-2024  
**Hardware Availability:** Nov-2024  
**Software Availability:** Oct-2024

### Compiler Version Notes (Continued)

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

=====  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
=====

### Base Compiler Invocation

C benchmarks:  
clang

C++ benchmarks:  
clang++

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

(Continued on next page)



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Lenovo Global Technology

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SPECrate®2017\_fp\_peak = 1960

CPU2017 License: 9017

Test Date: Sep-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Nov-2024

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

## Base Compiler Invocation (Continued)

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64  
 507.cactuBSSN\_r: -DSPEC\_LP64  
 508.namd\_r: -DSPEC\_LP64  
 510.parest\_r: -DSPEC\_LP64  
 511.povray\_r: -DSPEC\_LP64  
 519.lbm\_r: -DSPEC\_LP64  
 521.wrf\_r: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
 526.blender\_r: -funsigned-char -DSPEC\_LP64  
 527.cam4\_r: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
 538.imagick\_r: -DSPEC\_LP64  
 544.nab\_r: -DSPEC\_LP64  
 549.fotonik3d\_r: -DSPEC\_LP64  
 554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
 -Wl,-mllvm -Wl,-reduce-array-computations=3  
 -Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3  
 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
 -fstruct-layout=7 -mllvm -unroll-threshold=50  
 -mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
 -mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
 -lflang -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
 -Wl,-mllvm -Wl,-reduce-array-computations=3  
 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
 -O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
 -mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000

(Continued on next page)



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**Hardware Availability:** Nov-2024

**Software Availability:** Oct-2024

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc  
-lflang -ldl
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc  
-lflang -ldl
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-aggressive-gather=true  
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops  
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions  
-lamdlibm -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining  
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100  
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang  
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner  
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie  
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
```

(Continued on next page)



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**Software Availability:** Oct-2024

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```

Benchmarks using both Fortran and C:

```
flang clang
```

Benchmarks using both C and C++:

```
clang++ clang
```

(Continued on next page)



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Software Availability: Oct-2024

## Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

519.lbm\_r: basepeak = yes

538.imagick\_r: basepeak = yes

544.nab\_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand  
-fenable-aggressive-gather -Ofast -march=znver5  
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7  
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

C++ benchmarks:

508.namd\_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-mllvm -unroll-threshold=100  
-mllvm -reduce-array-computations=3 -zopt -lamdlibm  
-lamdalloc -ldl

510.parest\_r: basepeak = yes

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

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## Peak Optimization Flags (Continued)

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

554.roms\_r: basepeak = yes

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -freemap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm
-lamdalloc -ldl
```

(Continued on next page)



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## Peak Optimization Flags (Continued)

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-A.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-A.xml>

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