



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge C6615 (AMD EPYC 8224P 24-Core Processor)

SPECSpeed®2017_int_base = 10.8

SPECSpeed®2017_int_peak = 11.0

CPU2017 License: 6573

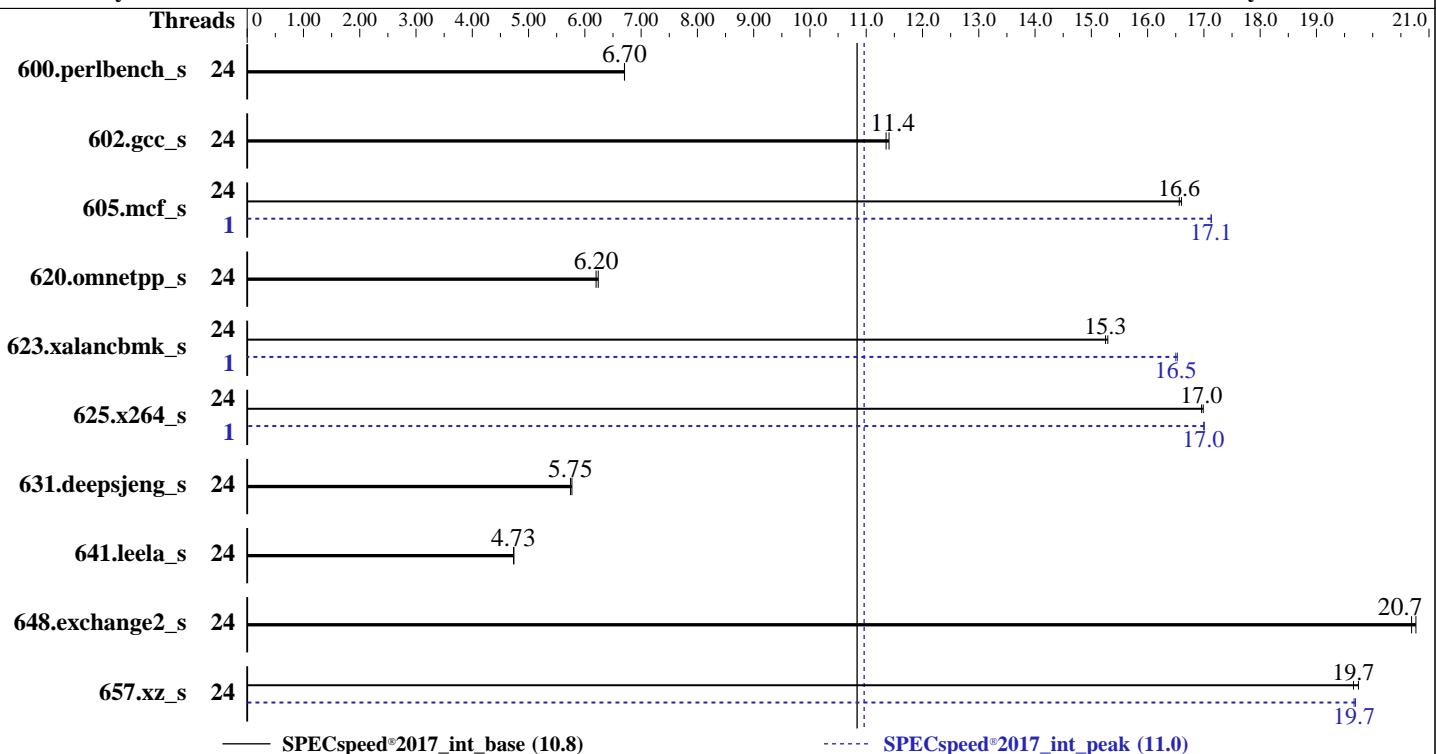
Test Date: Nov-2023

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2023

Tested by: Dell Inc.

Software Availability: Nov-2022



— SPECSpeed®2017_int_base (10.8)

- - - SPECSpeed®2017_int_peak (11.0)

Hardware

CPU Name: AMD EPYC 8224P
Max MHz: 3000
Nominal: 2550
Enabled: 24 cores, 1 chip
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 64 MB I+D on chip per chip, 16 MB shared / 6 cores
Other: None
Memory: 192 GB (6 x 32 GB 2Rx8 PC5-4800B-R)
Storage: 40 GB on tmpfs
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default
Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
Parallel: Yes
Firmware: Version 1.1.2 released Oct-2023
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	24	265	6.71	265	6.70			24	265	6.71	265	6.70		
602.gcc_s	24	351	11.4	349	11.4			24	351	11.4	349	11.4		
605.mcf_s	24	285	16.6	284	16.6			1	276	17.1	276	17.1		
620.omnetpp_s	24	261	6.24	263	6.20			24	261	6.24	263	6.20		
623.xalancbmk_s	24	92.9	15.3	92.7	15.3			1	85.9	16.5	85.7	16.5		
625.x264_s	24	104	17.0	104	17.0			1	104	17.0	104	17.0		
631.deepsjeng_s	24	249	5.75	248	5.77			24	249	5.75	248	5.77		
641.leela_s	24	361	4.73	360	4.74			24	361	4.73	360	4.74		
648.exchange2_s	24	142	20.7	142	20.8			24	142	20.7	142	20.8		
657.xz_s	24	314	19.7	313	19.7			24	314	19.7	314	19.7		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-23"  
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1/amd_speed_aocc400_znver4_A_lib/lib:  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "oversize_threshold:0,retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "24"
```

Environment variables set by runcpu during the 605.mcf_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 625.x264_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 657.xz_s peak run:

```
GOMP_CPU_AFFINITY = "0-23"
```

```
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "8"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

```
    DRAM Refresh Delay : Performance  
    DIMM Self Healing on  
    Uncorrectable Memory Error : Disabled  
  
    Logical Processor : Disabled  
    Virtualization Technology : Disabled  
    NUMA Nodes per Socket : 4  
  
    System Profile : Custom  
        C-States : Disabled  
    Memory Patrol Scrub : Disabled  
    PCI ASPM L1 Link  
        Power Management : Disabled  
        Determinism Slider : Power Determinism  
    Algorithm Performance  
    Boost Disable (ApbDis) : Enabled
```

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Platform Notes (Continued)

```
Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Nov  8 18:33:36 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

2. w
18:33:36 up 5 min, 1 user, load average: 0.81, 2.10, 1.18
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root ttym1 - 18:32 32.00s 2.03s 0.30s /bin/bash ./amd_speed_aocc400_znver4_A1.sh

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 771851
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024

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Platform Notes (Continued)

```
pipe size          (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority      (-r) 0
stack size            (kbytes, -s) unlimited
cpu time              (seconds, -t) unlimited
max user processes     (-u) 771851
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd linux --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./DELL_speed.sh
/bin/bash ./dell-run-main.sh speed
/bin/bash ./dell-run-main.sh speed
/bin/bash ./dell-run-speccpu.sh speed --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-VERS=v4.8.1
--output_format html,pdf,txt
python3 ./run_amd_speed_aocc400_znver4_A1.py
/bin/bash ./amd_speed_aocc400_znver4_A1.sh
runcpu --config amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-VERS=v4.8.1 --output_format
html,pdf,txt intspeed
runcpu --configfile amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=4 --define DL-BIOSinc=Dell-BIOS_EPYC-4.inc --define DL-VERS=v4.8.1 --output_format
html,pdf,txt --nopower --runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv
--note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1

-----
6. /proc/cpuinfo
model name      : AMD EPYC 8224P 24-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 160
stepping        : 2
microcode       : 0xaa00212
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores      : 24
siblings        : 24
1 physical ids (chips)
24 processors (hardware threads)
physical id 0: core ids 0-5,16-21,32-37,48-53
physical id 0: apicids 0-5,16-21,32-37,48-53
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----
7. lscpu

From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                24
```

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Platform Notes (Continued)

On-line CPU(s) list:

0-23

Vendor ID:

AuthenticAMD

Model name:

AMD EPYC 8224P 24-Core Processor

CPU family:

25

Model:

160

Thread(s) per core:

1

Core(s) per socket:

24

Socket(s):

1

Stepping:

2

Frequency boost:

enabled

CPU max MHz:

3008.2029

CPU min MHz:

1500.0000

BogoMIPS:

5092.06

Flags:

```
fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext
perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3
invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmil
avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbvl xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
umip pkru ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
AMD-V
```

Virtualization:

768 KiB (24 instances)

L1d cache:

768 KiB (24 instances)

L1i cache:

24 MiB (24 instances)

L2 cache:

64 MiB (4 instances)

NUMA node(s):

4

NUMA node0 CPU(s):

0-5

NUMA node1 CPU(s):

6-11

NUMA node2 CPU(s):

12-17

NUMA node3 CPU(s):

18-23

Vulnerability Itlb multihit:

Not affected

Vulnerability Llrf:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB filling

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	768K	8	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	8	Unified	2	2048	1	64
L3	16M	64M	16	Unified	3	16384	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

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Platform Notes (Continued)

```
available: 4 nodes (0-3)
node 0 cpus: 0-5
node 0 size: 47928 MB
node 0 free: 47406 MB
node 1 cpus: 6-11
node 1 size: 48382 MB
node 1 free: 48163 MB
node 2 cpus: 12-17
node 2 size: 48347 MB
node 2 free: 44596 MB
node 3 cpus: 18-23
node 3 size: 48327 MB
node 3 free: 48145 MB
node distances:
node   0   1   2   3
 0: 10 11 11 11
 1: 11 10 11 11
 2: 11 11 10 11
 3: 11 11 11 10

-----
9. /proc/meminfo
MemTotal:      197618484 kB

-----
10. who -r
run-level 3 Nov 8 18:31

-----
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
Default Target  Status
multi-user      running

-----
12. Services, from systemctl list-unit-files
STATE          UNIT FILES
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager firewalld getty@ 
                haveged irqbalance issue-generator kbdsettings klog lvm2-monitor nsqd
                nvmefc-boot-connections postfix purge-kernels rollback rsyslog smartd sshd tuned wicked
                wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info gpm
                grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmievfd issue-add-ssh-keys
                kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nvmf-autoconnect rdisc rpcbind
                rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                systemd-time-wait-sync systemd-timesyncd udisks2
indirect        wickedd

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=6a4e7333-2073-4051-b4eb-73e27c7c689c
linux
splash=silent
mitigations=auto
quiet
security=apparmor
```

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Platform Notes (Continued)

```
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 1.50 GHz and 2.55 GHz.  
        The governor "performance" may decide which speed to use  
        within this range.  
    boost state support:  
        Supported: yes  
        Active: yes
```

```
-----  
15. tuned-adm active  
Current active profile: throughput-performance
```

```
-----  
16. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space       0  
vm.compaction_proactiveness    20  
vm.dirty_background_bytes       0  
vm.dirty_background_ratio      10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs     3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs   500  
vm.dirtytime_expire_seconds    43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages     0  
vm.swappiness                  1  
vm.watermark_boost_factor      15000  
vm.watermark_scale_factor       10  
vm.zone_reclaim_mode           1
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage  
defrag           [always] defer defer+madvise madvise never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled    always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs  60000  
defrag               1  
max_ptes_none        511  
max_ptes_shared      256  
max_ptes_swap        64  
pages_to_scan         4096  
scan_sleep_millisecs 10000
```

```
-----  
19. OS release  
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP4
```

```
-----  
20. Disk information
```

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Platform Notes (Continued)

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc400-znver4-A1.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 40G 3.4G 37G 9% /mnt/ramdisk

21. /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge C6615
Product Family: PowerEdge
Serial: SC665C6

22. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
3x 80AD000080AD HMCG88MEBRA107N 32 GB 2 rank 4800
3x 80AD000080AD HMCG88MEBRA113N 32 GB 2 rank 4800

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.2
BIOS Date: 10/16/2023
BIOS Revision: 1.1

Compiler Version Notes

=====
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====
Fortran | 648.exchange2_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

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SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

PowerEdge C6615 (AMD EPYC 8224P 24-Core Processor)

CPU2017 License: 6573

Test Date: Nov-2023

Test Sponsor: Dell Inc.

Hardware Availability: Dec-2023

Tested by: Dell Inc.

Software Availability: Nov-2022

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -futo -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdalloc

C++ benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -futo

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Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -unroll-threshold=100 -finline-aggressive  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt  
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp  
-lomp -lamdlibm -flang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop  
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM  
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp  
-lomp -lamdlibm -flang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: basepeak = yes

602.gcc_s: basepeak = yes

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang

625.x264_s: Same as 605.mcf_s

657.xz_s: Same as 605.mcf_s

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdaloc-ext -lflang

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

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Peak Optimization Flags (Continued)

648.exchange2_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/aocc400-flags_A1.1.html

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.1.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/aocc400-flags_A1.1.xml

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.1.xml>

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