



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECSpeed®2017_int_base = 10.8

SPECSpeed®2017_int_peak = 11.0

CPU2017 License: 001176

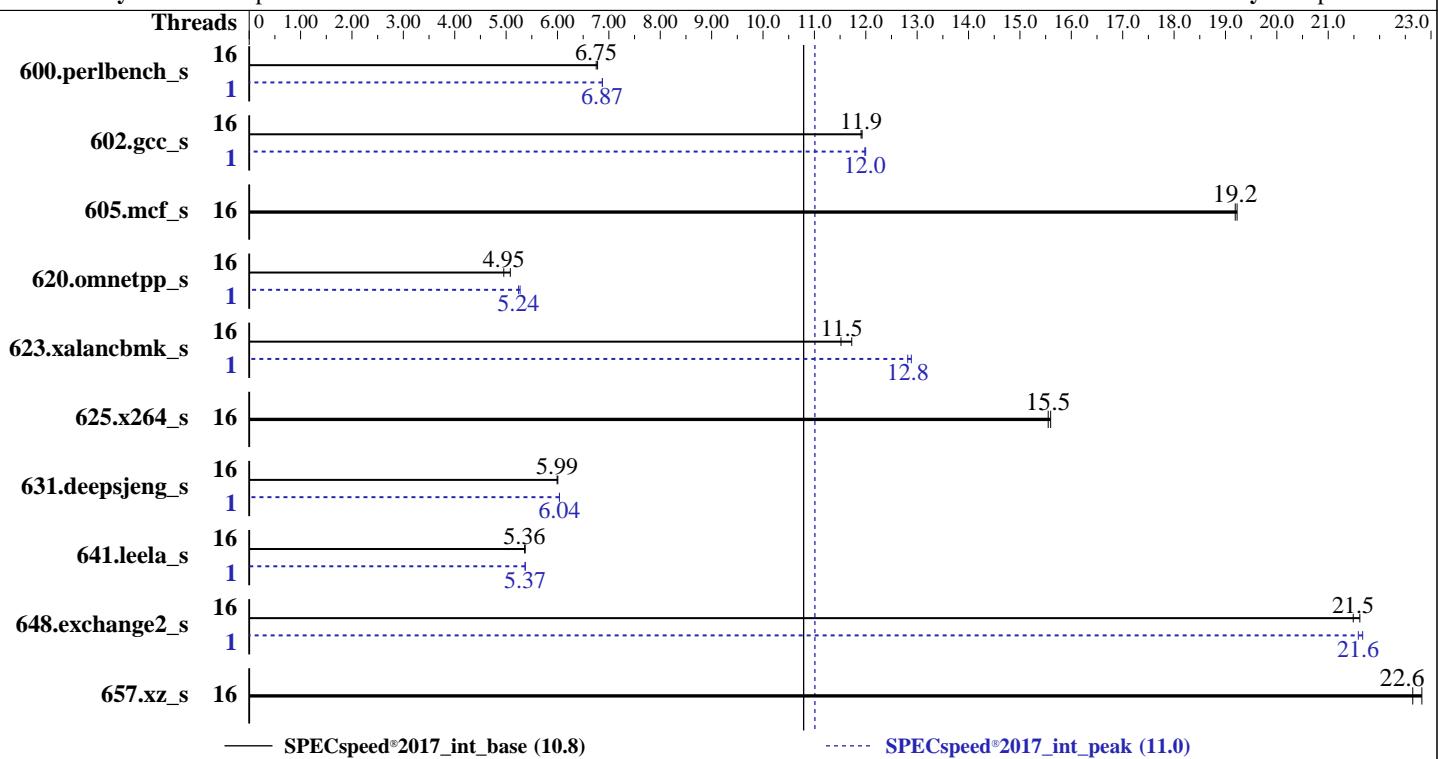
Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023



Hardware

CPU Name: AMD EPYC 7203
Max MHz: 3400
Nominal: 2800
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 512 KB I+D on chip per core
L3: 64 MB I+D on chip per chip, 16 MB shared / 2 cores
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 240 GB SATA III SSD
Other: None

Software

OS: Ubuntu 22.04.3 LTS
Compiler: Kernel 5.15.0-86-generic
Parallel: C/C++/Fortran: Version 3.2.0 of AOCC
Firmware: Yes
File System: Version 2.6 released Apr-2023
System State: ext4
Base Pointers: Run level 5 (multi-user)
Peak Pointers: 64-bit
Other: 64-bit
Power Management: jemalloc: jemalloc memory allocator library v5.1.0
BIOS and OS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	262	6.78	263	6.75			1	258	6.87	258	6.87				
602.gcc_s	16	334	11.9	334	11.9			1	332	12.0	332	12.0				
605.mcf_s	16	246	19.2	246	19.2			16	246	19.2	246	19.2				
620.omnetpp_s	16	329	4.95	321	5.08			1	311	5.24	309	5.27				
623.xalancbmk_s	16	123	11.5	121	11.7			1	111	12.8	110	12.9				
625.x264_s	16	113	15.6	113	15.5			16	113	15.6	113	15.5				
631.deepsjeng_s	16	239	6.01	239	5.99			1	237	6.04	237	6.04				
641.leela_s	16	318	5.37	318	5.36			1	318	5.37	317	5.37				
648.exchange2_s	16	136	21.6	137	21.5			1	136	21.7	136	21.6				
657.xz_s	16	273	22.6	271	22.8			16	273	22.6	271	22.8				
SPECspeed®2017_int_base = 10.8																
SPECspeed®2017_int_peak = 11.0																

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

Test Date: Oct-2023

Hardware Availability: Sep-2023

Software Availability: Sep-2023

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-31"  
LD_LIBRARY_PATH =  
    "/home/cpu2017/amd_speed_aocc320_milanx_A/lib;/home/cpu2017/amd_speed_aocc320_milanx_A/lib/lib32:  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "32"
```

Environment variables set by runcpu during the 600.perlbench_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 602.gcc_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 620.omnetpp_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 631.deepsjeng_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 641.leela_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

Environment variables set by runcpu during the 648.exchange2_s peak run:

```
GOMP_CPU_AFFINITY = "0"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>
spec.cpu2017.notes_plat_000: BIOS Settings:

Platform Notes

```
Determinism Control = Manual  
Determinism Slider = Power  
cTDP Control = Manual  
cTDP = 150  
Package Power Limit Control = Manual  
Package Power Limit = 150  
APBDIS = 1
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

Test Date: Oct-2023

Hardware Availability: Sep-2023

Software Availability: Sep-2023

Platform Notes (Continued)

NUMA Nodes Per Socket = NPS2

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on as-2024s-tr-7203 Mon Oct 23 05:37:44 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.10)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux as-2024s-tr-7203 5.15.0-86-generic #96-Ubuntu SMP Wed Sep 20 08:23:49 UTC 2023 x86_64 x86_64 x86_64
GNU/Linux

2. w
05:37:44 up 1 min, 2 users, load average: 0.69, 0.48, 0.19

USER	TTY	FROM	LOGIN@	IDLE	JCPU	PCPU	WHAT
lab	tty1	-	05:37	40.00s	0.12s	0.01s	-bash
lab	pts/0	-	05:37	6.00s	1.25s	0.09s	sudo su -

3. Username
From environment variable \$USER: root
From the command 'logname': lab

4. ulimit -a
time(seconds) unlimited
file(blocks) unlimited
data(kbytes) unlimited
stack(kbytes) unlimited
coredump(blocks) 0
memory(kbytes) unlimited

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Platform Notes (Continued)

```
locked memory(kbytes) 2097152
process 4127222
nofiles 1024
vmemory(kbytes) unlimited
locks unlimited
rtprio 0
```

```
-----5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
sudo su -
sudo su -
su -
-bash
python3 ./run_amd_speed_aocc320_milanx_A1.py
/bin/bash ./amd_speed_aocc320_milanx_A1.sh
runcpu --config amd_speed_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 intspeed
runcpu --configfile amd_speed_aocc320_milanx_A1.cfg --tune all --reportable --iterations 2 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

```
-----6. /proc/cpuinfo
model name : AMD EPYC 7203 8-Core Processor
vendor_id : AuthenticAMD
cpu family : 25
model : 1
stepping : 1
microcode : 0xa0011d1
bugs : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size : 2560 4K pages
cpu cores : 8
siblings : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 16-31
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----7. lscpu
```

```
From lscpu from util-linux 2.37.2:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 48 bits physical, 48 bits virtual
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Vendor ID: AuthenticAMD
Model name: AMD EPYC 7203 8-Core Processor
CPU family: 25
Model: 1
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

CPU2017 License: 001176

Test Sponsor: Supermicro

Tested by: Supermicro

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

Test Date: Oct-2023

Hardware Availability: Sep-2023

Software Availability: Sep-2023

Platform Notes (Continued)

```

Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
Stepping: 1
Frequency boost: enabled
CPU max MHz: 3431.6399
CPU min MHz: 1500.0000
BogoMIPS: 5600.49
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat pse36
       clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
       lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf
       rapl pnpi pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe
       popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic
       cr8_legacy abm sse4a misalignsse 3dnnowprefetch osvw ibs skinit wdt tce
       topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13
       cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall
       fsgsbase bmil avx2 smep bmi2 erms invpcid cqmq rdt_a rdseed adx smap
       clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqmq_llc
       cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local clzero irperf xsaveerptr
       rdpru wbnoinvd amd_ppin arat npt lbrv svm_lock nrip_save tsc_scale
       vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
       v_vmsave_vmlload vgif v_spec_ctrl umip pku ospke vaes vpclmulqdq rdpid
       overflow_recov succor smca fsrm

Virtualization: AMD-V
L1d cache: 512 KiB (16 instances)
L1i cache: 512 KiB (16 instances)
L2 cache: 8 MiB (16 instances)
L3 cache: 128 MiB (8 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-3,16-19
NUMA node1 CPU(s): 4-7,20-23
NUMA node2 CPU(s): 8-11,24-27
NUMA node3 CPU(s): 12-15,28-31
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; safe RET
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling, PBRSB-eIBRS Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	512K	8	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	512K	8M	8	Unified	2	1024	1	64
L3	16M	128M	16	Unified	3	16384	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-3,16-19

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Platform Notes (Continued)

```
node 0 size: 257844 MB
node 0 free: 257374 MB
node 1 cpus: 4-7,20-23
node 1 size: 258032 MB
node 1 free: 257595 MB
node 2 cpus: 8-11,24-27
node 2 size: 258044 MB
node 2 free: 257680 MB
node 3 cpus: 12-15,28-31
node 3 size: 257996 MB
node 3 free: 257508 MB
node distances:
node 0 1 2 3
 0: 10 12 32 32
 1: 12 10 32 32
 2: 32 32 10 12
 3: 32 32 12 10

-----
9. /proc/meminfo
MemTotal: 1056684836 kB

-----
10. who -r
run-level 5 Oct 23 05:36

-----
11. Systemd service manager version: systemd 249 (249.11-0ubuntu3.10)
Default Target Status
graphical running

-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor blk-availability cloud-config cloud-final cloud-init
           cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
           grub-common grub-initrd-fallback irqbalance keyboard-setup lm-sensors lvm2-monitor
           lxd-agent multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog
           secureboot-db setvtrgb ssh systemd-networkd systemd-networkd-wait-online systemd-pstore
           systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw
           vgauth
enabled-runtime netplan-ovs-cleanupsystemd-fsck-root systemd-remount-fs
disabled      console-getty debug-shell iscsid nftables rsync serial-getty@
               systemd-boot-check-no-failures systemd-network-generator systemd-sysext
               systemd-time-wait-sync upower
generated     apport
indirect       uidd
masked        cryptdisks cryptdisks-early hwclock lvm2 multipath-tools-boot rc rcS screen-cleanup sudo
               x11-common

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.15.0-86-generic
root=UUID=d06a341c-7b0d-4f57-aec5-11bd41a1c57a
ro

-----
14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 1.50 GHz and 2.80 GHz.
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Platform Notes (Continued)

The governor "performance" may decide which speed to use within this range.

boost state support:

Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2800MHz

15. sysctl
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1

16. /sys/kernel/mm/transparent_hugepage
defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

18. OS release
From /etc/*-release /etc/*-version
os-release Ubuntu 22.04.3 LTS

19. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 218G 16G 191G 8% /

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: AS-2024S-TR
Serial: WMF21BS602916

21. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: American Megatrends Inc.
BIOS Version: 2.6
BIOS Date: 04/13/2023
BIOS Revision: 5.22

Compiler Version Notes

=====

C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

=====

C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====

=====

Fortran | 648.exchange2_s(base, peak)

=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

C++ benchmarks:

-m64 -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100
-finline-aggressive -flv-function-specialization
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp=true
-mllvm -convert-pow-exp-to-int=false -z muldefs
-fvirtual-function-elimination -fvisibility=hidden -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -flang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -ftlo -z muldefs
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -flang
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

C++ benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:

```
-Wno-return-type
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

602.gcc_s: Same as 600.perlbench_s

605.mcf_s: basepeak = yes

625.x264_s: basepeak = yes

657.xz_s: basepeak = yes

C++ benchmarks:

```
620.omnetpp_s: -m64 -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-fvirtual-function-elimination -fvisibility=hidden
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Peak Optimization Flags (Continued)

```
623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

```
631.deepsjeng_s: -m64 -Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -convert-pow-exp-to-int=false
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp=libomp -lomp
-lamdlibm -ljemalloc -lflang
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Supermicro

Mainstream A+ Server AS -2024S-TR
(H12DSi-N6 , AMD EPYC 7203)

SPECspeed®2017_int_base = 10.8

SPECspeed®2017_int_peak = 11.0

CPU2017 License: 001176

Test Date: Oct-2023

Test Sponsor: Supermicro

Hardware Availability: Sep-2023

Tested by: Supermicro

Software Availability: Sep-2023

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

C++ benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revH.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-10-23 01:37:43-0400.

Report generated on 2023-11-07 18:44:28 by CPU2017 PDF formatter v6716.

Originally published on 2023-11-07.