



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Supermicro

UP SuperServer SYS-521C-NR  
(X13SEDW-F, Intel Xeon Platinum 8480+)

SPECSpeed®2017\_int\_base = 14.2

SPECSpeed®2017\_int\_peak = 14.4

CPU2017 License: 001176

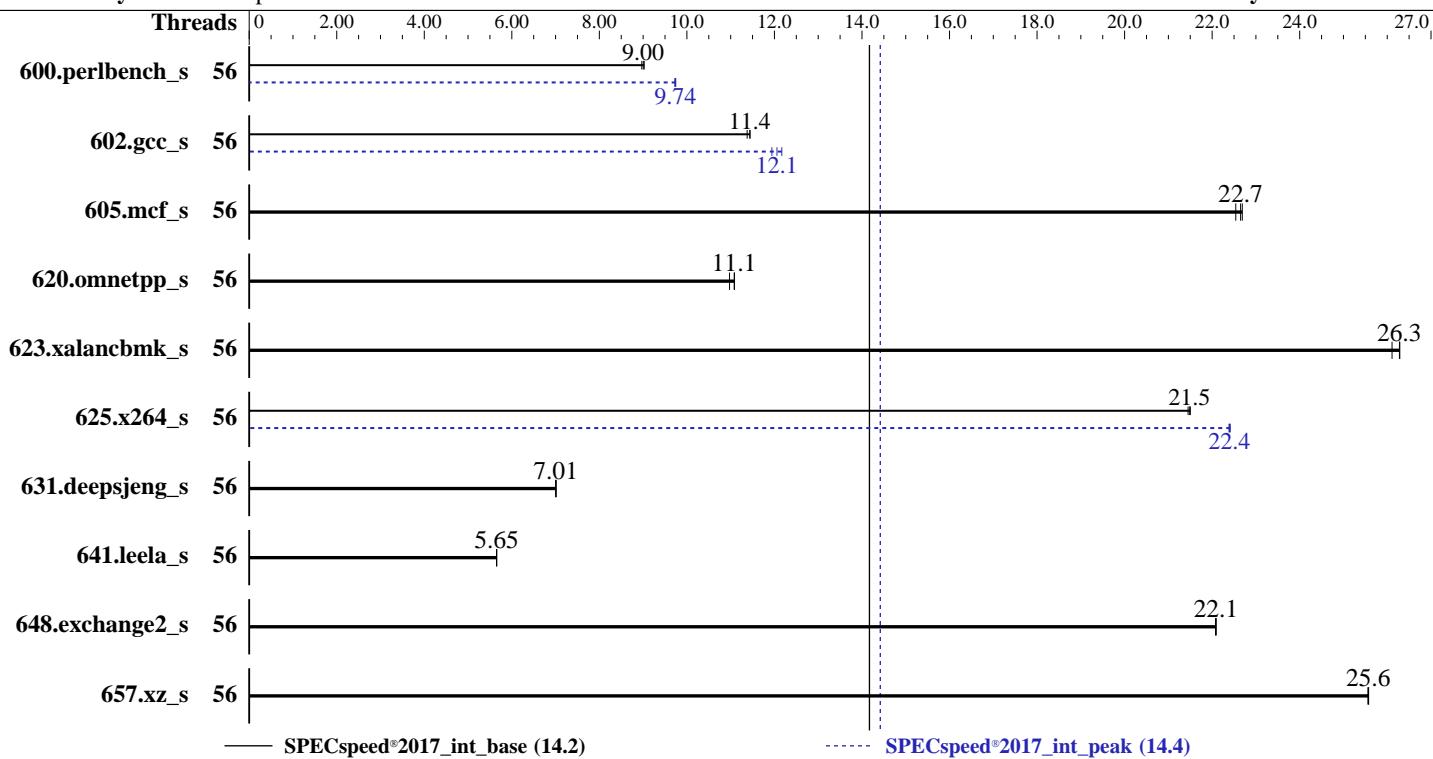
Test Date: Sep-2023

Test Sponsor: Supermicro

Hardware Availability: Jan-2023

Tested by: Supermicro

Software Availability: Dec-2022



Hardware		Software	
CPU Name:	Intel Xeon Platinum 8480+	OS:	SUSE Linux Enterprise Server 15 SP4
Max MHz:	3800	Compiler:	Kernel 5.14.21-150400.22-default
Nominal:	2000	Parallel:	C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
Enabled:	56 cores, 1 chip	Firmware:	Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
Orderable:	1 chip	File System:	Yes
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	Version 1.1 released Jan-2023
L2:	2 MB I+D on chip per core	Base Pointers:	xfs
L3:	105 MB I+D on chip per chip	Peak Pointers:	Run level 3 (multi-user)
Other:	None	Other:	64-bit
Memory:	256 GB (8 x 32 GB 2Rx8 PC5-4800B-R)	Power Management:	64-bit
Storage:	1 x 240 GB SATA III SSD	jemalloc memory allocator V5.0.1	
Other:	None	BIOS set to prefer performance at the cost of additional power usage.	



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## Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	56	198	8.97	197	9.02	<u>197</u>	<u>9.00</u>	56	183	9.72	<u>182</u>	<u>9.74</u>	182	9.74		
602.gcc_s	56	348	11.4	350	11.4	<u>348</u>	<u>11.4</u>	56	<u>330</u>	<u>12.1</u>	327	12.2	333	11.9		
605.mcf_s	56	<b>208</b>	<b>22.7</b>	209	22.5	208	22.7	56	<b>208</b>	<b>22.7</b>	209	22.5	208	22.7		
620.omnetpp_s	56	149	11.0	147	11.1	<u>147</u>	<u>11.1</u>	56	149	11.0	147	11.1	<u>147</u>	<u>11.1</u>		
623.xalancbmk_s	56	53.9	26.3	54.3	26.1	<u>53.9</u>	<u>26.3</u>	56	53.9	26.3	54.3	26.1	<u>53.9</u>	<u>26.3</u>		
625.x264_s	56	<b>82.1</b>	<b>21.5</b>	82.0	21.5	82.2	21.5	56	78.8	22.4	78.7	22.4	<b>78.8</b>	<b>22.4</b>		
631.deepsjeng_s	56	205	7.00	<u>205</u>	<u>7.01</u>	204	7.01	56	205	7.00	<u>205</u>	<u>7.01</u>	204	7.01		
641.leela_s	56	302	5.65	<u>302</u>	<u>5.65</u>	302	5.65	56	302	5.65	<u>302</u>	<u>5.65</u>	302	5.65		
648.exchange2_s	56	133	22.1	133	22.1	<u>133</u>	<u>22.1</u>	56	133	22.1	133	22.1	<u>133</u>	<u>22.1</u>		
657.xz_s	56	242	25.6	242	25.6	<u>242</u>	<u>25.6</u>	56	242	25.6	242	25.6	<u>242</u>	<u>25.6</u>		
SPECspeed®2017_int_base = 14.2								SPECspeed®2017_int_peak = 14.4								

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation

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## General Notes (Continued)

Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

### BIOS Settings:

Power Technology = Custom  
Power Performance Tuning = BIOS Controls EPB  
ENERGY\_PERF\_BIAS\_CFG mode = Performance  
DCU Streamer Prefetcher = Disable  
Hyper-Threading [ALL] = Disable  
LLC Dead Line Alloc = Disable  
KTI Prefetch = Enable  
Stale AtoS = Disable  
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on SYS-521C-NR-8480 Fri Sep 15 18:59:18 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
  10. who -r
  11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  12. Services, from systemctl list-unit-files
  13. Linux kernel boot-time arguments, from /proc/cmdline
  14. cpupower frequency-info
  15. sysctl
  16. /sys/kernel/mm/transparent\_hugepage
  17. /sys/kernel/mm/transparent\_hugepage/khugepaged
  18. OS release
  19. Disk information
  20. /sys/devices/virtual/dmi/id
  21. dmidecode
  22. BIOS
- 

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## Platform Notes (Continued)

1. uname -a  
Linux SYS-521C-NR-8480 5.14.21-150400.22-default #1 SMP PREEMPT\_DYNAMIC Wed May 11 06:57:18 UTC 2022  
(49db222) x86\_64 x86\_64 x86\_64 GNU/Linux

2. w  
18:59:18 up 3 min, 2 users, load average: 0.01, 0.02, 0.00  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root ttym1 - 18:58 2.00s 0.90s 0.00s -bash  
root ttym2 - 18:59 11.00s 0.02s 0.02s -bash

3. Username  
From environment variable \$USER: root

4. ulimit -a  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 1029610  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 1029610  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 30  
login -- root  
-bash  
-bash  
runcpu --nobuild --action validate --define default-platform-flags -c  
ic2023.0-lin-core-avx512-speed-20221201.cfg --define cores=56 --tune base,peak -o all --define  
intspeedaffinity --define drop\_caches intspeed  
runcpu --nobuild --action validate --define default-platform-flags --configfile  
ic2023.0-lin-core-avx512-speed-20221201.cfg --define cores=56 --tune base,peak --output\_format all  
--define intspeedaffinity --define drop\_caches --nopower --runmode speed --tune base:peak --size refspeed  
intspeed --nopreenv --note-preenv --logfile \$SPEC/tmp/CPU2017.003/templogs/preenv.intspeed.003.0.log  
--lognum 003.0 --from\_runcpu 2  
specperl \$SPEC/bin/sysinfo  
\$SPEC = /home/cpu2017

6. /proc/cpuinfo  
model name : Intel(R) Xeon(R) Platinum 8480+  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 143  
stepping : 8  
microcode : 0x2b000161

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## Platform Notes (Continued)

```
bugs          : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores    : 56
siblings     : 56
1 physical ids (chips)
56 processors (hardware threads)
physical id 0: core ids 0-55
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70,72
,74,76,78,80,82,84,86,88,90,92,94,96,98,100,102,104,106,108,110
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----  
7. lscpu

From lscpu from util-linux 2.37.2:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                56
On-line CPU(s) list:  0-55
Vendor ID:             GenuineIntel
Model name:            Intel(R) Xeon(R) Platinum 8480+
CPU family:            6
Model:                 143
Thread(s) per core:   1
Core(s) per socket:   56
Socket(s):            1
Stepping:              8
Frequency boost:      enabled
CPU max MHz:          2001.0000
CPU min MHz:          800.0000
BogoMIPS:              4000.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                      nonstop_tsc cpuid aperfmon tsc_known_freq pni pclmulqdq dtes64 monitor
                      ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1
                      sse4_2 x2apic movebe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                     lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
                      invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                      vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                      bmi2 erts invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                      avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                      xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                      cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                      arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
                      vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid
                      bus_lock_detect cldemote movdir64b enqcmd fsrm md_clear serialize
                      tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:        VT-x
L1d cache:             2.6 MiB (56 instances)
L1i cache:             1.8 MiB (56 instances)
L2 cache:              112 MiB (56 instances)
L3 cache:              105 MiB (1 instance)
NUMA node(s):          1
NUMA node0 CPU(s):     0-55
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:    Not affected
```

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## Platform Notes (Continued)

Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.6M	12	Data	1	64	1	64
L1i	32K	1.8M	8	Instruction	1	64	1	64
L2	2M	112M	16	Unified	2	2048	1	64
L3	105M	105M	15	Unified	3	114688	1	64

-----  
8. numactl --hardware  
NOTE: a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0-55  
node 0 size: 257429 MB  
node 0 free: 256353 MB  
node distances:  
node 0  
0: 10

-----  
9. /proc/meminfo  
MemTotal: 263607708 kB

-----  
10. who -r  
run-level 3 Sep 15 18:56

-----  
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)  
Default Target Status  
multi-user running

-----  
12. Services, from systemctl list-unit-files  

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ haveged irqbalance issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog smartd sshd wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chrony console-getty cups cups-browsed debug-shell dnsmasq ebtables exchange-bmc-os-info firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmiev issue-add-ssh-keys kexec-load lummask man-db-create multipathd nfs nfs-blkmap nm-cloud-setup rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2 wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant@
indirect	wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default

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## Platform Notes (Continued)

```
root=UUID=e03d955a-b534-4e13-b78b-6834d53b79c0
splash=silent
quiet
security=apparmor
mitigations=auto
```

```
-----  
14. cpupower frequency-info
analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 2.00 GHz.
                    The governor "ondemand" may decide which speed to use
                    within this range.
    boost state support:
        Supported: yes
        Active: yes
```

```
-----  
15. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space       2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  60
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage
defrag           always defer defer+madvise [madvise] never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
```

```
-----  
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

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## Platform Notes (Continued)

### 19. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xfs	221G	96G	126G	44%	/

### 20. /sys/devices/virtual/dmi/id

Vendor:	FruPM33
Product:	FruPPM33
Product Family:	Family
Serial:	FruPS33

### 21. dmidecode

Additional information from dmidecode 3.2 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

#### Memory:

8x Micron Technology MTC20F2085S1RC48BA1 32 GB 2 rank 4800

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor:	American Megatrends International, LLC.
BIOS Version:	1.1
BIOS Date:	01/19/2023
BIOS Revision:	5.29

## Compiler Version Notes

=====

C | 600.perlbench\_s(base, peak) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak) 625.x264\_s(base, peak)  
| 657.xz\_s(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak) 631.deepsjeng\_s(base, peak)  
| 641.leela\_s(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

=====

Fortran | 648.exchange2\_s(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201  
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## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -fsto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC\_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:

-m64 -std=c++14 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -fsto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -fsto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc



# SPEC CPU®2017 Integer Speed Result

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## Supermicro

UP SuperServer SYS-521C-NR  
(X13SEDW-F, Intel Xeon Platinum 8480+)

SPECspeed®2017\_int\_base = 14.2

SPECspeed®2017\_int\_peak = 14.4

CPU2017 License: 001176

Test Date: Sep-2023

Test Sponsor: Supermicro

Hardware Availability: Jan-2023

Tested by: Supermicro

Software Availability: Dec-2022

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
```

605.mcf\_s: basepeak = yes

```
625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

(Continued on next page)



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**Software Availability:** Dec-2022

## Peak Optimization Flags (Continued)

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64\\_revB.html](http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.html)

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revC.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64\\_revB.xml](http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64_revB.xml)

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revC.xml>

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