



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECSpeed®2017_int_base = 13.9

SPECSpeed®2017_int_peak = 14.1

CPU2017 License: 9017

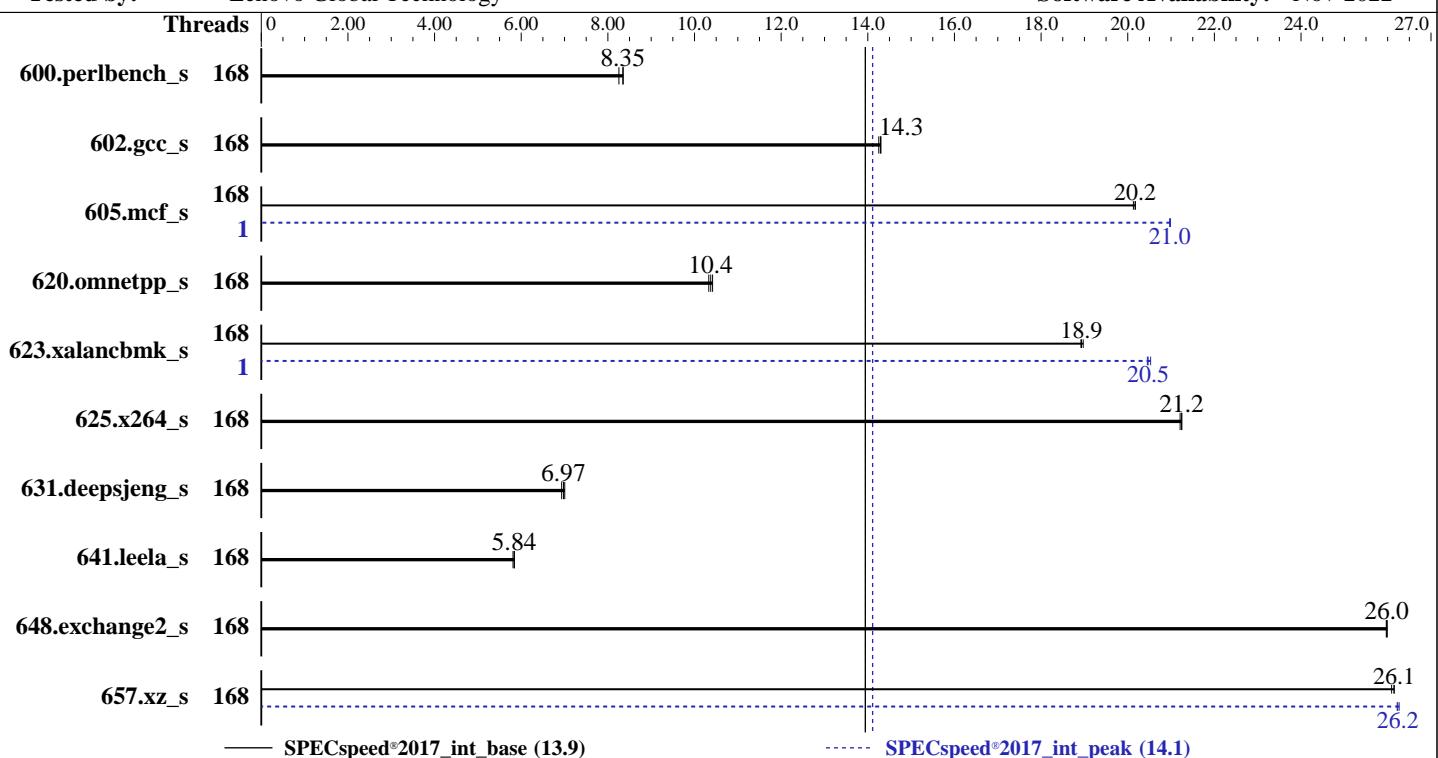
Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022



Hardware		Software	
CPU Name:	AMD EPYC 9634	OS:	SUSE Linux Enterprise Server 15 SP4
Max MHz:	3700	Compiler:	Kernel 5.14.21-150400.22-default
Nominal:	2250	Parallel:	C/C++/Fortran: Version 4.0.0 of AOCC
Enabled:	168 cores, 2 chips	Firmware:	Yes
Orderable:	1,2 chips	File System:	Lenovo BIOS Version KAE105L 1.20 released Dec-2022
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	xfs
L2:	1 MB I+D on chip per core	Base Pointers:	Run level 3 (multi-user)
L3:	384 MB I+D on chip per chip, 32 MB shared / 7 cores	Peak Pointers:	64-bit
Other:	None	Other:	64-bit
Memory:	768 GB (24 x 32 GB 2Rx8 PC5-4800B-R)	Power Management:	None
Storage:	1 x 480 GB SATA SSD		BIOS and OS set to prefer performance at the cost of additional power usage
Other:	None		



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	168	<u>213</u>	<u>8.35</u>	215	8.25	212	8.35	168	<u>213</u>	<u>8.35</u>	215	8.25	212	8.35
602.gcc_s	168	<u>279</u>	<u>14.3</u>	279	14.3	278	14.3	168	<u>279</u>	<u>14.3</u>	279	14.3	278	14.3
605.mcf_s	168	235	20.1	234	20.2	<u>234</u>	<u>20.2</u>	1	225	21.0	<u>225</u>	<u>21.0</u>	225	21.0
620.omnetpp_s	168	158	10.3	157	10.4	<u>157</u>	<u>10.4</u>	168	158	10.3	157	10.4	<u>157</u>	<u>10.4</u>
623.xalancbmk_s	168	74.9	18.9	<u>74.8</u>	<u>18.9</u>	74.7	19.0	1	69.0	20.5	<u>69.2</u>	<u>20.5</u>	69.3	20.5
625.x264_s	168	83.2	21.2	<u>83.0</u>	<u>21.2</u>	83.0	21.2	168	83.2	21.2	<u>83.0</u>	<u>21.2</u>	83.0	21.2
631.deepsjeng_s	168	205	7.00	<u>205</u>	<u>6.97</u>	207	6.93	168	205	7.00	<u>205</u>	<u>6.97</u>	207	6.93
641.leela_s	168	<u>292</u>	<u>5.84</u>	294	5.81	292	5.84	168	<u>292</u>	<u>5.84</u>	294	5.81	292	5.84
648.exchange2_s	168	113	26.0	<u>113</u>	<u>26.0</u>	113	26.0	168	113	26.0	<u>113</u>	<u>26.0</u>	113	26.0
657.xz_s	168	237	26.1	236	26.1	<u>236</u>	<u>26.1</u>	168	<u>236</u>	<u>26.2</u>	235	26.3	236	26.2
SPECspeed®2017_int_base = 13.9														
SPECspeed®2017_int_peak = 14.1														

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-167"
LD_LIBRARY_PATH =
 "/home/cpu2017-1.1.9-amd-aocc400-genoa-B1b/amd_speed_aocc400_genoa_B_lib
 /lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "168"

Environment variables set by runcpu during the 605.mcf_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 657.xz_s peak run:

GOMP_CPU_AFFINITY = "0-167"

LIBOMP_NUM_HIDDEN_HELPER_THREADS = "8"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance and then set it to Custom Mode

NUMA Nodes per Socket set to NPS4

SMT Mode set to Disabled

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc400-genoa-B1b/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Thu Feb  2 10:22:58 2023
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
-

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
10:22:59 up 7 min, 1 user, load average: 0.27, 0.42, 0.24
USER   TTY     FROM          LOGIN@    IDLE    JCPU    PCPU WHAT
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

```
root      tty1      -          10:16    1:12   1.89s  0.63s /bin/bash ./amd_speed_aocc400_genoa_B1.sh
```

3. Username

```
From environment variable $USER: root
```

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
cheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 3094025
max locked memory       (kbytes, -l) 2097152
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 3094025
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
/bin/bash ./run_SR645V3_genoa_rxy.sh
/bin/bash ./Run035-compliant-amd-speedint.sh
python3 ./run_amd_speed_aocc400_genoa_B1.py
/bin/bash ./amd_speed_aocc400_genoa_B1.sh
runcpu --config amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc400_genoa_B1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc400-genoa-B1b
```

6. /proc/cpuinfo

```
model name      : AMD EPYC 9634 84-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 17
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

```
stepping      : 1
microcode     : 0xa101111
bugs          : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 3584 4K pages
cpu cores     : 84
siblings      : 84
2 physical ids (chips)
168 processors (hardware threads)
physical id 0: core ids 0-6,16-22,32-38,48-54,64-70,80-86,96-102,112-118,128-134,144-150,160-166,176-182
physical id 1: core ids 0-6,16-22,32-38,48-54,64-70,80-86,96-102,112-118,128-134,144-150,160-166,176-182
physical id 0: apicids 0-6,16-22,32-38,48-54,64-70,80-86,96-102,112-118,128-134,144-150,160-166,176-182
    physical id 1: apicids
256-262,272-278,288-294,304-310,320-326,336-342,352-358,368-374,384-390,400-406,416-422,432-438
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

Architecture:	x86_64
CPU op-mode(s):	32-bit, 64-bit
Address sizes:	52 bits physical, 57 bits virtual
Byte Order:	Little Endian
CPU(s):	168
On-line CPU(s) list:	0-167
Vendor ID:	AuthenticAMD
Model name:	AMD EPYC 9634 84-Core Processor
CPU family:	25
Model:	17
Thread(s) per core:	1
Core(s) per socket:	84
Socket(s):	2
Stepping:	1
Frequency boost:	enabled
CPU max MHz:	3700.1951
CPU min MHz:	1500.0000
BogoMIPS:	4493.00
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skininit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

```
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cq_m_llc cq_m_occup_llc cq_m_mb_m_total cq_m_mb_m_local
avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
    svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
pausefilter pfthreshold avic v_vmsave_vmlload vgif v_spec_ctrl avx512vbmi
umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
```

Virtualization:

AMD-V

L1d cache: 5.3 MiB (168 instances)

L1i cache: 5.3 MiB (168 instances)

L2 cache: 168 MiB (168 instances)

L3 cache: 768 MiB (24 instances)

NUMA node(s): 8

NUMA node0 CPU(s): 0-20

NUMA node1 CPU(s): 21-41

NUMA node2 CPU(s): 42-62

NUMA node3 CPU(s): 63-83

NUMA node4 CPU(s): 84-104

NUMA node5 CPU(s): 105-125

NUMA node6 CPU(s): 126-146

NUMA node7 CPU(s): 147-167

Vulnerability Itlb multihit: Not affected

Vulnerability L1tf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB filling

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	5.3M	8	Data	1	64	1	64
L1i	32K	5.3M	8	Instruction	1	64	1	64
L2	1M	168M	8	Unified	2	2048	1	64
L3	32M	768M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-20

node 0 size: 96468 MB

node 0 free: 94817 MB

node 1 cpus: 21-41

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz,AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

```
node 1 size: 96752 MB
node 1 free: 96311 MB
node 2 cpus: 42-62
node 2 size: 96752 MB
node 2 free: 96437 MB
node 3 cpus: 63-83
node 3 size: 96752 MB
node 3 free: 96375 MB
node 4 cpus: 84-104
node 4 size: 96752 MB
node 4 free: 96368 MB
node 5 cpus: 105-125
node 5 size: 96752 MB
node 5 free: 96358 MB
node 6 cpus: 126-146
node 6 size: 96752 MB
node 6 free: 96500 MB
node 7 cpus: 147-167
node 7 size: 96549 MB
node 7 free: 96171 MB
node distances:
node  0   1   2   3   4   5   6   7
  0: 10  12  12  12  32  32  32  32
  1: 12  10  12  12  32  32  32  32
  2: 12  12  10  12  32  32  32  32
  3: 12  12  12  10  32  32  32  32
  4: 32  32  32  32  10  12  12  12
  5: 32  32  32  32  12  10  12  12
  6: 32  32  32  32  12  12  10  12
  7: 32  32  32  32  12  12  12  10
```

```
9. /proc/meminfo
  MemTotal:      792094988 kB
```

```
10. who -r
  run-level 3 Feb 2 10:16
```

```
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  Default Target  Status
  multi-user     running
```

```
12. Services, from systemctl list-unit-files
  STATE          UNIT FILES
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmievrd issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
generated	ntp_sync
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
root=UUID=3bd1de87-9e70-472e-8edf-b06ab6b3800b
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info
analyzing CPU 0:
current policy: frequency should be within 1.50 GHz and 2.25 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

```
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness             1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode      1
```

```
16. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs   60000
    defrag                  1
    max_ptes_none           511
    max_ptes_shared          256
    max_ptes_swap            64
    pages_to_scan            4096
    scan_sleep_millisecs     10000
```

```
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP4
```

```
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc400-genoa-B1b
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdb3        xfs   442G   40G  403G   9%  /
```

```
20. /sys/devices/virtual/dmi/id
  Vendor:          Lenovo
  Product:         ThinkSystem SR645 V3 MB, Genoa, DDR5, Oahu, 1U
  Product Family: ThinkSystem
  Serial:          1234567890
```

```
21. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

7x SK Hynix HMCG88AEBRA115N 32 GB 2 rank 4800
17x SK Hynix HMCG88AEBRA168N 32 GB 2 rank 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo
BIOS Version: KAE105L-1.20
BIOS Date: 12/29/2022
BIOS Revision: 1.20
Firmware Revision: 1.20

Compiler Version Notes

C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

Fortran | 648.exchange2_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Compiler Version Notes (Continued)

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -O3 -march=znver4 -fvecelib=AMDLIBM
-ffast-math -fopenmp -futo -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdaloc

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Base Optimization Flags (Continued)

C++ benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fvec/lib=AMDLIB -ffast-math -fopenmp -flto
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fvec/lib=AMDLIB
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz, AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: basepeak = yes

602.gcc_s: basepeak = yes

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang

625.x264_s: basepeak = yes

657.xz_s: Same as 605.mcf_s

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdaloc-ext -lflang

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR645 V3
(2.25 GHz,AMD EPYC 9634)

SPECspeed®2017_int_base = 13.9

SPECspeed®2017_int_peak = 14.1

CPU2017 License: 9017

Test Date: Feb-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Nov-2022

Peak Optimization Flags (Continued)

648.exchange2_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-Q.html>
<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-Q.xml>
<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-02-01 21:22:58-0500.

Report generated on 2023-03-02 11:24:54 by CPU2017 PDF formatter v6442.

Originally published on 2023-02-28.