



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017_fp_base = 497

PowerEdge R6525 (AMD EPYC 7473X 24-Core Processor)

SPECrate®2017_fp_peak = 506

CPU2017 License: 55

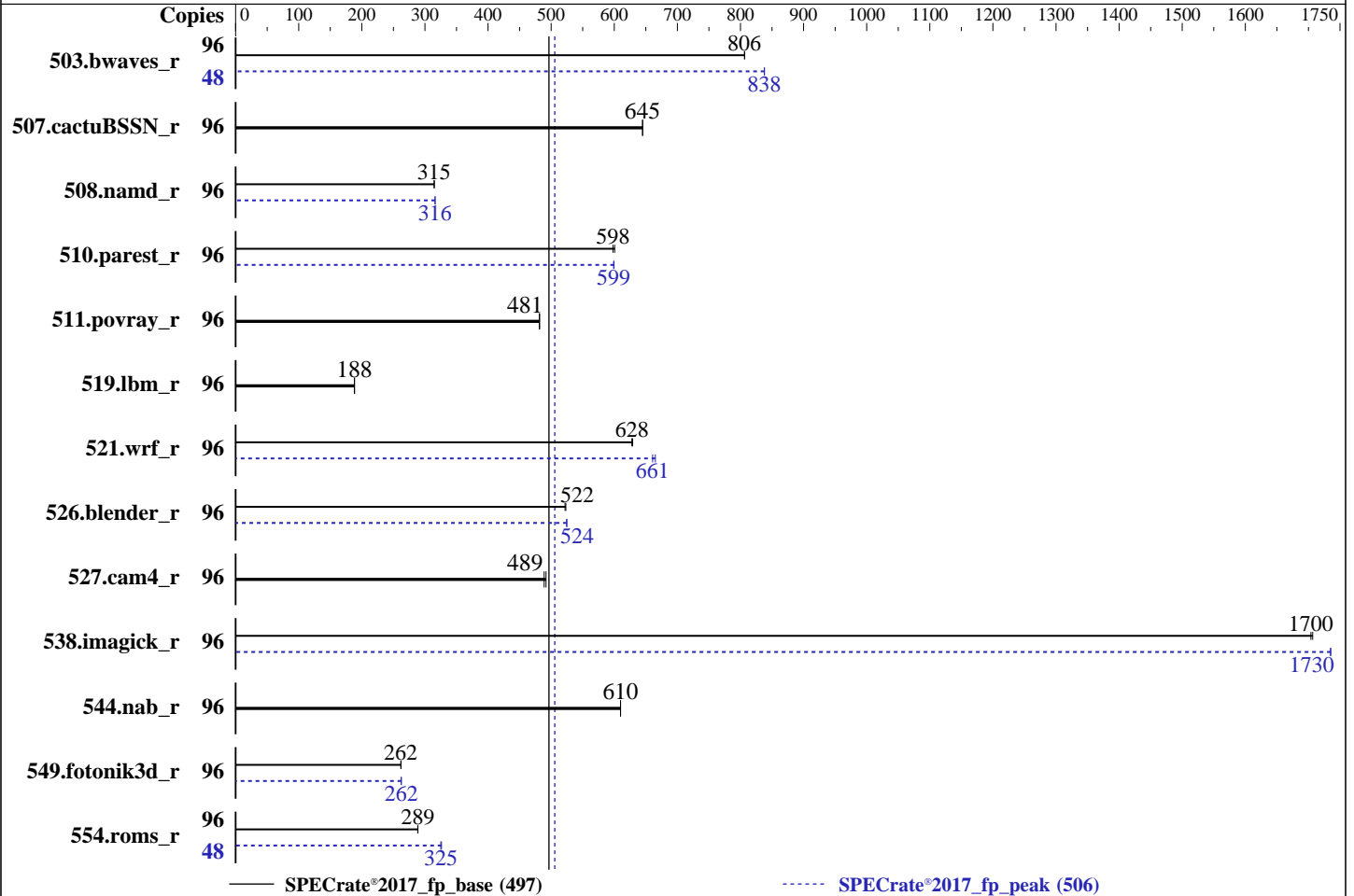
Test Date: Feb-2022

Test Sponsor: Dell Inc.

Hardware Availability: Mar-2022

Tested by: Dell Inc.

Software Availability: Dec-2021



Hardware

CPU Name: AMD EPYC 7473X
 Max MHz: 3700
 Nominal: 2800
 Enabled: 48 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 768 MB I+D on chip per chip, 96 MB shared / 3 cores
 Other: None
 Memory: 2 TB (16 x 128 GB 4Rx4 PC4-3200AA-L)
 Storage: 125 GB on tmpfs
 Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
 4.18.0-240.el8.x86_64
 Compiler: C/C++/Fortran: Version 3.2.0 of AOCC
 Parallel: No
 Firmware: Version 2.6.5 released Dec-2021
 File System: tmpfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc: jemalloc memory allocator library v5.1.0
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	1194	807	<u>1194</u>	<u>806</u>			48	<u>574</u>	<u>838</u>	574	838		
507.cactuBSSN_r	96	<u>189</u>	<u>645</u>	188	645			96	<u>189</u>	<u>645</u>	188	645		
508.namd_r	96	<u>290</u>	<u>315</u>	290	315			96	288	316	<u>289</u>	<u>316</u>		
510.parest_r	96	<u>420</u>	<u>598</u>	418	601			96	<u>419</u>	<u>599</u>	419	600		
511.povray_r	96	465	482	<u>466</u>	<u>481</u>			96	465	482	<u>466</u>	<u>481</u>		
519.lbm_r	96	<u>538</u>	<u>188</u>	537	189			96	<u>538</u>	<u>188</u>	537	189		
521.wrf_r	96	<u>342</u>	<u>628</u>	342	629			96	323	665	<u>325</u>	<u>661</u>		
526.blender_r	96	279	523	<u>280</u>	<u>522</u>			96	279	525	<u>279</u>	<u>524</u>		
527.cam4_r	96	341	492	<u>343</u>	<u>489</u>			96	341	492	<u>343</u>	<u>489</u>		
538.imagick_r	96	140	1710	<u>140</u>	<u>1700</u>			96	138	1740	<u>138</u>	<u>1730</u>		
544.nab_r	96	<u>265</u>	<u>610</u>	265	610			96	<u>265</u>	<u>610</u>	265	610		
549.fotonik3d_r	96	<u>1429</u>	<u>262</u>	1428	262			96	<u>1425</u>	<u>262</u>	1425	263		
554.roms_r	96	528	289	<u>528</u>	<u>289</u>			48	234	326	<u>235</u>	<u>325</u>		

SPECrate®2017_fp_base = **497**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.

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Operating System Notes (Continued)

To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations, 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/amd_rate_aocc320_milanx_A_lib/lib  
    ;/mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/amd_rate_aocc320_milanx_A_lib/lib  
    32:"  
MALLOC_CONF = "retain:true"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

NUMA Nodes per Socket : 4

L3 Cache as NUMA Domain : Enabled

Virtualization Technology : Disabled

DRAM Refresh Delay : Performance

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Platform Notes (Continued)

System Profile : Custom
 CPU Power Management : Maximum Performance
 Memory Patrol Scrub : Disabled
 PCI ASPM Ll Link
 Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on rhel-8-3-amd Thu Feb 24 13:28:47 2022

SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo
 model name : AMD EPYC 7473X 24-Core Processor
 2 "physical id"s (chips)
 96 "processors"
 cores, siblings (Caution: counting these is hw and system dependent. The following
 excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
 cpu cores : 24
 siblings : 48
 physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
 physical 1: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30

From lscpu from util-linux 2.32.1:
 Architecture: x86_64
 CPU op-mode(s): 32-bit, 64-bit
 Byte Order: Little Endian
 CPU(s): 96
 On-line CPU(s) list: 0-95
 Thread(s) per core: 2
 Core(s) per socket: 24
 Socket(s): 2
 NUMA node(s): 16
 Vendor ID: AuthenticAMD
 CPU family: 25
 Model: 1
 Model name: AMD EPYC 7473X 24-Core Processor
 Stepping: 2
 CPU MHz: 3041.293
 BogoMIPS: 5589.22
 Virtualization: AMD-V
 L1d cache: 32K
 L1i cache: 32K
 L2 cache: 512K

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Platform Notes (Continued)

```

L3 cache:                98304K
NUMA node0 CPU(s):      0-2,48-50
NUMA node1 CPU(s):      3-5,51-53
NUMA node2 CPU(s):      6-8,54-56
NUMA node3 CPU(s):      9-11,57-59
NUMA node4 CPU(s):      12-14,60-62
NUMA node5 CPU(s):      15-17,63-65
NUMA node6 CPU(s):      18-20,66-68
NUMA node7 CPU(s):      21-23,69-71
NUMA node8 CPU(s):      24-26,72-74
NUMA node9 CPU(s):      27-29,75-77
NUMA node10 CPU(s):     30-32,78-80
NUMA node11 CPU(s):     33-35,81-83
NUMA node12 CPU(s):     36-38,84-86
NUMA node13 CPU(s):     39-41,87-89
NUMA node14 CPU(s):     42-44,90-92
NUMA node15 CPU(s):     45-47,93-95

```

```

Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 invpcid_single hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall
fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap clflushopt clwb
sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local clzero irperf xsaveerptr wbnoinvd amd_ppin arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
v_omsave_vmload vgif unip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 16 nodes (0-15)
node 0 cpus: 0 1 2 48 49 50
node 0 size: 128487 MB
node 0 free: 128479 MB
node 1 cpus: 3 4 5 51 52 53
node 1 size: 128916 MB
node 1 free: 128921 MB
node 2 cpus: 6 7 8 54 55 56
node 2 size: 128930 MB
node 2 free: 128941 MB
node 3 cpus: 9 10 11 57 58 59
node 3 size: 128916 MB

```

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Platform Notes (Continued)

```

node 3 free: 128933 MB
node 4 cpus: 12 13 14 60 61 62
node 4 size: 128948 MB
node 4 free: 128921 MB
node 5 cpus: 15 16 17 63 64 65
node 5 size: 128920 MB
node 5 free: 128936 MB
node 6 cpus: 18 19 20 66 67 68
node 6 size: 128938 MB
node 6 free: 128884 MB
node 7 cpus: 21 22 23 69 70 71
node 7 size: 128922 MB
node 7 free: 128923 MB
node 8 cpus: 24 25 26 72 73 74
node 8 size: 128976 MB
node 8 free: 128794 MB
node 9 cpus: 27 28 29 75 76 77
node 9 size: 128918 MB
node 9 free: 128823 MB
node 10 cpus: 30 31 32 78 79 80
node 10 size: 128966 MB
node 10 free: 125435 MB
node 11 cpus: 33 34 35 81 82 83
node 11 size: 128978 MB
node 11 free: 128897 MB
node 12 cpus: 36 37 38 84 85 86
node 12 size: 128956 MB
node 12 free: 128894 MB
node 13 cpus: 39 40 41 87 88 89
node 13 size: 128978 MB
node 13 free: 128763 MB
node 14 cpus: 42 43 44 90 91 92
node 14 size: 128982 MB
node 14 free: 128928 MB
node 15 cpus: 45 46 47 93 94 95
node 15 size: 128970 MB
node 15 free: 128920 MB
node distances:
node  0  1  2  3  4  5  6  7  8  9 10 11 12 13 14 15
  0: 10 11 12 12 12 12 12 12 32 32 32 32 32 32 32 32
  1: 11 10 12 12 12 12 12 12 12 32 32 32 32 32 32 32
  2: 12 12 10 11 12 12 12 12 12 32 32 32 32 32 32 32
  3: 12 12 11 10 12 12 12 12 12 32 32 32 32 32 32 32
  4: 12 12 12 12 10 11 12 12 12 32 32 32 32 32 32 32
  5: 12 12 12 12 11 10 12 12 12 32 32 32 32 32 32 32
  6: 12 12 12 12 12 12 10 11 32 32 32 32 32 32 32 32
  7: 12 12 12 12 12 12 11 10 32 32 32 32 32 32 32 32

```

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Platform Notes (Continued)

8:	32	32	32	32	32	32	32	32	32	10	11	12	12	12	12	12	12
9:	32	32	32	32	32	32	32	32	32	11	10	12	12	12	12	12	12
10:	32	32	32	32	32	32	32	32	32	12	12	10	11	12	12	12	12
11:	32	32	32	32	32	32	32	32	32	12	12	11	10	12	12	12	12
12:	32	32	32	32	32	32	32	32	32	12	12	12	12	10	11	12	12
13:	32	32	32	32	32	32	32	32	32	12	12	12	12	11	10	12	12
14:	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	10	11
15:	32	32	32	32	32	32	32	32	32	12	12	12	12	12	12	11	10

From /proc/meminfo

```
MemTotal:      2113388792 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

/sbin/tuned-adm active

Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:

```
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
```

redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:

```
Linux rhel-8-3-amd 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swapgs barriers and __user pointer sanitization

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Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: always-on, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Feb 24 09:39

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-aocc320-A1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
tmpfs	tmpfs	125G	3.3G	122G	3%	/mnt/ramdisk

From /sys/devices/virtual/dmi/id

Vendor: Dell Inc.

Product: PowerEdge R6525

Product Family: PowerEdge

Serial: C3JTPX2

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x 802C8632802C 72ASS16G72LZ-3G2B3 128 GB 4 rank 3200

16x Not Specified Not Specified

BIOS:

BIOS Vendor: Dell Inc.

BIOS Version: 2.6.5

BIOS Date: 12/28/2021

BIOS Revision: 2.6

(End of data from sysinfo program)

Compiler Version Notes

```
=====
C          | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
          | 544.nab_r(base, peak)
=====
```

```
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
=====
```

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Compiler Version Notes (Continued)

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
=====

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
=====

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
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Compiler Version Notes (Continued)

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
| 554.roms_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



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Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-m64 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang

```

C++ benchmarks:

```

-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang

```

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Tested by: Dell Inc.

Software Availability: Dec-2021

Base Optimization Flags (Continued)

Fortran benchmarks:

```

-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -Hz,1,0x1 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loop-fusion
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang

```

Benchmarks using both Fortran and C:

```

-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang

```

Benchmarks using both C and C++:

```

-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch

```

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Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):

```
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -z muldefs -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -mllvm -extra-vectorizer-passes
-mllvm -convert-pow-exp-to-int=false -Hz,1,0x1 -Kieee -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -lamdlibm -ljemalloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument
```

Benchmarks using both C and C++:

```
-Wno-unused-command-line-argument
```

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Base Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: -m64 -flto -Wl,-mllvm -Wl,-function-specialize

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

-march=znver3 -fveclib=AMDLIBM -ffast-math

-fstruct-layout=7 -mllvm -unroll-threshold=50

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Peak Optimization Flags (Continued)

538.imagick_r (continued):

```
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: -m64 -std=c++98 -mno-adx -mno-sse4a

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc
```

510.parest_r: -m64 -std=c++98 -mno-adx -mno-sse4a

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -lamdlibm -ljemalloc
```

Fortran benchmarks:

503.bwaves_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching

```
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
```

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Peak Optimization Flags (Continued)

503.bwaves_r (continued):

-lamdlibm -ljemalloc -lflang

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching

-Wl,-mllvm -Wl,-enable-licm-vrp -flto

-Wl,-mllvm -Wl,-function-specialize

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

-march=znver3 -fveclib=AMDLIBM -ffast-math -Kieee

-Mrecursive -mllvm -reduce-array-computations=3

-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp

-lamdlibm -ljemalloc -lflang

554.roms_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching

-Wl,-mllvm -Wl,-enable-licm-vrp -flto

-Wl,-mllvm -Wl,-function-specialize

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

-march=znver3 -fveclib=AMDLIBM -ffast-math -Mrecursive

-mllvm -reduce-array-computations=3

-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp

-Hz,1,0x1 -mllvm -fuse-tile-inner-loop -lamdlibm

-ljemalloc -lflang

Benchmarks using both Fortran and C:

521.wrf_r: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching

-Wl,-mllvm -Wl,-enable-licm-vrp -flto

-Wl,-mllvm -Wl,-function-specialize

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

-march=znver3 -fveclib=AMDLIBM -ffast-math

-fstruct-layout=7 -mllvm -unroll-threshold=50

-fremap-arrays -flv-function-specialization

-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist

-mllvm -global-vectorize-slp=true

-mllvm -function-specialize -mllvm -enable-licm-vrp

-mllvm -reduce-array-computations=3 -Mrecursive -lamdlibm

-ljemalloc -lflang

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

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Peak Optimization Flags (Continued)

```

526.blender_r: -m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -finline-aggressive
-mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -lamdlibm -ljemalloc

```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.4.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.4.xml>

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