SPEC CPU®2017 Integer Rate Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R7525 (AMD EPYC 7773X 64-Core Processor)

$SPECrate^{®}2017\_int\_base = 818$

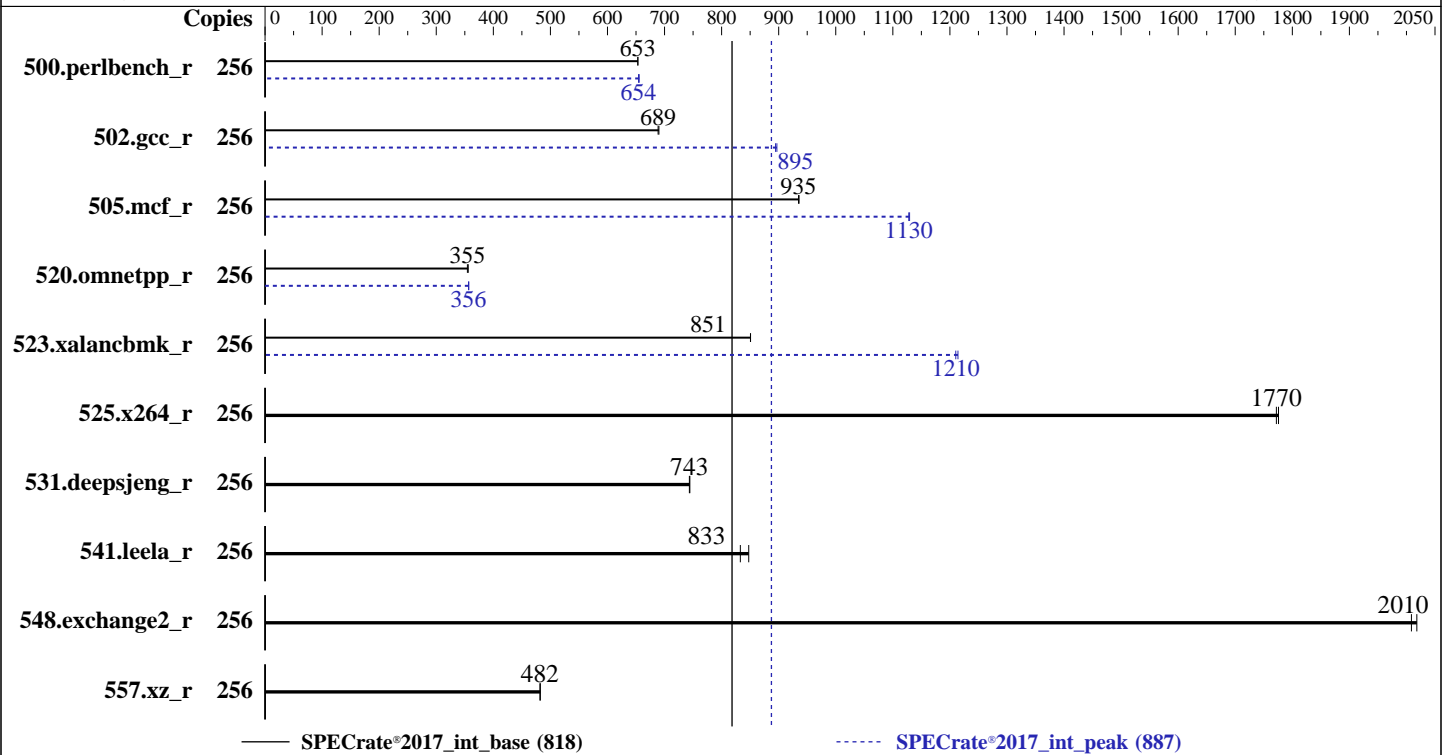$SPECrate^{®}2017\_int\_peak = 887$

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Feb-2022
Hardware Availability: Mar-2022
Software Availability: Feb-2022

Benchmark	Copies	base	peak
500.perlbench_r	256	653	654
502.gcc_r	256	689	895
505.mcf_r	256	935	1130
520.omnetpp_r	256	355	356
523.xalancbmk_r	256	851	1210
525.x264_r	256	1770	
531.deepsjeng_r	256	743	
541.leela_r	256	833	
548.exchange2_r	256	2010	
557.xz_r	256	482	

—— SPECrate®2017_int_base (818)   ----- SPECrate®2017_int_peak (887)

Hardware

CPU Name: AMD EPYC 7773X
 Max MHz: 3500
 Nominal: 2200
Enabled: 128 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 768 MB I+D on chip per chip, 96 MB shared / 8 cores
 Other: None
Memory: 2 TB (16 x 128 GB 4Rx4 PC4-3200AA-L)
Storage: 125 GB on tmpfs
Other: None

Software

OS: Ubuntu 20.04.4 LTS
 5.4.0-100-generic
Compiler: C/C++/Fortran: Version 3.2.0 of AOCC
Parallel: No
Firmware: Version 2.6.5 released Dec-2021
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library v5.1.0
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	256	**624**	**653**	624	653			256	622	656	**623**	**654**		
502.gcc_r	256	**526**	**689**	525	690			256	404	897	**405**	**895**		
505.mcf_r	256	442	936	**443**	**935**			256	366	1130	**367**	**1130**		
520.omnetpp_r	256	**947**	**355**	943	356			256	**942**	**356**	941	357		
523.xalancbmk_r	256	318	851	**318**	**851**			256	223	1210	**223**	**1210**		
525.x264_r	256	**253**	**1770**	252	1780			256	**253**	**1770**	252	1780		
531.deepsjeng_r	256	394	744	**395**	**743**			256	394	744	**395**	**743**		
541.leela_r	256	**509**	**833**	500	848			256	**509**	**833**	500	848		
548.exchange2_r	256	332	2020	**334**	**2010**			256	332	2020	**334**	**2010**		
557.xz_r	256	573	482	**574**	**482**			256	573	482	**574**	**482**		
SPECrate®2017_int_base =		**818**												
SPECrate®2017_int_peak =		**887**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)

https://www.spec.org/

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Operating System Notes (Continued)

```
To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/amd_rate_aocc320_milanx_A_lib/lib
     ;/mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/amd_rate_aocc320_milanx_A_lib/lib
     32:"
MALLOC_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk_r peak run:
MALLOC_CONF = "thp:never"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using OpenSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2


 Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"
```

Platform Notes

```
  BIOS settings:
      NUMA Nodes per Socket : 4
    L3 Cache as NUMA Domain : Enabled
```

(Continued on next page)

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Platform Notes (Continued)

```
  Virtualization Technology : Disabled
        DRAM Refresh Delay : Performance

            System Profile : Custom
      CPU Power Management : Maximum Performance
       Memory Patrol Scrub : Disabled
       PCI ASPM L1 Link
          Power Management : Disabled


 Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-aocc320-A1/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on spa-dev-sut Thu Feb 24 19:37:21 2022

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo


 From /proc/cpuinfo
    model name : AMD EPYC 7773X 64-Core Processor
       2  "physical id"s (chips)
       256 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 64
       siblings  : 128
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
       53 54 55 56 57 58 59 60 61 62 63
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
       53 54 55 56 57 58 59 60 61 62 63

 From lscpu from util-linux 2.34:
       Architecture:              x86_64
       CPU op-mode(s):            32-bit, 64-bit
       Byte Order:                Little Endian
       Address sizes:             48 bits physical, 48 bits virtual
       CPU(s):                    256
       On-line CPU(s) list:       0-255
       Thread(s) per core:        2
       Core(s) per socket:        64
       Socket(s):                 2
       NUMA node(s):              16
       Vendor ID:                 AuthenticAMD
       CPU family:                25
       Model:                     1
       Model name:                AMD EPYC 7773X 64-Core Processor
```

(Continued on next page)

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Platform Notes (Continued)

```
          Stepping:                        2
          CPU MHz:                         1768.576
          BogoMIPS:                        4391.98
          Virtualization:                  AMD-V
          L1d cache:                       4 MiB
          L1i cache:                       4 MiB
          L2 cache:                        64 MiB
          L3 cache:                        1.5 GiB
          NUMA node0 CPU(s):               0-7,128-135
          NUMA node1 CPU(s):               8-15,136-143
          NUMA node2 CPU(s):               16-23,144-151
          NUMA node3 CPU(s):               24-31,152-159
          NUMA node4 CPU(s):               32-39,160-167
          NUMA node5 CPU(s):               40-47,168-175
          NUMA node6 CPU(s):               48-55,176-183
          NUMA node7 CPU(s):               56-63,184-191
          NUMA node8 CPU(s):               64-71,192-199
          NUMA node9 CPU(s):               72-79,200-207
          NUMA node10 CPU(s):              80-87,208-215
          NUMA node11 CPU(s):              88-95,216-223
          NUMA node12 CPU(s):              96-103,224-231
          NUMA node13 CPU(s):              104-111,232-239
          NUMA node14 CPU(s):              112-119,240-247
          NUMA node15 CPU(s):              120-127,248-255
          Vulnerability Itlb multihit:     Not affected
          Vulnerability L1tf:              Not affected
          Vulnerability Mds:               Not affected
          Vulnerability Meltdown:          Not affected
          Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
          prctl and seccomp
          Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user
          pointer sanitization
          Vulnerability Spectre v2:        Mitigation; Full AMD retpoline, IBPB conditional,
          IBRS_FW, STIBP always-on, RSB filling
          Vulnerability Srbds:             Not affected
          Vulnerability Tsx async abort:   Not affected
          Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
          pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
          pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
          aperfmperf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
          popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
          misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
          bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
          ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap
          clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
          cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock
          nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
```

(Continued on next page)

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Platform Notes (Continued)

```
     v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL
      L1d        32K       4M    8 Data              1
      L1i        32K       4M    8 Instruction       1
      L2        512K      64M    8 Unified           2
      L3         96M     1.5G   16 Unified           3

 /proc/cpuinfo cache data
    cache size : 512 KB

 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 16 nodes (0-15)
   node 0 cpus: 0 1 2 3 4 5 6 7 128 129 130 131 132 133 134 135
   node 0 size: 128648 MB
   node 0 free: 128423 MB
   node 1 cpus: 8 9 10 11 12 13 14 15 136 137 138 139 140 141 142 143
   node 1 size: 129018 MB
   node 1 free: 128841 MB
   node 2 cpus: 16 17 18 19 20 21 22 23 144 145 146 147 148 149 150 151
   node 2 size: 129020 MB
   node 2 free: 128816 MB
   node 3 cpus: 24 25 26 27 28 29 30 31 152 153 154 155 156 157 158 159
   node 3 size: 129019 MB
   node 3 free: 128847 MB
   node 4 cpus: 32 33 34 35 36 37 38 39 160 161 162 163 164 165 166 167
   node 4 size: 129020 MB
   node 4 free: 128796 MB
   node 5 cpus: 40 41 42 43 44 45 46 47 168 169 170 171 172 173 174 175
   node 5 size: 129019 MB
   node 5 free: 128860 MB
   node 6 cpus: 48 49 50 51 52 53 54 55 176 177 178 179 180 181 182 183
   node 6 size: 129020 MB
   node 6 free: 128808 MB
   node 7 cpus: 56 57 58 59 60 61 62 63 184 185 186 187 188 189 190 191
   node 7 size: 129007 MB
   node 7 free: 128818 MB
   node 8 cpus: 64 65 66 67 68 69 70 71 192 193 194 195 196 197 198 199
   node 8 size: 129020 MB
   node 8 free: 128769 MB
   node 9 cpus: 72 73 74 75 76 77 78 79 200 201 202 203 204 205 206 207
   node 9 size: 129019 MB
   node 9 free: 128637 MB
   node 10 cpus: 80 81 82 83 84 85 86 87 208 209 210 211 212 213 214 215
   node 10 size: 129020 MB
```

(Continued on next page)

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Platform Notes (Continued)

```
    node 10 free: 125375 MB
    node 11 cpus: 88 89 90 91 92 93 94 95 216 217 218 219 220 221 222 223
    node 11 size: 129019 MB
    node 11 free: 128774 MB
    node 12 cpus: 96 97 98 99 100 101 102 103 224 225 226 227 228 229 230 231
    node 12 size: 129020 MB
    node 12 free: 128786 MB
    node 13 cpus: 104 105 106 107 108 109 110 111 232 233 234 235 236 237 238 239
    node 13 size: 129019 MB
    node 13 free: 128751 MB
    node 14 cpus: 112 113 114 115 116 117 118 119 240 241 242 243 244 245 246 247
    node 14 size: 128991 MB
    node 14 free: 128750 MB
    node 15 cpus: 120 121 122 123 124 125 126 127 248 249 250 251 252 253 254 255
    node 15 size: 129016 MB
    node 15 free: 128823 MB
    node distances:
    node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
      0:  10  11  12  12  12  12  12  12  32  32  32  32  32  32  32  32
      1:  11  10  12  12  12  12  12  12  32  32  32  32  32  32  32  32
      2:  12  12  10  11  12  12  12  12  32  32  32  32  32  32  32  32
      3:  12  12  11  10  12  12  12  12  32  32  32  32  32  32  32  32
      4:  12  12  12  12  10  11  12  12  32  32  32  32  32  32  32  32
      5:  12  12  12  12  11  10  12  12  32  32  32  32  32  32  32  32
      6:  12  12  12  12  12  12  10  11  32  32  32  32  32  32  32  32
      7:  12  12  12  12  12  12  11  10  32  32  32  32  32  32  32  32
      8:  32  32  32  32  32  32  32  32  10  11  12  12  12  12  12  12
      9:  32  32  32  32  32  32  32  32  11  10  12  12  12  12  12  12
     10:  32  32  32  32  32  32  32  32  12  12  10  11  12  12  12  12
     11:  32  32  32  32  32  32  32  32  12  12  11  10  12  12  12  12
     12:  32  32  32  32  32  32  32  32  12  12  12  12  10  11  12  12
     13:  32  32  32  32  32  32  32  32  12  12  12  12  11  10  12  12
     14:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  10  11
     15:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  11  10


  From /proc/meminfo
    MemTotal:       2113430912 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

  /sbin/tuned-adm active
     Current active profile: throughput-performance

  /usr/bin/lsb_release -d
    Ubuntu 20.04.4 LTS

  From /etc/*release* /etc/*version*
```

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Platform Notes (Continued)

```
    debian_version: bullseye/sid
    os-release:
        NAME="Ubuntu"
        VERSION="20.04.4 LTS (Focal Fossa)"
        ID=ubuntu
        ID_LIKE=debian
        PRETTY_NAME="Ubuntu 20.04.4 LTS"
        VERSION_ID="20.04"
        HOME_URL="https://www.ubuntu.com/"
        SUPPORT_URL="https://help.ubuntu.com/"

 uname -a:
    Linux spa-dev-sut 5.4.0-100-generic #113-Ubuntu SMP Thu Feb 3 18:43:29 UTC 2022 x86_64
    x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                  Not affected
CVE-2018-3620 (L1 Terminal Fault):               Not affected
Microarchitectural Data Sampling:                Not affected
CVE-2017-5754 (Meltdown):                        Not affected
CVE-2018-3639 (Speculative Store Bypass):        Mitigation: Speculative Store
                                                 Bypass disabled via prctl and
                                                 seccomp
CVE-2017-5753 (Spectre variant 1):               Mitigation: usercopy/swapgs
                                                 barriers and __user pointer
                                                 sanitization
CVE-2017-5715 (Spectre variant 2):               Mitigation: Full AMD retpoline,
                                                IBPB: conditional, IBRS_FW, STIBP:
                                                 always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):         Not affected

 run-level 3 Feb 24 19:35

 SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-aocc320-A1
    Filesystem      Type   Size  Used Avail Use% Mounted on
    tmpfs           tmpfs  125G  3.3G  122G   3% /mnt/ramdisk

 From /sys/devices/virtual/dmi/id
     Vendor:          Dell Inc.
     Product:         PowerEdge R7525
     Product Family:  PowerEdge
     Serial:          48LN333

 Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
 interpret this section. The 'dmidecode' program reads system data which is "intended to
```

(Continued on next page)

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Platform Notes (Continued)

```
 allow hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     16x 802C8632802C 72ASS16G72LZ-3G2B3 128 GB 4 rank 3200
     16x Not Specified Not Specified

 BIOS:
    BIOS Vendor:        Dell Inc.
    BIOS Version:       2.6.5
    BIOS Date:          12/28/2021
    BIOS Revision:      2.6

 (End of data from sysinfo program)
```

Compiler Version Notes

```
================================================================================
C        | 502.gcc_r(peak)
--------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
--------------------------------------------------------------------------------


================================================================================
C        | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
         | 525.x264_r(base, peak) 557.xz_r(base, peak)
--------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
--------------------------------------------------------------------------------


================================================================================
C        | 502.gcc_r(peak)
--------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
--------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
==============================================================================
C          | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
           | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C++        | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C++        | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
           | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C++        | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
C++        | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
           | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
```

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Dell Inc.

PowerEdge R7525 (AMD EPYC 7773X 64-Core Processor)

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SPECrate®2017_int_peak = 887

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Tested by: Dell Inc.

Test Date: Feb-2022
Hardware Availability: Mar-2022
Software Availability: Feb-2022

Compiler Version Notes (Continued)

```
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------


==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
  LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Base Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-DSPEC_LP64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

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Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-loop-fusion -z muldefs -lamdlibm -ljemalloc -lflang
```

C++ benchmarks:

```
-m64 -std=c++98 -flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-mllvm -enable-loop-fusion -z muldefs -fvirtual-function-elimination
-fvisibility=hidden -lamdlibm -ljemalloc -lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-loop-fusion -O3 -march=znver3 -fveclib=AMDLIBM
-ffast-math -z muldefs -mllvm -unroll-aggressive
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

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Test Sponsor: Dell Inc.		**Hardware Availability:**	Mar-2022
Tested by: Dell Inc.		**Software Availability:**	Feb-2022

Base Other Flags (Continued)

C++ benchmarks:
`-Wno-unused-command-line-argument`

Peak Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Peak Portability Flags

500.perlbench_r: `-DSPEC_LINUX_X64 -DSPEC_LP64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LINUX -DSPEC_LP64`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

Peak Optimization Flags

C benchmarks:

500.perlbench_r: `-m64 -Wl,-allow-multiple-definition`
`-Wl,-mllvm -Wl,-enable-licm-vrp -flto`
`-Wl,-mllvm -Wl,-function-specialize`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-fprofile-instr-generate`(pass 1)
`-fprofile-instr-use`(pass 2) `-Ofast -march=znver3`

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Peak Optimization Flags (Continued)

500.perlbench_r (continued):
```
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=false
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

502.gcc_r: `-m32 -Wl,-allow-multiple-definition`
```
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -fgnu89-inline
-ljemalloc
```

505.mcf_r: `-m64 -Wl,-allow-multiple-definition`
```
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc
```

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: `-m64 -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize`
```
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
```

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Peak Optimization Flags (Continued)

520.omnetpp_r (continued):
```
-mllvm -global-vectorize-slp=true
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -ljemalloc
```

523.xalancbmk_r: `-m32 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto`
```
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-flv-function-specialization -mllvm -enable-licm-vrp
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-ljemalloc
```

531.deepsjeng_r: `basepeak = yes`

541.leela_r: `basepeak = yes`

Fortran benchmarks:

548.exchange2_r: `basepeak = yes`

Peak Other Flags

C benchmarks (except as noted below):
`-Wno-unused-command-line-argument`

502.gcc_r: `-L/usr/lib -Wno-unused-command-line-argument`
`-L/sppo/bin/cpu2017v118-aocc3-milanX/amd_rate_aocc320_milanx_A_lib/lib32`

C++ benchmarks (except as noted below):
`-Wno-unused-command-line-argument`

523.xalancbmk_r: `-L/usr/lib -Wno-unused-command-line-argument`
`-L/sppo/bin/cpu2017v118-aocc3-milanX/amd_rate_aocc320_milanx_A_lib/lib32`

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Tested by:	Dell Inc.	**Software Availability:**	Feb-2022

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.4.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-Milan-rev2.4.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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