



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## NEC Corporation

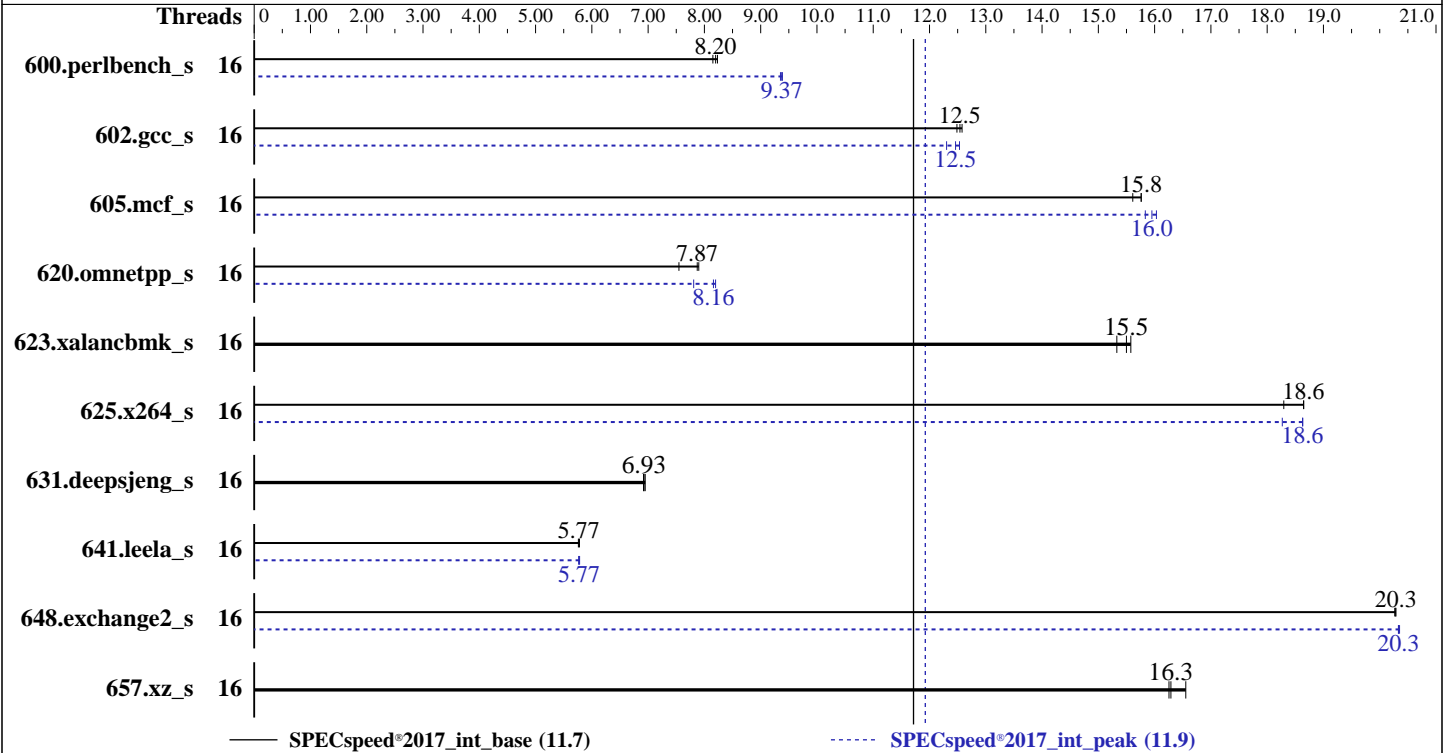
SPECspeed®2017\_int\_base = 11.7

### Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Feb-2020  
Hardware Availability: Jan-2020  
Software Availability: Sep-2019



### Hardware

CPU Name: Intel Xeon E-2278G  
Max MHz: 5000  
Nominal: 3400  
Enabled: 8 cores, 1 chip, 2 threads/core  
Orderable: 1 chip  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 256 KB I+D on chip per core  
L3: 16 MB I+D on chip per chip  
Other: None  
Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2666V-E)  
Storage: 1 x 1 TB SATA, 7200 RPM, RAID 0  
Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.7 (Maipo)  
Kernel 3.10.0-1062.1.1.el7.x86\_64  
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;  
Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
Parallel: Yes  
Firmware: NEC BIOS Version U43 v2.12 12/06/2019 released Mar-2020  
File System: ext4  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_int\_base = 11.7

Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Feb-2020  
Hardware Availability: Jan-2020  
Software Availability: Sep-2019

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	218	8.15	216	8.23	<u>216</u>	<u>8.20</u>	16	190	9.35	<u>189</u>	<u>9.37</u>	189	9.39
602.gcc_s	16	319	12.5	317	12.6	<u>318</u>	<u>12.5</u>	16	318	12.5	<u>320</u>	<u>12.5</u>	324	12.3
605.mcf_s	16	<u>300</u>	<u>15.8</u>	300	15.8	302	15.6	16	294	16.0	<u>296</u>	<u>16.0</u>	298	15.8
620.omnetpp_s	16	206	7.90	<u>207</u>	<u>7.87</u>	216	7.55	16	209	7.81	199	8.20	<u>200</u>	<u>8.16</u>
623.xalancbmk_s	16	91.0	15.6	<u>91.4</u>	<u>15.5</u>	92.4	15.3	16	91.0	15.6	<u>91.4</u>	<u>15.5</u>	92.4	15.3
625.x264_s	16	94.6	18.7	96.4	18.3	<u>94.6</u>	<u>18.6</u>	16	96.6	18.3	<u>94.7</u>	<u>18.6</u>	94.7	18.6
631.deepsjeng_s	16	207	6.92	<u>207</u>	<u>6.93</u>	206	6.95	16	207	6.92	<u>207</u>	<u>6.93</u>	206	6.95
641.leela_s	16	296	5.76	<u>296</u>	<u>5.77</u>	295	5.78	16	296	5.77	<u>296</u>	<u>5.77</u>	295	5.78
648.exchange2_s	16	145	20.3	145	20.3	<u>145</u>	<u>20.3</u>	16	144	20.4	<u>145</u>	<u>20.3</u>	145	20.3
657.xz_s	16	373	16.6	<u>380</u>	<u>16.3</u>	380	16.3	16	373	16.6	<u>380</u>	<u>16.3</u>	380	16.3

SPECspeed®2017\_int\_base = 11.7

SPECspeed®2017\_int\_peak = 11.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_int\_base = 11.7

### Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Feb-2020  
**Hardware Availability:** Jan-2020  
**Software Availability:** Sep-2019

## General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS Settings:

Thermal Configuration: Maximum Cooling  
Intel Virtualization Technology (Intel VT): Disabled

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
running on r110j1 Wed Feb 26 18:58:13 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) E-2278G CPU @ 3.40GHz
 1 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 8
  siblings     : 16
 physical 0:   cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                16
On-line CPU(s) list:   0-15
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):             1
NUMA node(s):         1
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 158
Model name:            Intel(R) Xeon(R) E-2278G CPU @ 3.40GHz
Stepping:              13
CPU MHz:               4908.666
CPU max MHz:           5000.0000
CPU min MHz:           800.0000
BogoMIPS:              6816.00
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_int\_base = 11.7

### Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Feb-2020  
**Hardware Availability:** Jan-2020  
**Software Availability:** Sep-2019

## Platform Notes (Continued)

```

Virtualization:      VT-x
L1d cache:          32K
L1i cache:          32K
L2 cache:           256K
L3 cache:           16384K
NUMA node0 CPU(s):  0-15
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdc cmov pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb invpcid_single intel_pt ssbd
ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt
xsaveopt xsavec xgetbv1 dtherm ida arat pln pts hwp hwp_notify hwp_act_window
hwp_epp md_clear spec_ctrl intel_stibp flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 16384 KB

```

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 32617 MB
node 0 free: 31380 MB
node distances:
node 0
0: 10

```

```

From /proc/meminfo
MemTotal:      32789116 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.7 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.7"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.7 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)

```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_int\_base = 11.7

### Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Feb-2020  
**Hardware Availability:** Jan-2020  
**Software Availability:** Sep-2019

## Platform Notes (Continued)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.7:ga:server

uname -a:

```
Linux r110j1 3.10.0-1062.1.1.el7.x86_64 #1 SMP Tue Aug 13 18:39:59 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: Load fences, usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Full retpoline, IBPB

run-level 3 Feb 26 18:52

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	ext4	908G	41G	821G	5%	/

From /sys/devices/virtual/dmi/id

```
BIOS: NEC U43 12/06/2019
Vendor: NEC
Product: Express5800/R110j-1
Serial: CN69380JHR
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
2x UNKNOWN NOT AVAILABLE
2x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666
```

(End of data from sysinfo program)

## Compiler Version Notes

```
=====  
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,  
      | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)  
=====
```

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## NEC Corporation

SPECspeed®2017\_int\_base = 11.7

Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Test Date:** Feb-2020  
**Hardware Availability:** Jan-2020  
**Software Availability:** Sep-2019

### Compiler Version Notes (Continued)

Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)  
=====

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

=====  
Fortran | 648.exchange2\_s(base, peak)  
=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

### Base Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

### Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017\_int\_base = 11.7

Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

CPU2017 License: 9006  
Test Sponsor: NEC Corporation  
Tested by: NEC Corporation

Test Date: Feb-2020  
Hardware Availability: Jan-2020  
Software Availability: Sep-2019

## Base Portability Flags (Continued)

657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs

## Peak Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017\_int\_base = 11.7

Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

CPU2017 License: 9006

Test Sponsor: NEC Corporation

Tested by: NEC Corporation

Test Date: Feb-2020

Hardware Availability: Jan-2020

Software Availability: Sep-2019

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

```
620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
```

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

```
641.leela_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs
```





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

NEC Corporation

SPECspeed®2017\_int\_base = 11.7

Express5800/R110j-1 (Intel Xeon E-2278G)

SPECspeed®2017\_int\_peak = 11.9

**CPU2017 License:** 9006

**Test Date:** Feb-2020

**Test Sponsor:** NEC Corporation

**Hardware Availability:** Jan-2020

**Tested by:** NEC Corporation

**Software Availability:** Sep-2019

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.html>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-R110j-RevD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic19.0u1-official-linux64.2019-07-09.xml>

<http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-R110j-RevD.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2020-02-26 04:58:12-0500.

Report generated on 2020-04-14 14:03:04 by CPU2017 PDF formatter v6255.

Originally published on 2020-04-14.