



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_base = 361

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

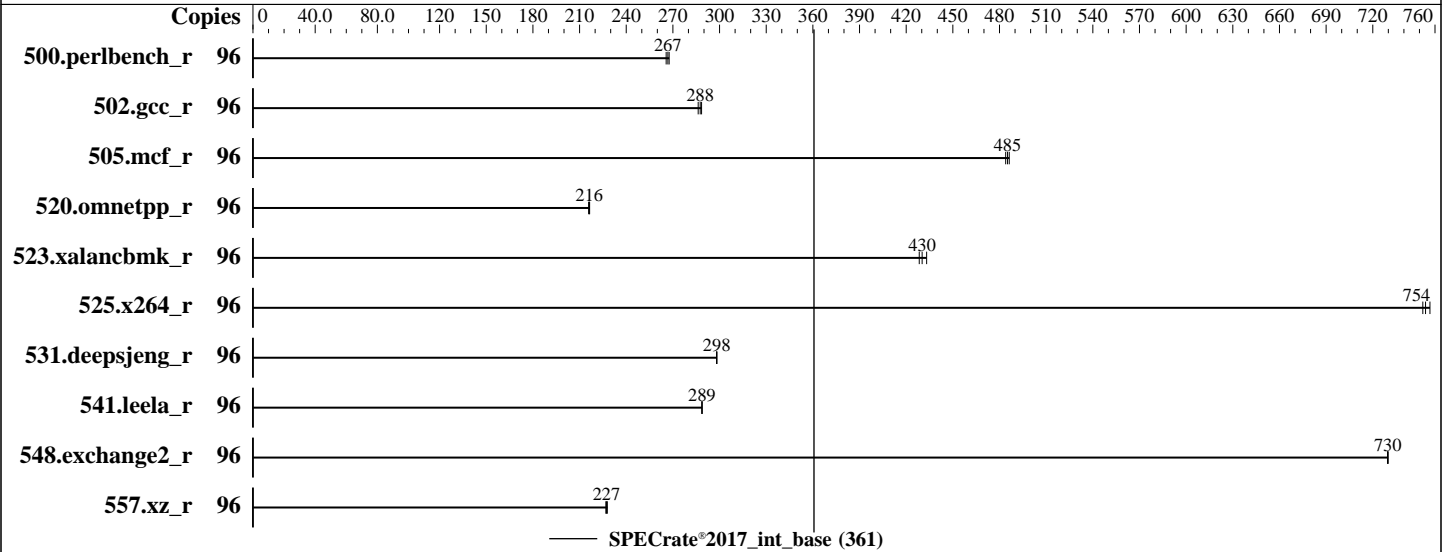
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2019

Hardware Availability: Jul-2019

Software Availability: May-2019



### Hardware

CPU Name: Intel Xeon Gold 6246  
 Max MHz: 4200  
 Nominal: 3300  
 Enabled: 48 cores, 4 chips, 2 threads/core  
 Orderable: 2,3,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 24.75 MB I+D on chip per chip  
 Other: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2933Y-R)  
 Storage: 1 x 800 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 (x86\_64)  
 Kernel 4.12.14-25.13-default  
 Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 19.0.4.227 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Lenovo BIOS Version PSE122R 1.53 released Aug-2019 tested as PSE121R 1.53 Jul-2019  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None  
 Power Management: --



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_base = 361

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2019  
**Hardware Availability:** Jul-2019  
**Software Availability:** May-2019

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	575	266	<b><u>573</u></b>	<b><u>267</u></b>	571	268							
502.gcc_r	96	471	288	475	286	<b><u>473</u></b>	<b><u>288</u></b>							
505.mcf_r	96	319	486	<b><u>320</u></b>	<b><u>485</u></b>	321	484							
520.omnetpp_r	96	584	216	<b><u>582</u></b>	<b><u>216</u></b>	582	216							
523.xalancbmk_r	96	234	433	237	428	<b><u>236</u></b>	<b><u>430</u></b>							
525.x264_r	96	222	757	223	752	<b><u>223</u></b>	<b><u>754</u></b>							
531.deepsjeng_r	96	<b><u>369</u></b>	<b><u>298</u></b>	369	298	369	298							
541.leela_r	96	551	289	<b><u>551</u></b>	<b><u>289</u></b>	551	289							
548.exchange2_r	96	345	729	<b><u>345</u></b>	<b><u>730</u></b>	345	730							
557.xz_r	96	457	227	455	228	<b><u>456</u></b>	<b><u>227</u></b>							

SPECrate®2017\_int\_base = 361

SPECrate®2017\_int\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017-1.0.5-ic19.0u4/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECrate®2017\_int\_base = 361

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2019  
**Hardware Availability:** Jul-2019  
**Software Availability:** May-2019

## General Notes (Continued)

is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance  
Choose Operating Mode set to Custom Mode  
DCU Streamer Prefetcher set to Disable  
MONITOR/MWAIT set to Enable  
SNC set to Enable  
Sysinfo program /home/cpu2017-1.0.5-ic19.0u4/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-ulb8 Wed Aug 7 14:26:39 2019

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6246 CPU @ 3.30GHz  
4 "physical id"s (chips)  
96 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 12  
siblings : 24  
physical 0: cores 0 2 4 8 9 10 11 17 18 19 25 27  
physical 1: cores 0 1 2 3 4 8 10 11 17 18 25 27  
physical 2: cores 1 2 3 4 10 11 16 17 18 24 25 27  
physical 3: cores 1 2 4 8 9 10 11 16 17 19 26 27

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 96  
On-line CPU(s) list: 0-95  
Thread(s) per core: 2  
Core(s) per socket: 12  
Socket(s): 4  
NUMA node(s): 8  
Vendor ID: GenuineIntel

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 361

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2019

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2019

Tested by: Lenovo Global Technology

Software Availability: May-2019

### Platform Notes (Continued)

```

CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Gold 6246 CPU @ 3.30GHz
Stepping:             7
CPU MHz:              3300.000
CPU max MHz:          4200.0000
CPU min MHz:          1200.0000
BogoMIPS:             6600.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             25344K
NUMA node0 CPU(s):   0,1,3,4,7,10,48,49,51,52,55,58
NUMA node1 CPU(s):   2,5,6,8,9,11,50,53,54,56,57,59
NUMA node2 CPU(s):   12-14,17,20,22,60-62,65,68,70
NUMA node3 CPU(s):   15,16,18,19,21,23,63,64,66,67,69,71
NUMA node4 CPU(s):   24,25,30,31,33,34,72,73,78,79,81,82
NUMA node5 CPU(s):   26-29,32,35,74-77,80,83
NUMA node6 CPU(s):   36,37,39,40,43,44,84,85,87,88,91,92
NUMA node7 CPU(s):   38,41,42,45-47,86,89,90,93-95
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f
avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
dtherm ida arat pln pts pku ospke avx512_vnni flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 25344 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 8 nodes (0-7)
node 0 cpus: 0 1 3 4 7 10 48 49 51 52 55 58
node 0 size: 96359 MB
node 0 free: 94960 MB
node 1 cpus: 2 5 6 8 9 11 50 53 54 56 57 59
node 1 size: 96757 MB
node 1 free: 90356 MB
node 2 cpus: 12 13 14 17 20 22 60 61 62 65 68 70
node 2 size: 96757 MB

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Lenovo Global Technology

SPECrate®2017\_int\_base = 361

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2019

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2019

Tested by: Lenovo Global Technology

Software Availability: May-2019

### Platform Notes (Continued)

```

node 2 free: 96612 MB
node 3 cpus: 15 16 18 19 21 23 63 64 66 67 69 71
node 3 size: 96757 MB
node 3 free: 96607 MB
node 4 cpus: 24 25 30 31 33 34 72 73 78 79 81 82
node 4 size: 96728 MB
node 4 free: 96587 MB
node 5 cpus: 26 27 28 29 32 35 74 75 76 77 80 83
node 5 size: 96757 MB
node 5 free: 96597 MB
node 6 cpus: 36 37 39 40 43 44 84 85 87 88 91 92
node 6 size: 96757 MB
node 6 free: 96407 MB
node 7 cpus: 38 41 42 45 46 47 86 89 90 93 94 95
node 7 size: 96754 MB
node 7 free: 92176 MB
node distances:
node  0  1  2  3  4  5  6  7
  0: 10 11 21 21 21 21 21 21
  1: 11 10 21 21 21 21 21 21
  2: 21 21 10 11 21 21 21 21
  3: 21 21 11 10 21 21 21 21
  4: 21 21 21 21 10 11 21 21
  5: 21 21 21 21 11 10 21 21
  6: 21 21 21 21 21 21 10 11
  7: 21 21 21 21 21 21 11 10

```

```

From /proc/meminfo
MemTotal:      792197316 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

```

```

uname -a:
Linux linux-ulb8 4.12.14-25.13-default #1 SMP Tue Aug 14 15:07:35 UTC 2018 (947aa51)
x86_64 x86_64 x86_64 GNU/Linux

```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_base = 361

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2019  
**Hardware Availability:** Jul-2019  
**Software Availability:** May-2019

### Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected  
CVE-2017-5753 (Spectre variant 1): Mitigation: \_\_user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS\_FW

run-level 3 Aug 7 14:06

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u4  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 xfs 744G 45G 700G 6% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[PSE121R-1.53]- 07/03/2019  
Memory:  
48x NO DIMM NO DIMM  
48x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base)  
525.x264\_r(base) 557.xz\_r(base)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 548.exchange2\_r(base)  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECrate®2017\_int\_base = 361

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Aug-2019

Test Sponsor: Lenovo Global Technology

Hardware Availability: Jul-2019

Tested by: Lenovo Global Technology

Software Availability: May-2019

## Compiler Version Notes (Continued)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR950  
(3.30 GHz, Intel Xeon Gold 6246)

SPECrate®2017\_int\_base = 361

SPECrate®2017\_int\_peak = Not Run

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2019

**Hardware Availability:** Jul-2019

**Software Availability:** May-2019

## Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.html>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.xml>

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.0.5 on 2019-08-07 02:26:39-0400.

Report generated on 2019-09-17 16:15:36 by CPU2017 PDF formatter v6255.

Originally published on 2019-09-17.