



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

SPECSpeed®2017\_fp\_peak = 105

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

CPU2017 License: 55

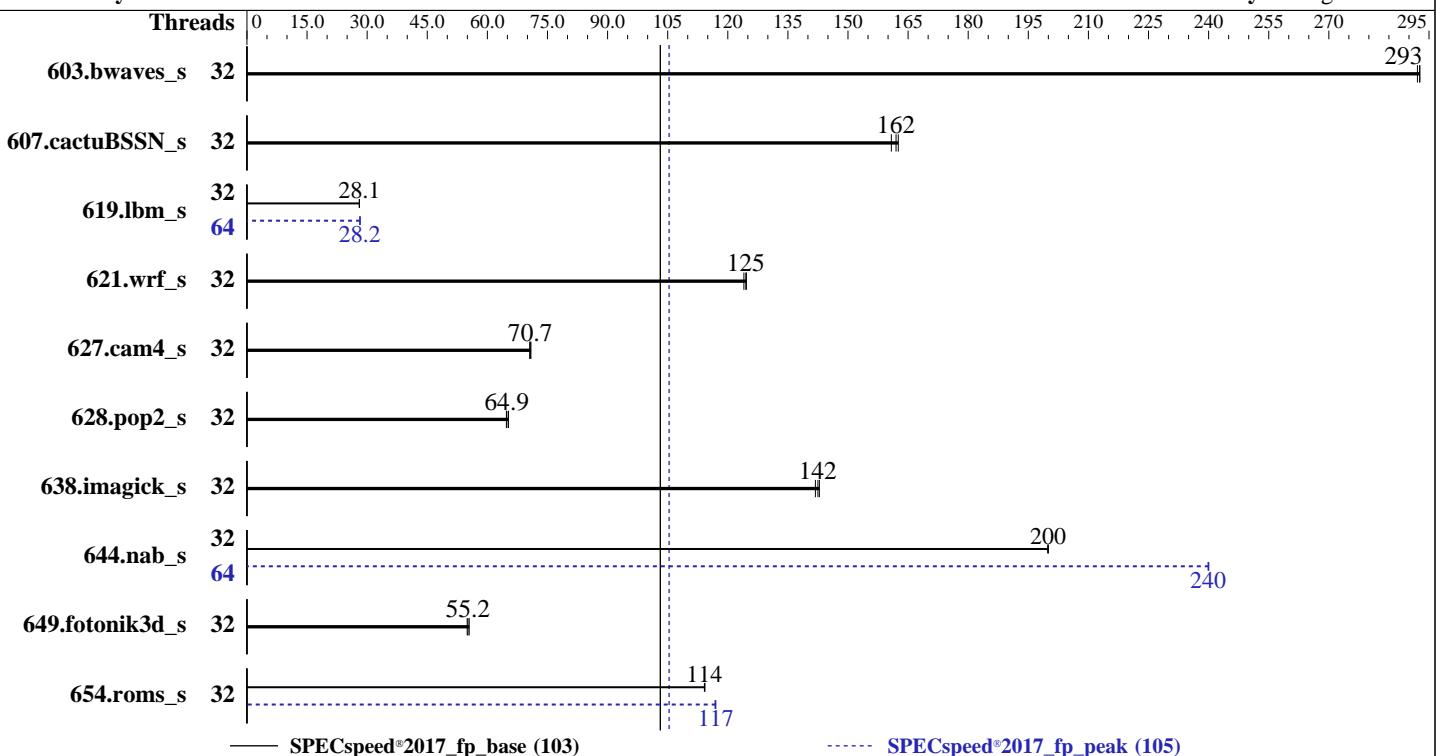
Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019



Hardware		Software	
CPU Name:	AMD EPYC 7502P	OS:	SUSE Linux Enterprise Server 15 SP1
Max MHz:	3350		kernel 4.12.14-195-default
Nominal:	2500		4.12.14-195-default
Enabled:	32 cores, 1 chip, 2 threads/core	Compiler:	C/C++/Fortran: Version 2.0.0 of AOCC
Orderable:	1 chip	Parallel:	Yes
Cache L1:	32 KB I + 32 KB D on chip per core	Firmware:	Version 1.0.3 released Aug-2019
L2:	512 KB I+D on chip per core	File System:	xfs
L3:	128 MB I+D on chip per chip, 16 MB shared / 4 cores	System State:	Run level 3 (multi-user)
Other:	None	Base Pointers:	64-bit
Memory:	256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R, running at 3200)	Peak Pointers:	64-bit
Storage:	1 x 960 GB SATA SSD	Other:	jemalloc: jemalloc memory allocator library v5.1.0
Other:	None	Power Management:	--



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Aug-2019  
Hardware Availability: Sep-2019  
Software Availability: Aug-2019

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	<b>202</b>	<b>293</b>	202	293	202	292	32	<b>202</b>	<b>293</b>	202	293	202	292
607.cactuBSSN_s	32	104	161	<b>103</b>	<b>162</b>	103	163	32	104	161	<b>103</b>	<b>162</b>	103	163
619.lbm_s	32	187	28.0	<b>187</b>	<b>28.1</b>	187	28.1	64	<b>186</b>	<b>28.2</b>	186	28.2	186	28.2
621.wrf_s	32	<b>106</b>	<b>125</b>	107	124	106	125	32	<b>106</b>	<b>125</b>	107	124	106	125
627.cam4_s	32	125	70.9	<b>125</b>	<b>70.7</b>	126	70.6	32	125	70.9	<b>125</b>	<b>70.7</b>	126	70.6
628.pop2_s	32	182	65.3	<b>183</b>	<b>64.9</b>	183	64.7	32	182	65.3	<b>183</b>	<b>64.9</b>	183	64.7
638.imagick_s	32	<b>101</b>	<b>142</b>	101	143	102	142	32	<b>101</b>	<b>142</b>	101	143	102	142
644.nab_s	32	<b>87.4</b>	<b>200</b>	87.4	200	87.4	200	64	72.8	240	72.8	240	<b>72.8</b>	<b>240</b>
649.fotonik3d_s	32	166	55.0	164	55.5	<b>165</b>	<b>55.2</b>	32	166	55.0	164	55.5	<b>165</b>	<b>55.2</b>
654.roms_s	32	<b>138</b>	<b>114</b>	138	114	138	114	32	<b>135</b>	<b>117</b>	135	117	135	117
SPECSpeed®2017_fp_base = 103							SPECSpeed®2017_fp_peak = 105							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

Set dirty\_ratio=8 to limit dirty cache to 8% of memory  
Set swappiness=1 to swap only if necessary  
Set zone\_reclaim\_mode=1 to free local node memory and avoid remote memory sync then drop\_caches=3 to reset caches before invoking runcpu

dirty\_ratio, swappiness, zone\_reclaim\_mode and drop\_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55

Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019

## General Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-63"  
LD_LIBRARY_PATH = "/root/cpu2017-1.0.5/amd_speed_aocc200_rome_B_lib/64;  
/root/cpu2017-1.0.5/amd_speed_aocc200_rome_B_lib/32"  
MALLOC_CONF = "retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "64"
```

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto  
jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

## Platform Notes

BIOS settings:

```
NUMA Nodes Per Socket set to 4  
CCX as NUMA Domain set to Enabled  
System Profile set to Custom  
CPU Power Management set to Maximum Performance  
Memory Frequency set to Maximum Performance  
Turbo Boost Enabled  
C states set to Enabled  
Memory Patrol Scrub Disabled  
Memory Refresh Rate set to 1x  
PCI ASPM L1 Link Power Management Disabled  
Determinism Slider set to Power Determinism  
Efficiency Optimized Mode Disabled  
Sysinfo program /root/cpu2017-1.0.5/bin/sysinfo  
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-g3ob Tue Aug 27 12:56:02 2019
```

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Aug-2019  
Hardware Availability: Sep-2019  
Software Availability: Aug-2019

## Platform Notes (Continued)

```
From /proc/cpuinfo
model name : AMD EPYC 7502P 32-Core Processor
  1 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 32
  siblings : 64
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31
```

```
From lscpu:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
Address sizes:         43 bits physical, 48 bits virtual
CPU(s):                64
On-line CPU(s) list:  0-63
Thread(s) per core:   2
Core(s) per socket:   32
Socket(s):             1
NUMA node(s):          8
Vendor ID:             AuthenticAMD
CPU family:            23
Model:                 49
Model name:            AMD EPYC 7502P 32-Core Processor
Stepping:               0
CPU MHz:                2495.461
BogoMIPS:              4990.92
Virtualization:        AMD-V
L1d cache:              32K
L1i cache:              32K
L2 cache:                512K
L3 cache:                16384K
NUMA node0 CPU(s):    0-3,32-35
NUMA node1 CPU(s):    4-7,36-39
NUMA node2 CPU(s):    8-11,40-43
NUMA node3 CPU(s):    12-15,44-47
NUMA node4 CPU(s):    16-19,48-51
NUMA node5 CPU(s):    20-23,52-55
NUMA node6 CPU(s):    24-27,56-59
NUMA node7 CPU(s):    28-31,60-63
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                        pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                        constant_tsc rep_good nopl xtTopology nonstop_tsc cpuid extd_apicid aperfmpfperf pnpi
                        pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55

Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019

## Platform Notes (Continued)

```
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch  
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx cpb  
cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep  
bmi2 cqmq rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves  
cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local clzero irperf xsaverptr arat npt  
lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter  
pfthreshold avic v_vmsave_vmlload vgif umip rdpid overflow_recov succor smca
```

```
/proc/cpuinfo cache data  
cache size : 512 KB
```

From numactl --hardware    WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)  
node 0 cpus: 0 1 2 3 32 33 34 35  
node 0 size: 31691 MB  
node 0 free: 31356 MB  
node 1 cpus: 4 5 6 7 36 37 38 39  
node 1 size: 32253 MB  
node 1 free: 32154 MB  
node 2 cpus: 8 9 10 11 40 41 42 43  
node 2 size: 32254 MB  
node 2 free: 32196 MB  
node 3 cpus: 12 13 14 15 44 45 46 47  
node 3 size: 32253 MB  
node 3 free: 32203 MB  
node 4 cpus: 16 17 18 19 48 49 50 51  
node 4 size: 32224 MB  
node 4 free: 32160 MB  
node 5 cpus: 20 21 22 23 52 53 54 55  
node 5 size: 32253 MB  
node 5 free: 32184 MB  
node 6 cpus: 24 25 26 27 56 57 58 59  
node 6 size: 32254 MB  
node 6 free: 32181 MB  
node 7 cpus: 28 29 30 31 60 61 62 63  
node 7 size: 32240 MB  
node 7 free: 32188 MB  
node distances:  
node 0 1 2 3 4 5 6 7  
0: 10 11 12 12 12 12 12 12  
1: 11 10 12 12 12 12 12 12  
2: 12 12 10 11 12 12 12 12  
3: 12 12 11 10 12 12 12 12  
4: 12 12 12 12 10 11 12 12  
5: 12 12 12 12 11 10 12 12  
6: 12 12 12 12 12 12 10 11
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Aug-2019  
Hardware Availability: Sep-2019  
Software Availability: Aug-2019

## Platform Notes (Continued)

7: 12 12 12 12 12 12 11 10

```
From /proc/meminfo
MemTotal:      263606296 kB
HugePages_Total:       0
Hugepagesize:     2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="SLES"
  VERSION="15-SP1"
  VERSION_ID="15.1"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
Linux linux-g3ob 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):          Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional,
IBRS_FW, STIBP: conditional, RSB filling

run-level 3 Aug 26 15:27 last=5

SPEC is set to: /root/cpu2017-1.0.5
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        xfs   440G   39G  402G   9%  /


Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.3 08/23/2019
Memory:
 8x 802C8632802C 36ASF4G72PZ-3G2E2 32 GB 2 rank 3200
 8x Not Specified Not Specified

(End of data from sysinfo program)
```



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_base = 103

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55

Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019

## Compiler Version Notes

=====

C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak)  
| 644.nab\_s(base, peak)

=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====

C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====

Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)

=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins  
AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

=====

Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
| 628.pop2\_s(base, peak)

=====

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECspeed®2017\_fp\_peak = 105

CPU2017 License: 55

Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019

## Compiler Version Notes (Continued)

AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)  
Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

AOCC.LLVM.2.0.0.B191.2019\_07\_19 clang version 8.0.0 (CLANG: Jenkins

AOCC\_2\_0\_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019\_07\_19)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Aug-2019  
Hardware Availability: Sep-2019  
Software Availability: Aug-2019

## Base Optimization Flags

C benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm
-ljemalloc -lflang
```

Fortran benchmarks:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

```
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-freemap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp -DUSE_OPENMP
-fopenmp=libomp -lomp -lpthread -ldl -lmvec -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -freemap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55

Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-DSPEC\_OPENMP -fopenmp -DUSE\_OPENMP -fopenmp=libomp -lomp -lpthread  
-ldl -lmvec -lamdlibm -ljemalloc -flang

## Base Other Flags

C benchmarks:

-Wno-return-type

Fortran benchmarks:

-Wno-return-type

Benchmarks using both Fortran and C:

-Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_base = 103

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Aug-2019  
Hardware Availability: Sep-2019  
Software Availability: Aug-2019

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -lmvec -lamdlibm -fopenmp=libomp -lomp
-lpthread -ldl -ljemalloc -lflang
```

638.imagick\_s: basepeak = yes

644.nab\_s: Same as 619.lbm\_s

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

```
654.roms_s: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -DSPEC_OPENMP -fopenmp
-DUSE_OPENMP -fopenmp=libomp -lomp -lpthread -ldl
-lmvec -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_fp\_base = 103

PowerEdge R6515 (AMD EPYC 7502P, 2.50 GHz)

SPECSpeed®2017\_fp\_peak = 105

CPU2017 License: 55

Test Date: Aug-2019

Test Sponsor: Dell Inc.

Hardware Availability: Sep-2019

Tested by: Dell Inc.

Software Availability: Aug-2019

## Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

607.cactubSSN\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-return-type

Fortran benchmarks:

-Wno-return-type

Benchmarks using both Fortran and C:

-Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc200-flags-C1-Dell.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE4.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc200-flags-C1-Dell.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE4.xml>

SPEC CPU and SPECSpeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.0.5 on 2019-08-27 13:56:02-0400.

Report generated on 2019-09-17 16:05:32 by CPU2017 PDF formatter v6255.

Originally published on 2019-09-17.