



# SPEC® CINT2006 Result

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## Huawei

**SPECint®2006 = 61.8**

Huawei 2288H V5 (Intel Xeon Silver 4114)

**SPECint\_base2006 = 59.2**

CPU2006 license: 3175

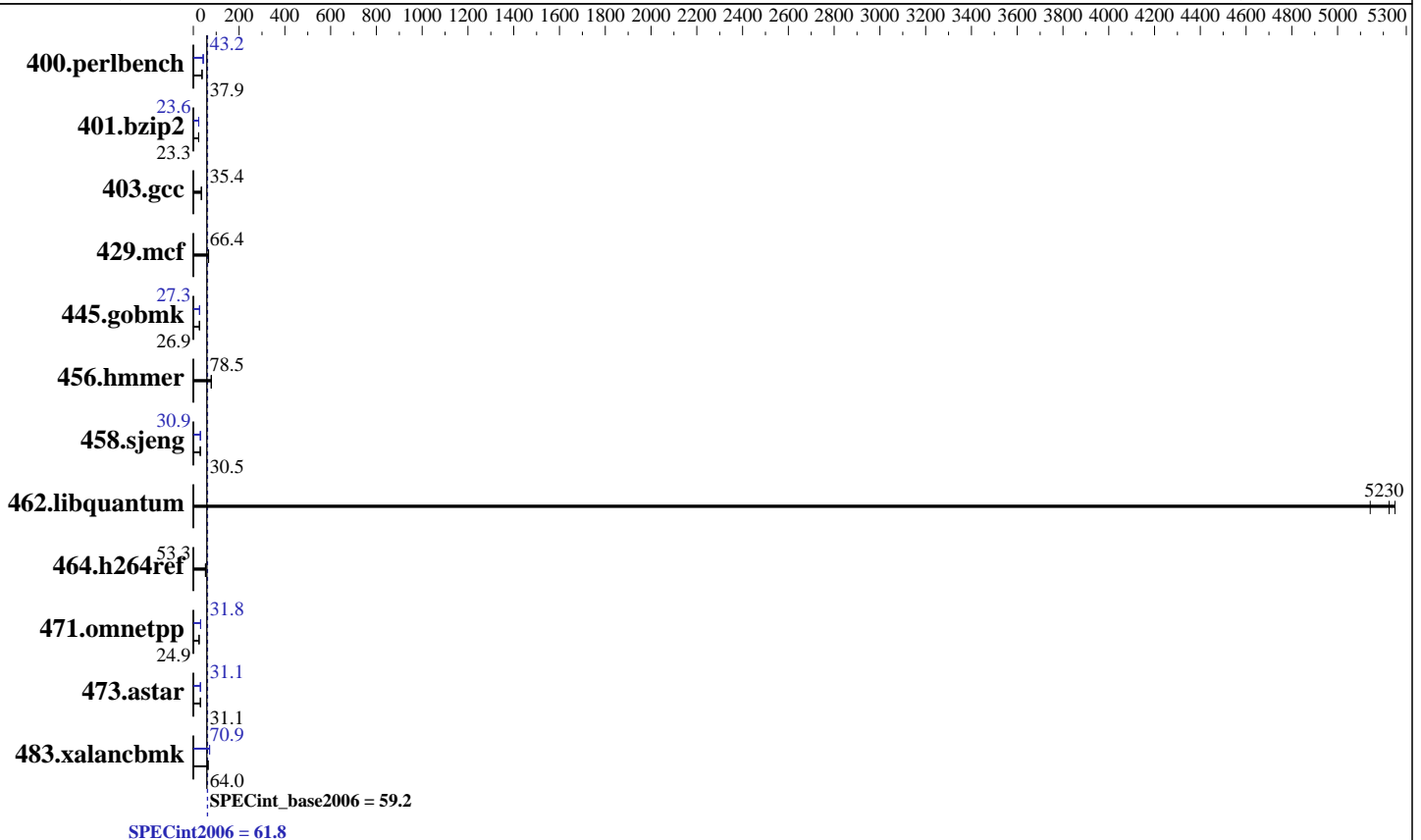
Test date: Jul-2017

Test sponsor: Huawei

Hardware Availability: Sep-2017

Tested by: Huawei

Software Availability: Nov-2016



### Hardware

CPU Name: Intel Xeon Silver 4114  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 13.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R, running at 2400 MHz)  
 Disk Subsystem: 1 x 1200 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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### Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	257	38.1	258	37.9	<b><u>257</u></b>	<b><u>37.9</u></b>	<b><u>226</u></b>	<b><u>43.2</u></b>	227	43.1	226	43.2
401.bzip2	414	23.3	<b><u>414</u></b>	<b><u>23.3</u></b>	414	23.3	409	23.6	407	23.7	<b><u>409</u></b>	<b><u>23.6</u></b>
403.gcc	227	35.4	228	35.3	<b><u>228</u></b>	<b><u>35.4</u></b>	227	35.4	228	35.3	<b><u>228</u></b>	<b><u>35.4</u></b>
429.mcf	136	66.9	138	66.1	<b><u>137</u></b>	<b><u>66.4</u></b>	136	66.9	138	66.1	<b><u>137</u></b>	<b><u>66.4</u></b>
445.gobmk	391	26.9	<b><u>390</u></b>	<b><u>26.9</u></b>	390	26.9	384	27.3	385	27.3	<b><u>385</u></b>	<b><u>27.3</u></b>
456.hammer	<b><u>119</u></b>	<b><u>78.5</u></b>	119	78.5	119	78.7	<b><u>119</u></b>	<b><u>78.5</u></b>	119	78.5	119	78.7
458.sjeng	397	30.5	397	30.5	<b><u>397</u></b>	<b><u>30.5</u></b>	391	30.9	<b><u>391</u></b>	<b><u>30.9</u></b>	391	30.9
462.libquantum	3.95	5250	<b><u>3.97</u></b>	<b><u>5230</u></b>	4.03	5140	3.95	5250	<b><u>3.97</u></b>	<b><u>5230</u></b>	4.03	5140
464.h264ref	<b><u>415</u></b>	<b><u>53.3</u></b>	415	53.3	416	53.2	<b><u>415</u></b>	<b><u>53.3</u></b>	415	53.3	416	53.2
471.omnetpp	<b><u>251</u></b>	<b><u>24.9</u></b>	251	24.9	249	25.1	196	31.8	196	32.0	<b><u>196</u></b>	<b><u>31.8</u></b>
473.astar	<b><u>226</u></b>	<b><u>31.1</u></b>	226	31.0	224	31.3	225	31.1	<b><u>225</u></b>	<b><u>31.1</u></b>	226	31.1
483.xalancbmk	108	63.8	108	64.1	<b><u>108</u></b>	<b><u>64.0</u></b>	<b><u>97.3</u></b>	<b><u>70.9</u></b>	96.8	71.3	97.4	70.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Hyper-Threading to Disable  
Sysinfo program /spec17/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on localhost.localdomain Tue Jul 25 21:51:42 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz  
2 "physical id"s (chips)  
20 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 10

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## Platform Notes (Continued)

```

siblings      : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size   : 14080 KB

```

From /proc/meminfo

```

MemTotal:      790482140 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

uname -a:

```

Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jul 25 11:00

SPEC is set to: /spec17

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda2        xfs       898G      19G  880G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.20 07/14/2017

Memory:

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/spec17/libs/32:/spec17/libs/64:/spec17/sh10.2"

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## General Notes (Continued)

OMP\_NUM\_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div -auto-ilp32 -qopt-prefetch

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## Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-ra-region-strategy=block  
-Wl,-z,muldefs -L/sh10.2 -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-Wl,-z,muldefs -L/sh10.2 -lsmarheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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