



SPEC® CFP2006 Result

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Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant BL460c Gen9
(1.70 GHz, Intel Xeon E5-2603 v4)

SPECfp®2006 = 67.8

SPECfp_base2006 = 66.2

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jun-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



Hardware

CPU Name: Intel Xeon E5-2603 v4
CPU Characteristics:
CPU MHz: 1700
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)
Compiler: Kernel 3.10.0-327.el7.x86_64
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R,
running at 1866 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	39.8	342	38.6	352	<u>39.4</u>	<u>345</u>	39.7	342	39.1	347	<u>39.4</u>	<u>345</u>
416.gamess	916	21.4	914	21.4	<u>916</u>	<u>21.4</u>	<u>861</u>	<u>22.7</u>	863	22.7	861	22.7
433.milc	187	49.1	187	49.1	<u>187</u>	<u>49.1</u>	187	49.1	187	49.1	<u>187</u>	<u>49.1</u>
434.zeusmp	<u>65.3</u>	<u>139</u>	65.5	139	64.7	141	<u>65.3</u>	<u>139</u>	65.5	139	64.7	141
435.gromacs	231	30.9	<u>231</u>	<u>30.9</u>	235	30.4	231	30.9	230	31.0	<u>230</u>	<u>31.0</u>
436.cactusADM	26.9	444	26.4	453	<u>26.5</u>	<u>451</u>	26.9	444	26.4	453	<u>26.5</u>	<u>451</u>
437.leslie3d	57.2	164	58.1	162	<u>57.6</u>	<u>163</u>	<u>57.2</u>	<u>164</u>	58.1	162	<u>57.6</u>	<u>163</u>
444.namd	532	15.1	532	15.1	<u>532</u>	<u>15.1</u>	523	15.3	520	15.4	<u>521</u>	<u>15.4</u>
447.dealII	328	34.9	326	35.1	<u>326</u>	<u>35.0</u>	328	34.9	326	35.1	<u>326</u>	<u>35.0</u>
450.soplex	<u>301</u>	<u>27.7</u>	301	27.7	302	27.6	<u>301</u>	<u>27.7</u>	301	27.7	302	27.6
453.povray	<u>174</u>	<u>30.6</u>	174	30.7	175	30.5	154	34.5	152	35.0	<u>153</u>	<u>34.9</u>
454.calculix	259	31.8	<u>259</u>	<u>31.8</u>	260	31.8	254	32.5	<u>254</u>	<u>32.5</u>	254	32.5
459.GemsFDTD	74.6	142	70.1	151	<u>71.1</u>	<u>149</u>	<u>65.0</u>	<u>163</u>	65.4	162	63.7	167
465.tonto	357	27.6	<u>357</u>	<u>27.6</u>	357	27.5	332	29.6	<u>332</u>	<u>29.6</u>	332	29.7
470.lbm	<u>32.0</u>	<u>429</u>	32.5	423	31.9	430	<u>32.0</u>	<u>429</u>	32.5	423	31.9	430
481.wrf	<u>178</u>	<u>62.8</u>	175	63.9	180	62.1	<u>178</u>	<u>62.8</u>	175	63.9	180	62.1
482.sphinx3	390	50.0	388	50.2	<u>389</u>	<u>50.0</u>	<u>389</u>	<u>50.1</u>	388	50.2	389	50.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

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Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling

Processor Power and Utilization Monitoring set to Disabled

Memory Refresh Rate set to 1x Refresh

```
Sysinfo program /home/cpuv1.5/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on BL460c-Gen9-B Wed Jun 1 09:39:38 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2603 v4 @ 1.70GHz
        2 "physical id"s (chips)
        12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 6
        siblings : 6
        physical 0: cores 0 1 2 3 4 5
        physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      263844004 kB
HugePages_Total:      0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.2 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.2"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux BL460c-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jun 1 09:34

```
SPEC is set to: /home/cpuv1.5/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda5        xfs   314G  192G  123G  61%  /home
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```



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Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 02/29/2016

Memory:

8x UNKNOWN NOT AVAILABLE

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 1866 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 1866 MHz

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

OMP_NUM_THREADS = "12"

LD_LIBRARY_PATH = "/home/cpuv1.5/cpu2006/libs/32:/home/cpuv1.5/cpu2006/libs/64:/home/cpuv1.5/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
-fp-model fast=2
```

Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

-nofor-main

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint -funroll-all-loops
-auto-ilp32

-nofor-main

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml>
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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