SPEC® CFP2006 Result
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Huawei

Huawei RH2288 V3 (Intel Xeon E5-2683 v3)

$SPECfp^{®}\_rate2006 =$ 825

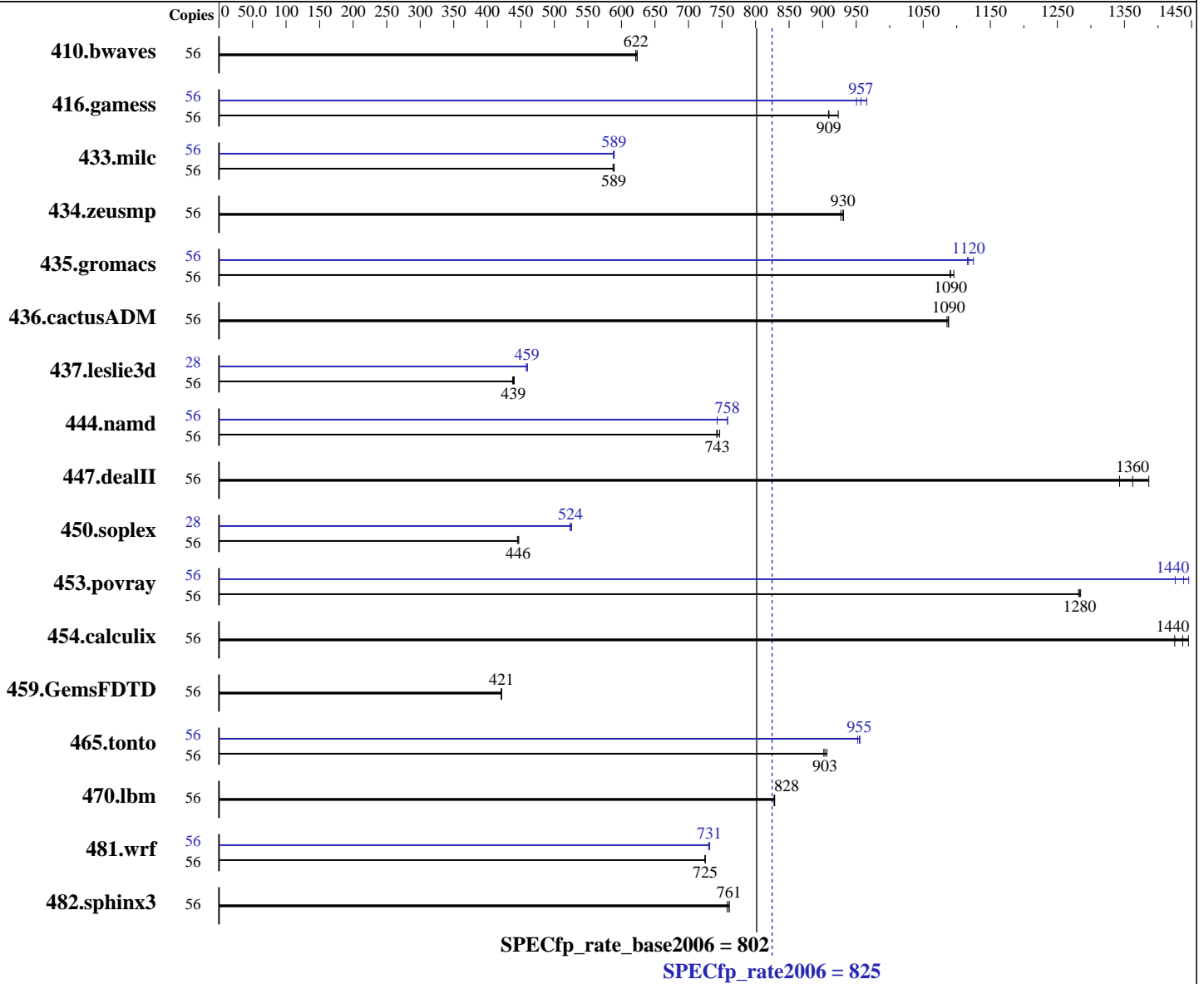SPECfp_rate_base2006 = 802

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Sep-2015
Hardware Availability: Sep-2014
Software Availability: Sep-2014

Copies	Benchmark	Result
56	410.bwaves	622
56	416.gamess	957
56	416.gamess	909
56	433.milc	589
56	433.milc	589
56	434.zeusmp	930
56	435.gromacs	1120
56	435.gromacs	1090
56	436.cactusADM	1090
28	437.leslie3d	459
56	437.leslie3d	439
56	444.namd	758
56	444.namd	743
56	447.dealII	1360
28	450.soplex	524
56	450.soplex	446
56	453.povray	1440
56	453.povray	1280
56	454.calculix	1440
56	459.GemsFDTD	421
56	465.tonto	955
56	465.tonto	903
56	470.lbm	828
56	481.wrf	731
56	481.wrf	725
56	482.sphinx3	761

SPECfp_rate_base2006 = 802

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Hardware

CPU Name: Intel Xeon E5-2683 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
3.10.0-123.el7.x86_64
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4

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L3 Cache:	35 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 500 GB SATA, 7200 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
410.bwaves	56	1220	624	**1224**	**622**	1225	622	56	1220	624	**1224**	**622**	1225	622
416.gamess	56	1207	909	1188	923	**1206**	**909**	56	**1146**	**957**	1135	966	1154	951
433.milc	56	**873**	**589**	875	588	872	589	56	874	588	872	589	**873**	**589**
434.zeusmp	56	**548**	**930**	550	927	547	931	56	**548**	**930**	550	927	547	931
435.gromacs	56	367	1090	365	1100	**367**	**1090**	56	**358**	**1120**	355	1120	358	1120
436.cactusADM	56	615	1090	**615**	**1090**	617	1090	56	615	1090	**615**	**1090**	617	1090
437.leslie3d	56	1203	438	1196	440	**1199**	**439**	28	574	458	572	460	**573**	**459**
444.namd	56	602	747	**604**	**743**	605	742	56	**592**	**758**	592	758	605	743
447.dealII	56	**470**	**1360**	477	1340	462	1390	56	**470**	**1360**	477	1340	462	1390
450.soplex	56	1045	447	1050	445	**1047**	**446**	28	446	524	444	526	**445**	**524**
453.povray	56	232	1280	232	1280	**232**	**1280**	56	**207**	**1440**	209	1430	206	1450
454.calculix	56	324	1430	**322**	**1440**	320	1450	56	324	1430	**322**	**1440**	320	1450
459.GemsFDTD	56	1412	421	1410	421	**1411**	**421**	56	1412	421	1410	421	**1411**	**421**
465.tonto	56	**610**	**903**	611	902	608	906	56	577	956	579	952	**577**	**955**
470.lbm	56	930	828	**929**	**828**	929	829	56	930	828	**929**	**828**	929	829
481.wrf	56	863	725	**863**	**725**	863	725	56	**856**	**731**	855	732	857	730
482.sphinx3	56	1435	761	1441	758	**1435**	**761**	56	1435	761	1441	758	**1435**	**761**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode

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Platform Notes (Continued)

```
Set Patrol Scrub to Disable
Sysinfo program /spec15/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Sun Sep 27 17:31:41 2015

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2683 v3 @ 2.00GHz
      2 "physical id"s (chips)
      56 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 7
      siblings  : 14
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
   cache size : 17920 KB

From /proc/meminfo
   MemTotal:       263574228 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
   EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 22 07:06

SPEC is set to: /spec15
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      ext4  448G  209G  217G  49% /
Additional information from dmidecode:
```

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Platform Notes (Continued)

```
    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS Insyde Corp. 1.26 12/22/2014
   Memory:
    8x Micron 36ASF2G72PZ-2G1A2 16 GB 1 rank 2133 MHz
    8x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec15/libs/32:/spec15/libs/64:/spec15/sh"

 Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB
 memory using RedHat EL 7.0
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
     icc   -m64
```

C++ benchmarks:
```
     icpc  -m64
```

Fortran benchmarks:
```
     ifort -m64
```

Benchmarks using both Fortran and C:
```
     icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks:
```
icc    -m64
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
  450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

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CPU2006 license: 3175　　　　　　　　　　　　　　　　**Test date:**　　　　Sep-2015
Test sponsor:　　Huawei　　　　　　　　　　　　　　**Hardware Availability:** Sep-2014
Tested by:　　　Huawei　　　　　　　　　　　　　　**Software Availability:** Sep-2014

Peak Portability Flags

```
   410.bwaves: -DSPEC_CPU_LP64
   416.gamess: -DSPEC_CPU_LP64
     433.milc: -DSPEC_CPU_LP64
   434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
   453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
     470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Peak Optimization Flags

C benchmarks:

```
     433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
              -auto-ilp32

     470.lbm: basepeak = yes

   482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
     444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
              -auto-ilp32

    447.dealII: basepeak = yes

   450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
              -opt-malloc-options=3

   453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2)
              -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
              -ansi-alias
```

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Peak Optimization Flags (Continued)

Fortran benchmarks:

    410.bwaves: `basepeak = yes`

    416.gamess: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                 `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2`
                 `-inline-level=0 -scalar-rep-`

    434.zeusmp: `basepeak = yes`

    437.leslie3d: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch`

  459.GemsFDTD: `basepeak = yes`

      465.tonto: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4`
               `-auto -inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

    435.gromacs: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2)`
                `-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)`
                `-opt-prefetch -auto-ilp32`

  436.cactusADM: `basepeak = yes`

    454.calculix: `basepeak = yes`

      481.wrf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-HASWELL-V1.4.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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