



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 99.3

PowerEdge T330 (Intel Xeon E3-1280 v5, 3.70 GHz)

SPECfp_base2006 = 96.9

CPU2006 license: 55

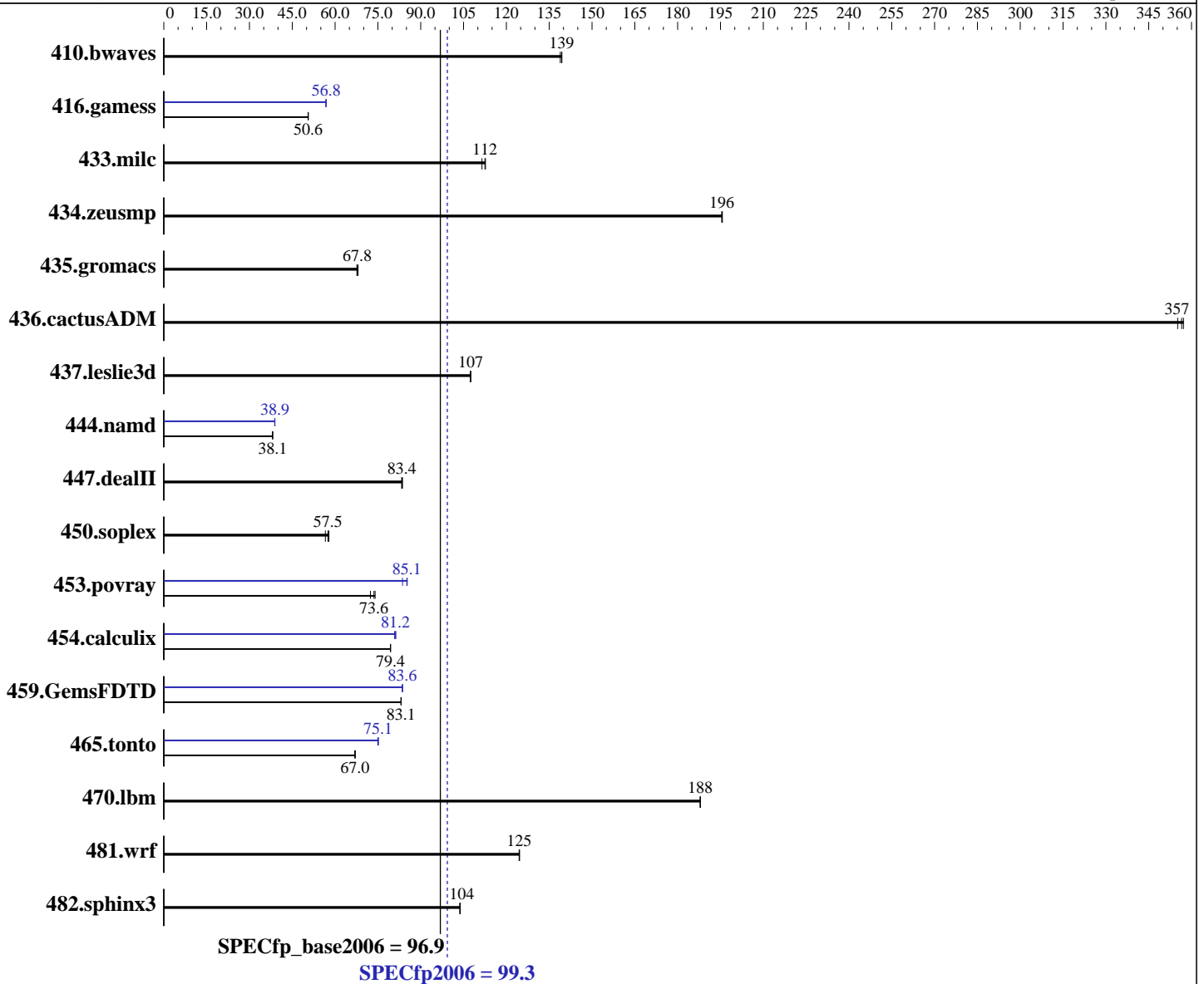
Test date: Sep-2015

Test sponsor: Dell Inc.

Hardware Availability: Nov-2015

Tested by: Dell Inc.

Software Availability: Sep-2015



Hardware

CPU Name: Intel Xeon E3-1280 v5
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz
 CPU MHz: 3700
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 12
 3.12.28-4-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 multi-user

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-U)
Disk Subsystem: 1 x 500 GB 7200 RPM SATA
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	97.9	139	97.5	139	<u>97.5</u>	<u>139</u>	97.9	139	97.5	139	<u>97.5</u>	<u>139</u>
416.gamess	387	50.6	387	50.6	<u>387</u>	<u>50.6</u>	<u>344</u>	<u>56.8</u>	345	56.8	344	56.9
433.milc	82.4	111	81.5	113	<u>81.6</u>	<u>112</u>	82.4	111	81.5	113	<u>81.6</u>	<u>112</u>
434.zeusmp	46.6	195	<u>46.5</u>	<u>196</u>	46.5	196	46.6	195	<u>46.5</u>	<u>196</u>	46.5	196
435.gromacs	<u>105</u>	<u>67.8</u>	105	68.1	106	67.6	<u>105</u>	<u>67.8</u>	105	68.1	106	67.6
436.cactusADM	33.5	357	<u>33.5</u>	<u>357</u>	33.6	355	33.5	357	<u>33.5</u>	<u>357</u>	33.6	355
437.leslie3d	87.4	108	87.6	107	<u>87.4</u>	<u>107</u>	87.4	108	87.6	107	<u>87.4</u>	<u>107</u>
444.namd	211	38.1	210	38.1	<u>210</u>	<u>38.1</u>	206	38.9	206	38.9	<u>206</u>	<u>38.9</u>
447.dealII	137	83.3	137	83.6	<u>137</u>	<u>83.4</u>	137	83.3	137	83.6	<u>137</u>	<u>83.4</u>
450.soplex	144	57.8	<u>145</u>	<u>57.5</u>	147	56.6	144	57.8	<u>145</u>	<u>57.5</u>	147	56.6
453.povray	71.9	74.0	<u>72.3</u>	<u>73.6</u>	73.5	72.4	<u>62.5</u>	<u>85.1</u>	63.6	83.6	62.4	85.3
454.calculix	104	79.4	<u>104</u>	<u>79.4</u>	104	79.5	102	80.8	<u>102</u>	<u>81.2</u>	102	81.2
459.GemsFDTD	128	83.2	128	83.1	<u>128</u>	<u>83.1</u>	<u>127</u>	<u>83.6</u>	127	83.6	127	83.6
465.tonto	147	66.9	147	67.0	<u>147</u>	<u>67.0</u>	131	75.2	<u>131</u>	<u>75.1</u>	131	75.0
470.lbm	73.1	188	73.1	188	<u>73.1</u>	<u>188</u>	73.1	188	73.1	188	<u>73.1</u>	<u>188</u>
481.wrf	89.6	125	89.7	124	<u>89.7</u>	<u>125</u>	89.6	125	89.7	124	<u>89.7</u>	<u>125</u>
482.sphinx3	188	104	188	104	<u>188</u>	<u>104</u>	188	104	188	104	<u>188</u>	<u>104</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Performance
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on linux-l6fb Wed Sep 16 15:07:52 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1280 v5 @ 3.70GHz
 1 "physical id"s (chips)
 8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 1 2 3
cache size : 8192 KB

```

```

From /proc/meminfo
MemTotal:      66066084 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

```

```

uname -a:
Linux linux-16fb 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Sep 16 11:02

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  451G   10G  440G   3% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.16 09/09/2015

Memory:

1x 00AD00000000 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

2x 00AD0000020B HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

1x 00AD00000800 HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.lelie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

Benchmarks using both Fortran and C:

```

icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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