SPEC® CFP2006 Result
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NEC Corporation

Express5800/B120f (Intel Xeon E5-2690 v3)

$SPECfp®\_rate2006 =$ 646

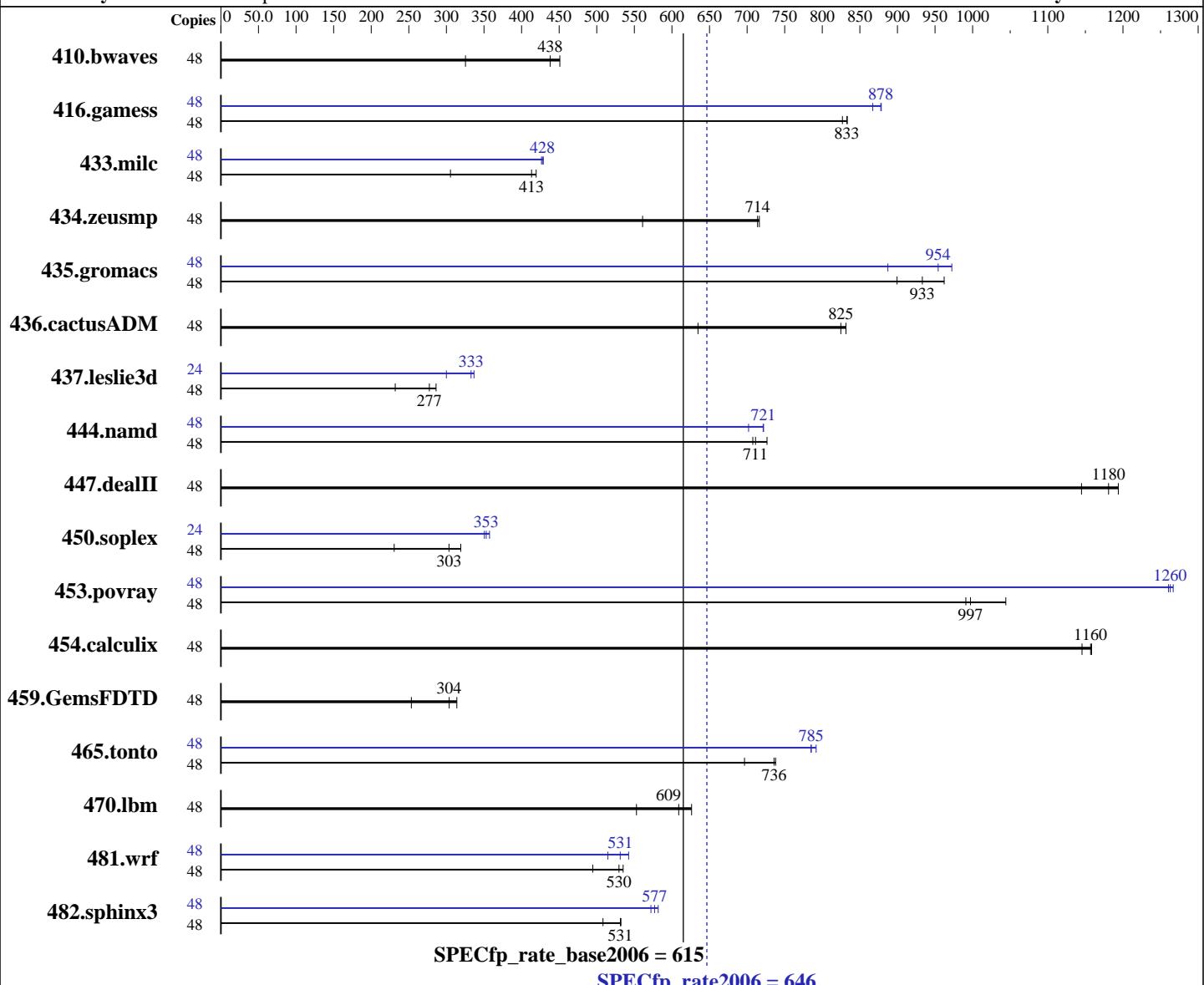$SPECfp\_rate\_base2006 =$ 615

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Nov-2014
Hardware Availability: Apr-2015
Software Availability: Jul-2014

SPECfp_rate_base2006 = 615

SPECfp_rate2006 = 646

Hardware

CPU Name: Intel Xeon E5-2690 v3
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
Kernel 2.6.32-431.20.3.el6.x86_64
Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;
Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
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L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (6 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x 300 GB SAS, 10000 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	2005	325	**1489**	**438**	1447	451	48	2005	325	**1489**	**438**	1447	451
416.gamess	48	1137	827	**1129**	**833**	1128	833	48	1084	867	**1071**	**878**	1070	879
433.milc	48	1443	305	**1066**	**413**	1051	419	48	1027	429	**1030**	**428**	1033	426
434.zeusmp	48	779	561	610	716	**612**	**714**	48	779	561	610	716	**612**	**714**
435.gromacs	48	381	899	**367**	**933**	356	962	48	386	887	352	972	**359**	**954**
436.cactusADM	48	904	635	**696**	**825**	690	832	48	904	635	**696**	**825**	690	832
437.leslie3d	48	1945	232	**1627**	**277**	1576	286	24	752	300	670	337	**678**	**333**
444.namd	48	530	726	**541**	**711**	544	708	48	548	702	**534**	**721**	533	722
447.dealII	48	480	1140	**465**	**1180**	460	1190	48	480	1140	**465**	**1180**	460	1190
450.soplex	48	1737	231	**1319**	**303**	1254	319	24	560	357	571	350	**567**	**353**
453.povray	48	**256**	**997**	258	991	245	1040	48	203	1260	202	1270	**202**	**1260**
454.calculix	48	346	1150	**342**	**1160**	342	1160	48	346	1150	**342**	**1160**	342	1160
459.GemsFDTD	48	2010	253	**1677**	**304**	1622	314	48	2010	253	**1677**	**304**	1622	314
465.tonto	48	640	738	**642**	**736**	678	697	48	602	785	596	792	**601**	**785**
470.lbm	48	1193	553	**1083**	**609**	1053	626	48	1193	553	**1083**	**609**	1053	626
481.wrf	48	1084	495	**1012**	**530**	1002	535	48	1042	515	989	542	**1009**	**531**
482.sphinx3	48	1841	508	**1760**	**531**	1758	532	48	**1622**	**577**	1608	582	1635	572

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

   The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

   BIOS Settings:
    Processor C6 Report: Enabled
    Energy Performance: Performance

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Platform Notes (Continued)

```
    Patrol Scrub: Disabled
    Early Snoop: Disabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
    icc    -m64
```

C++ benchmarks:
```
    icpc   -m64
```

Fortran benchmarks:
```
    ifort -m64
```

Benchmarks using both Fortran and C:
```
    icc    -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
     481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

```
   482.sphinx3: icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks (except as noted below):
```
icpc   -m64
```

```
    450.soplex: icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

```
   410.bwaves: -DSPEC_CPU_LP64
  416.gamess: -DSPEC_CPU_LP64
     433.milc: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
       447.dealII: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
         481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
       433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2)
                 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
                 -auto-ilp32

        470.lbm: basepeak = yes

     482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
                 -unroll2
```

C++ benchmarks:

```
        444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2)
                 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias
                 -auto-ilp32

       447.dealII: basepeak = yes

      450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2)
                 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
                 -opt-malloc-options=3

      453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2)
                 -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4
                 -ansi-alias
```

Fortran benchmarks:

NEC Corporation

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2)
             -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)
             -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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