



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 1120

PRIMERGY RX2530 M1, Intel Xeon E5-2690 v3, 2.6 GHz

SPECint_rate_base2006 = 1090

CPU2006 license: 19

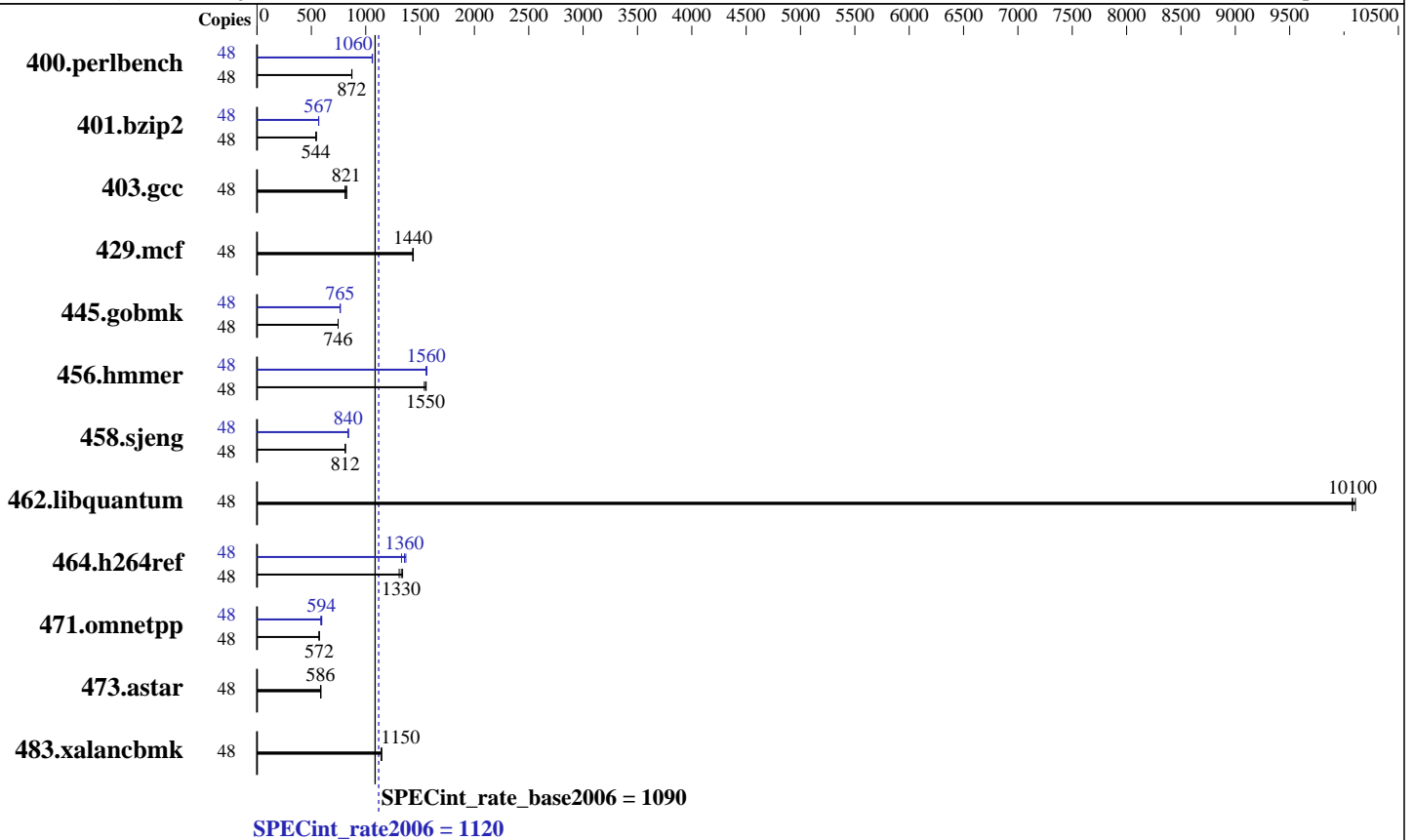
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2015

Hardware Availability: Feb-2015

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2690 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	537	873	538	871	<u>538</u>	<u>872</u>	48	<u>442</u>	<u>1060</u>	441	1060	443	1060
401.bzip2	48	855	542	<u>851</u>	<u>544</u>	848	546	48	816	568	<u>817</u>	<u>567</u>	817	567
403.gcc	48	468	825	477	810	<u>471</u>	<u>821</u>	48	468	825	477	810	<u>471</u>	<u>821</u>
429.mcf	48	304	1440	<u>305</u>	<u>1440</u>	306	1430	48	304	1440	<u>305</u>	<u>1440</u>	306	1430
445.gobmk	48	674	747	<u>675</u>	<u>746</u>	675	746	48	658	765	<u>658</u>	<u>765</u>	658	766
456.hammer	48	<u>288</u>	<u>1550</u>	288	1560	291	1540	48	<u>287</u>	<u>1560</u>	287	1560	288	1560
458.sjeng	48	715	813	<u>715</u>	<u>812</u>	715	812	48	692	839	<u>692</u>	<u>840</u>	692	840
462.libquantum	48	98.7	10100	98.4	10100	<u>98.7</u>	<u>10100</u>	48	98.7	10100	98.4	10100	<u>98.7</u>	<u>10100</u>
464.h264ref	48	<u>800</u>	<u>1330</u>	793	1340	812	1310	48	<u>783</u>	<u>1360</u>	799	1330	776	1370
471.omnetpp	48	524	573	<u>524</u>	<u>572</u>	525	571	48	512	587	<u>505</u>	<u>594</u>	504	596
473.astar	48	577	584	573	588	<u>575</u>	<u>586</u>	48	577	584	573	588	<u>575</u>	<u>586</u>
483.xalancbmk	48	<u>289</u>	<u>1150</u>	289	1150	290	1140	48	<u>289</u>	<u>1150</u>	289	1150	290	1140

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy performance = Performance
QPI snoop mode: COD = enable, Early snoop = disable
CPU ClE Support = Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/ICC14.0-SPECcpu2006/libs/32:/ICC14.0-SPECcpu2006/libs/64:/ICC14.0-SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches

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General Notes (Continued)

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

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Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LINUX`

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

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Peak Optimization Flags (Continued)

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

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