



SPEC® CINT2006 Result

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Fujitsu

PRIMERGY RX4770 M1, Intel Xeon E7-4850 v2, 2.30 GHz

SPECint®_rate2006 = 1580

SPECint_rate_base2006 = 1530

CPU2006 license: 19

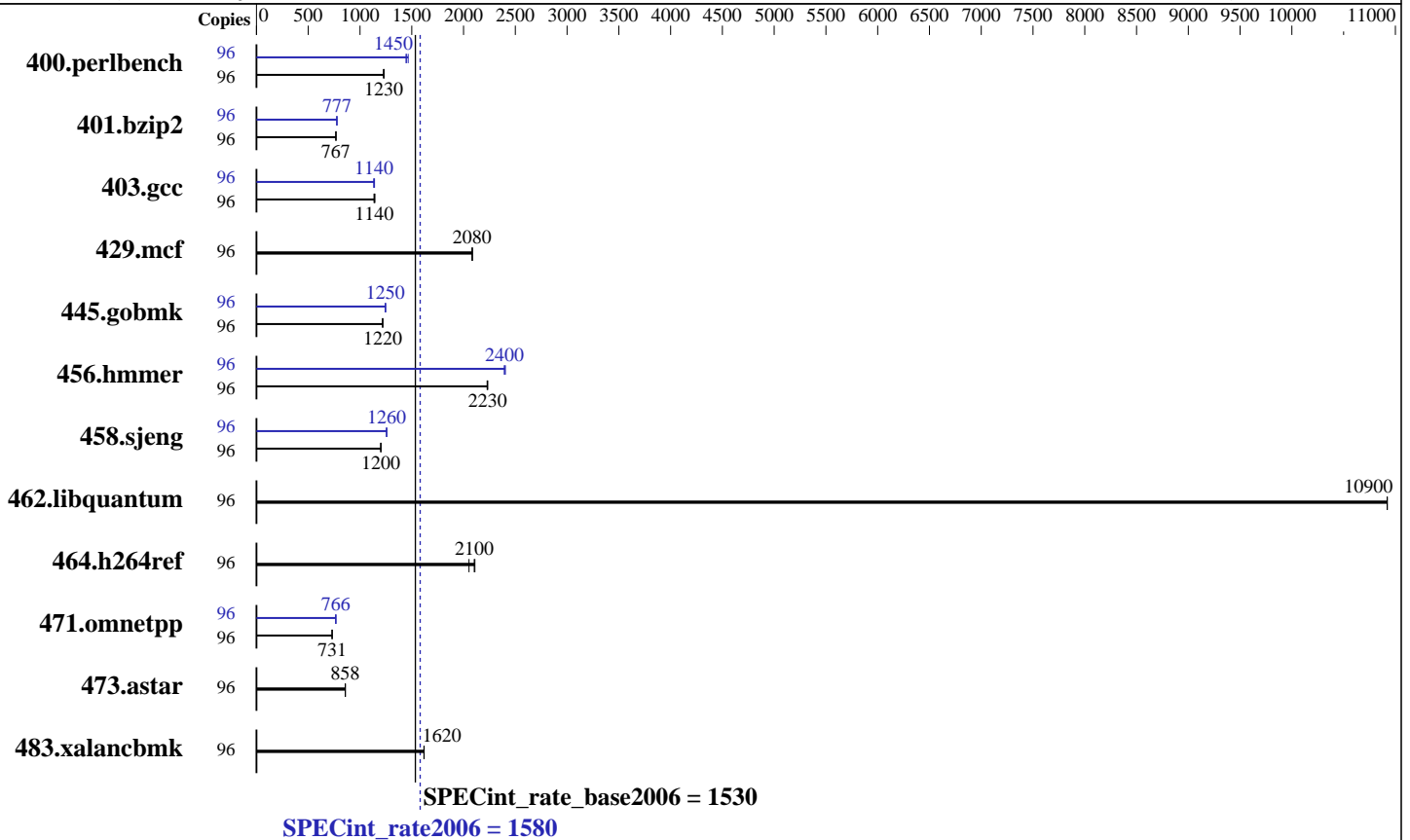
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2015

Hardware Availability: Jun-2014

Software Availability: Jan-2015



Hardware

CPU Name: Intel Xeon E7-4850 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2300
 FPU: Integrated
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip, 2 threads/core
 CPU(s) orderable: 4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	96	765	1230	<u>762</u>	<u>1230</u>	762	1230	96	649	1450	<u>648</u>	<u>1450</u>	640	1470
401.bzip2	96	<u>1207</u>	<u>767</u>	1208	767	1206	768	96	1194	776	1192	777	<u>1192</u>	<u>777</u>
403.gcc	96	<u>679</u>	<u>1140</u>	675	1140	680	1140	96	678	1140	<u>681</u>	<u>1140</u>	682	1130
429.mcf	96	420	2080	<u>420</u>	<u>2080</u>	420	2090	96	420	2080	<u>420</u>	<u>2080</u>	420	2090
445.gobmk	96	<u>826</u>	<u>1220</u>	826	1220	826	1220	96	810	1240	<u>809</u>	<u>1250</u>	807	1250
456.hammer	96	401	2230	<u>401</u>	<u>2230</u>	402	2230	96	<u>374</u>	<u>2400</u>	373	2400	374	2390
458.sjeng	96	<u>967</u>	<u>1200</u>	966	1200	968	1200	96	924	1260	<u>924</u>	<u>1260</u>	925	1260
462.libquantum	96	182	10900	182	10900	<u>182</u>	<u>10900</u>	96	182	10900	182	10900	<u>182</u>	<u>10900</u>
464.h264ref	96	1035	2050	<u>1010</u>	<u>2100</u>	1007	2110	96	1035	2050	<u>1010</u>	<u>2100</u>	1007	2110
471.omnetpp	96	821	730	<u>821</u>	<u>731</u>	821	731	96	782	767	784	765	<u>783</u>	<u>766</u>
473.astar	96	<u>785</u>	<u>858</u>	785	858	786	858	96	<u>785</u>	<u>858</u>	785	858	786	858
483.xalancbmk	96	409	1620	409	1620	<u>409</u>	<u>1620</u>	96	409	1620	409	1620	<u>409</u>	<u>1620</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

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General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

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Peak Compiler Invocation (Continued)

456.hmmr: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmr: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmr: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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