



SPEC® CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp®_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

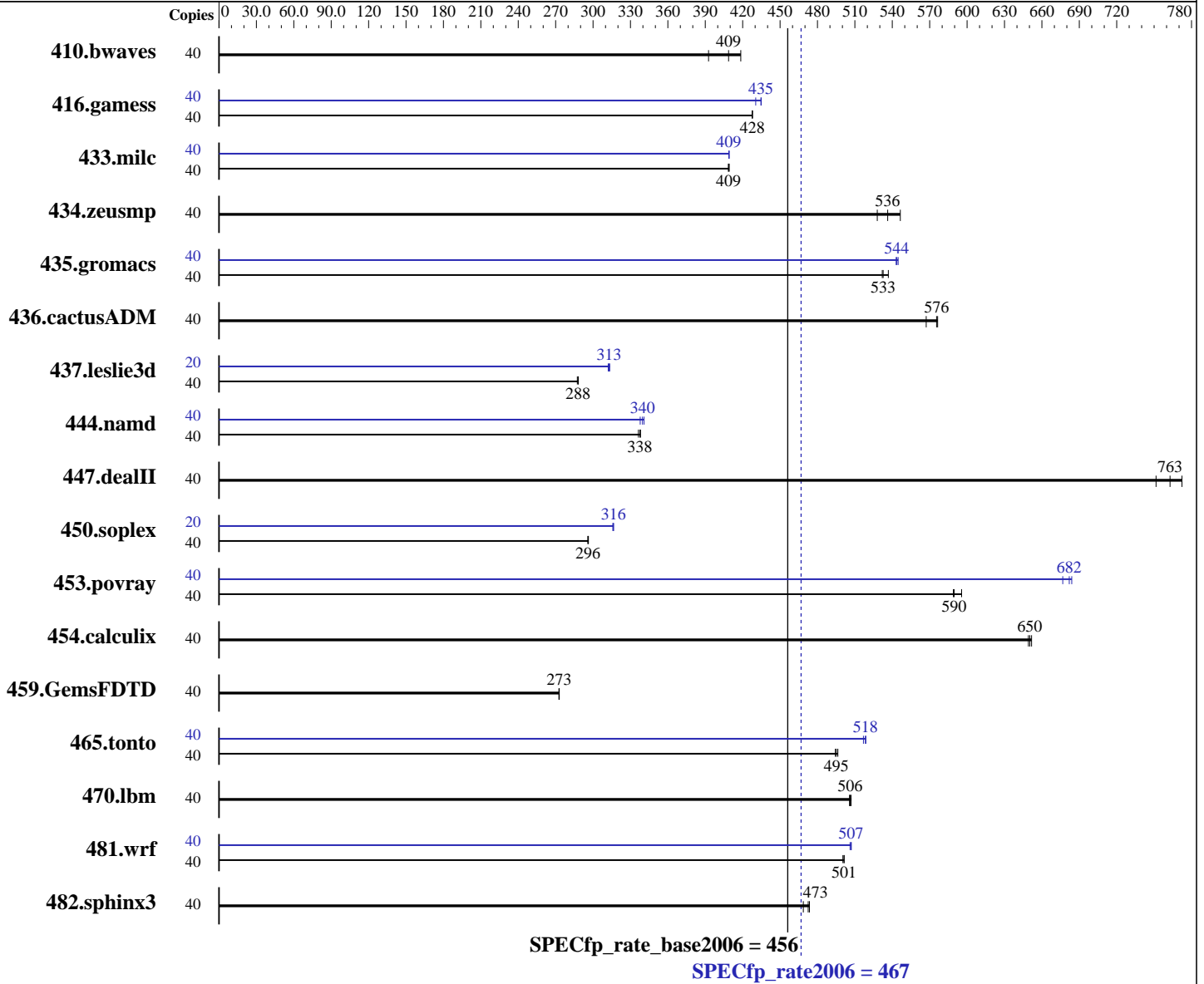
Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2650L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz
 CPU MHz: 1700
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013

L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx8 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	1299	418	1384	393	1330	409	40	1299	418	1384	393	1330	409
416.gamess	40	1831	428	1832	428	1831	428	40	1801	435	1819	430	1802	435
433.milc	40	899	409	897	409	898	409	40	898	409	897	409	898	409
434.zeusmp	40	689	528	666	546	679	536	40	689	528	666	546	679	536
435.gromacs	40	537	532	536	533	532	537	40	525	544	524	545	526	543
436.cactusADM	40	831	576	843	567	829	576	40	831	576	843	567	829	576
437.leslie3d	40	1305	288	1305	288	1308	287	20	601	313	600	313	602	312
444.namd	40	949	338	950	338	954	336	40	950	338	945	340	941	341
447.dealII	40	592	772	609	752	600	763	40	592	772	609	752	600	763
450.soplex	40	1128	296	1126	296	1127	296	20	528	316	527	316	527	316
453.povray	40	361	589	361	590	357	596	40	312	682	314	677	311	684
454.calculix	40	506	652	507	650	508	649	40	506	652	507	650	508	649
459.GemsFDTD	40	1555	273	1557	273	1556	273	40	1555	273	1557	273	1556	273
465.tonto	40	796	494	793	496	795	495	40	759	518	762	517	759	519
470.lbm	40	1087	506	1086	506	1084	507	40	1087	506	1086	506	1084	507
481.wrf	40	891	501	893	500	891	501	40	881	507	883	506	881	507
482.sphinx3	40	1650	473	1663	469	1646	474	40	1650	473	1663	469	1646	474

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Baseboard Management Controller used to adjust the fan speed to 100%

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Platform Notes (Continued)

Sysinfo program /spec/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on wbspeccpu Sun Aug 24 06:36:19 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v2 @ 1.70GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      132103760 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux wbspeccpu 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 22 01:34
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal        ext4  270G  113G  143G  45% /
```

```
Additional information from dmidecode:
BIOS Insyde Corp. RMIBV629 05/12/2014
Memory:
 16x Hynix HMT41GR7MFR8C-PB 8 GB 1600 MHz 2 rank
 8x NO DIMM NO DIMM
```

(End of data from sysinfo program)



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

Test date: Aug-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Sep-2013

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

Test date: Aug-2014

Test sponsor: Huawei

Hardware Availability: Sep-2013

Tested by: Huawei

Software Availability: Sep-2013

Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 467

Huawei CH221 (Intel Xeon E5-2650L v2)

SPECfp_rate_base2006 = 456

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Aug-2014

Hardware Availability: Sep-2013

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Fri Jan 16 17:48:16 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 24 September 2014.