



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Sugon

SPECint®\_rate2006 = 851

I620-G15 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 822

CPU2006 license: 9046

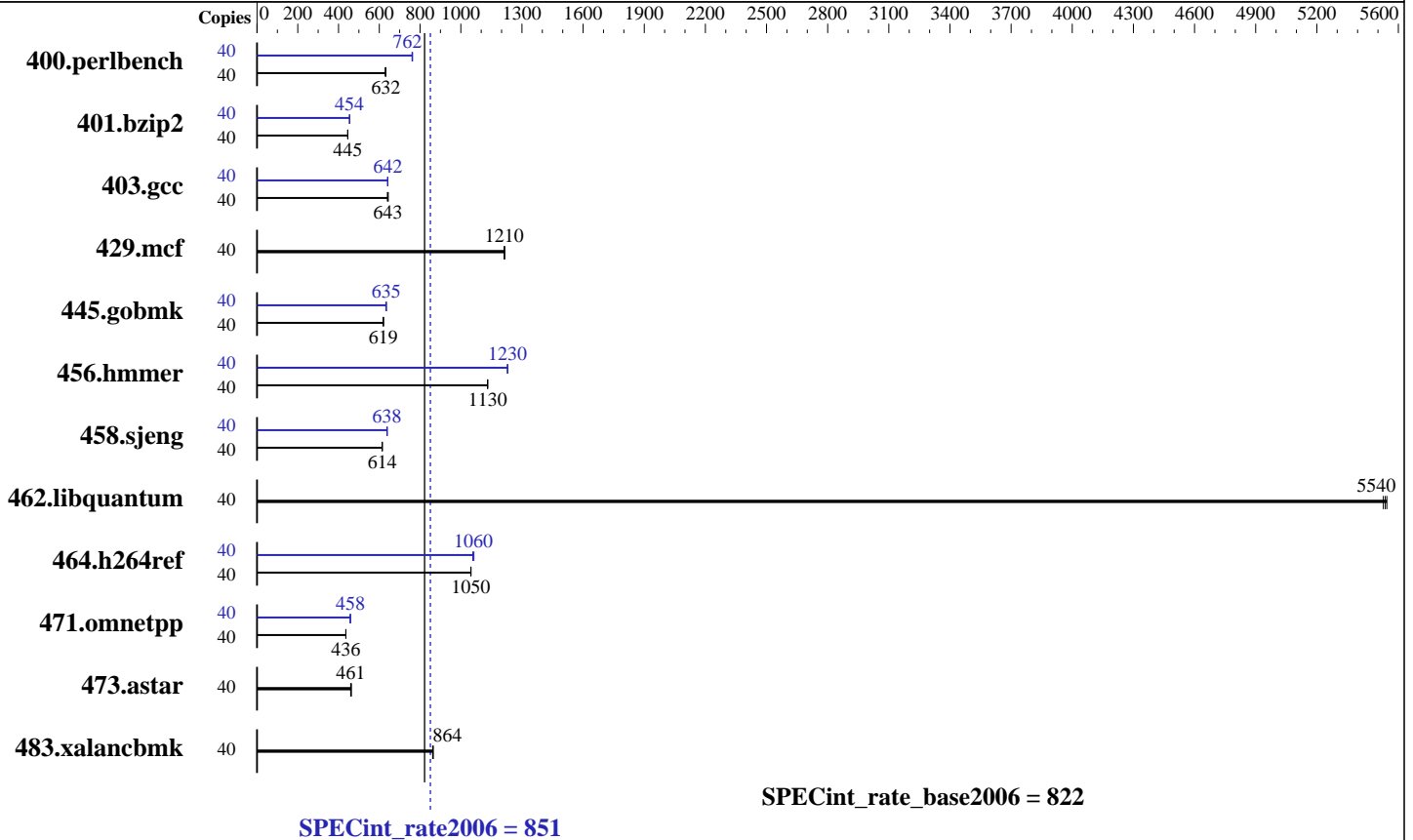
Test sponsor: Sugon

Tested by: Sugon

Test date: Jan-2014

Hardware Availability: Jan-2014

Software Availability: Jan-2014



### Hardware

CPU Name: Intel Xeon E5-2680 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)  
 Disk Subsystem: 1 X 2 TB SATA 7200 RPM, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Sugon

SPECint\_rate2006 = 851

I620-G15 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 822

CPU2006 license: 9046  
Test sponsor: Sugon  
Tested by: Sugon

Test date: Jan-2014  
Hardware Availability: Jan-2014  
Software Availability: Jan-2014

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	618	633	<b>618</b>	<b>632</b>	622	629	40	512	763	<b>513</b>	<b>762</b>	514	761
401.bzip2	40	866	446	868	445	<b>867</b>	<b>445</b>	40	850	454	851	454	<b>850</b>	<b>454</b>
403.gcc	40	<b>501</b>	<b>643</b>	503	640	500	644	40	502	642	503	641	<b>502</b>	<b>642</b>
429.mcf	40	300	1220	301	1210	<b>301</b>	<b>1210</b>	40	300	1220	301	1210	<b>301</b>	<b>1210</b>
445.gobmk	40	<b>678</b>	<b>619</b>	679	618	675	621	40	664	632	<b>661</b>	<b>635</b>	661	635
456.hammer	40	330	1130	<b>330</b>	<b>1130</b>	330	1130	40	304	1230	303	1230	<b>304</b>	<b>1230</b>
458.sjeng	40	787	615	<b>788</b>	<b>614</b>	789	614	40	<b>759</b>	<b>638</b>	759	638	757	639
462.libquantum	40	<b>150</b>	<b>5540</b>	150	5530	150	5540	40	<b>150</b>	<b>5540</b>	150	5530	150	5540
464.h264ref	40	843	1050	844	1050	<b>843</b>	<b>1050</b>	40	832	1060	<b>834</b>	<b>1060</b>	836	1060
471.omnetpp	40	<b>573</b>	<b>436</b>	573	436	573	436	40	548	457	<b>546</b>	<b>458</b>	545	459
473.astar	40	606	463	<b>610</b>	<b>461</b>	611	460	40	606	463	<b>610</b>	<b>461</b>	611	460
483.xalancbmk	40	<b>319</b>	<b>864</b>	319	864	320	862	40	<b>319</b>	<b>864</b>	319	864	320	862

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Configuration:

Intel Virtualization technology set to disabled

Power Technology set to performance

Turbo boost set to enabled

DDR Speed set to force 1866

Sysinfo program /home/cpu2006/config/sysinfo.rev6874

\$Rev: 6874 \$ \$Date:: 2013-11-20 #\$ 654bd3fcf53b06faef0efe54ed011998

running on cpu2006 Fri Jan 10 01:59:16 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2680 v2 @ 2.80GHz

2 "physical id"s (chips)

40 "processors"

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 851

I620-G15 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 822

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Jan-2014

Hardware Availability: Jan-2014

Software Availability: Jan-2014

## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 10
siblings  : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

From /proc/meminfo

```
MemTotal:      264501612 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux cpu2006 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jan 10 01:56

SPEC is set to: /home/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_cpu2006-lv_home
                ext4      1.8T  94G  1.6T   6% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. V8.100A 10/31/2013

Memory:

```
16x Hynix Semiconductor HMT42GR7AFR4C-RD 16 GB 1 rank 1866 MHz
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 851

I620-G15 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 822

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Jan-2014

Hardware Availability: Jan-2014

Software Availability: Jan-2014

## General Notes (Continued)

memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 851

I620-G15 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 822

CPU2006 license: 9046

Test date: Jan-2014

Test sponsor: Sugon

Hardware Availability: Jan-2014

Tested by: Sugon

Software Availability: Jan-2014

## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Sugon

SPECint\_rate2006 = 851

I620-G15 (Intel Xeon E5-2680 v2, 2.80 GHz)

SPECint\_rate\_base2006 = 822

CPU2006 license: 9046

Test sponsor: Sugon

Tested by: Sugon

Test date: Jan-2014

Hardware Availability: Jan-2014

Software Availability: Jan-2014

## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Sugon-Platform-Settings-V1.2-IVB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 21:06:03 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 28 January 2014.