



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 98.3

PowerEdge T620 (Intel Xeon E5-2697 v2, 2.70 GHz)

SPECfp_base2006 = 93.4

CPU2006 license: 55

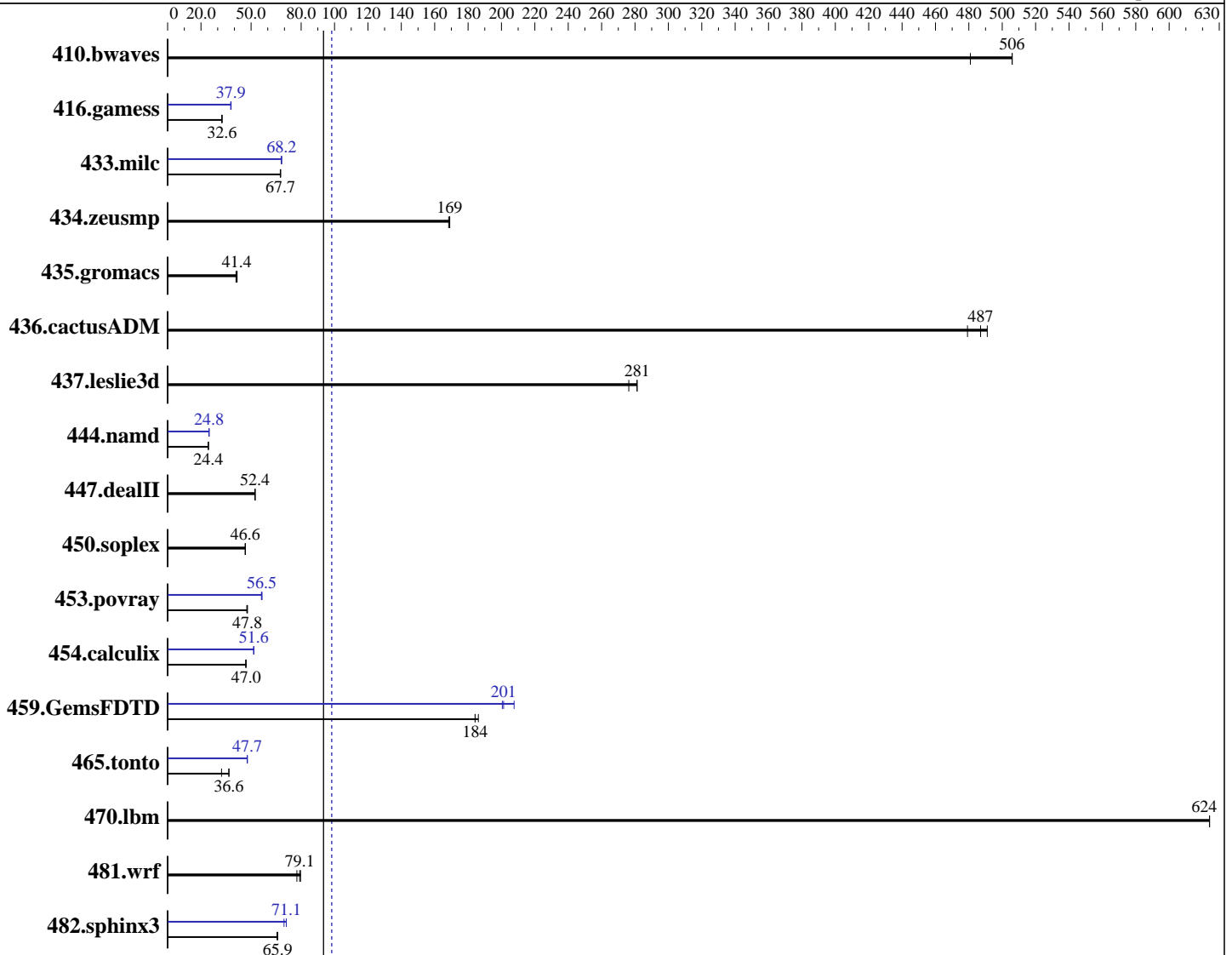
Test date: Sep-2013

Test sponsor: Dell Inc.

Hardware Availability: Sep-2013

Tested by: Dell Inc.

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2697 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2700
 FPU: Integrated
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86_64)
 3.0.76-0.11-default
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)

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L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	26.9	506	28.3	481	<u>26.9</u>	<u>506</u>	26.9	506	28.3	481	<u>26.9</u>	<u>506</u>
416.gamess	601	32.6	<u>601</u>	<u>32.6</u>	602	32.5	<u>517</u>	<u>37.9</u>	515	38.0	517	37.8
433.milc	136	67.7	136	67.5	<u>136</u>	<u>67.7</u>	134	68.3	<u>135</u>	<u>68.2</u>	135	68.1
434.zeusmp	53.8	169	54.0	168	<u>53.8</u>	<u>169</u>	53.8	169	54.0	168	<u>53.8</u>	<u>169</u>
435.gromacs	172	41.6	<u>172</u>	<u>41.4</u>	174	41.0	172	41.6	<u>172</u>	<u>41.4</u>	174	41.0
436.cactusADM	<u>24.5</u>	<u>487</u>	24.9	479	24.3	491	<u>24.5</u>	<u>487</u>	24.9	479	24.3	491
437.leslie3d	<u>33.4</u>	<u>281</u>	34.0	276	33.4	281	<u>33.4</u>	<u>281</u>	34.0	276	33.4	281
444.namd	<u>329</u>	<u>24.4</u>	329	24.4	329	24.4	323	24.8	<u>323</u>	<u>24.8</u>	323	24.8
447.dealII	218	52.4	<u>218</u>	<u>52.4</u>	218	52.4	218	52.4	<u>218</u>	<u>52.4</u>	218	52.4
450.soplex	<u>179</u>	<u>46.6</u>	179	46.6	180	46.4	<u>179</u>	<u>46.6</u>	179	46.6	180	46.4
453.povray	111	47.8	112	47.5	<u>111</u>	<u>47.8</u>	<u>94.2</u>	<u>56.5</u>	94.2	56.5	94.7	56.2
454.calculix	<u>176</u>	<u>47.0</u>	176	46.8	176	47.0	160	51.6	<u>160</u>	<u>51.6</u>	160	51.6
459.GemsFDTD	<u>57.6</u>	<u>184</u>	57.6	184	57.0	186	<u>52.7</u>	<u>201</u>	52.9	201	51.1	208
465.tonto	<u>269</u>	<u>36.6</u>	305	32.3	267	36.8	206	47.8	206	47.7	<u>206</u>	<u>47.7</u>
470.lbm	22.0	624	22.0	624	<u>22.0</u>	<u>624</u>	22.0	624	22.0	624	<u>22.0</u>	<u>624</u>
481.wrf	<u>141</u>	<u>79.1</u>	140	79.7	144	77.4	<u>141</u>	<u>79.1</u>	140	79.7	144	77.4
482.sphinx3	295	66.0	297	65.5	<u>296</u>	<u>65.9</u>	279	69.7	<u>274</u>	<u>71.1</u>	274	71.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Virtualization Technology disabled
 Execute Disable disabled
 Logical Processor disabled
 System Profile set to Performance
 Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on linux Thu Sep 12 14:18:13 2013

This section contains SUT (System Under Test) info as seen by
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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
    2 "physical id"s (chips)
    24 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 12
    siblings  : 12
    physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
    physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
  cache size : 30720 KB

```

```

From /proc/meminfo
MemTotal:      264634596 kB
HugePages_Total:    0
Hugepagesize:   2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

```

```

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

```

```
run-level 3 Sep 12 08:46 last=S
```

```

SPEC is set to: /root/cpu2006.1.2.ic13
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext2  267G   99G  168G  38% /

```

```

Additional information from dmidecode:
BIOS Dell Inc. 2.0.19 08/30/2013
Memory:
16x 00CE00B300CE M393B2G70CB0-CMA 16 GB 1866 MHz

```

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"
OMP_NUM_THREADS = "24"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>



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