



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint®\_rate2006 = 456

Servidor Itaotec MX225+ (Intel Xeon E5-2650L)

SPECint\_rate\_base2006 = 437

CPU2006 license: 9001

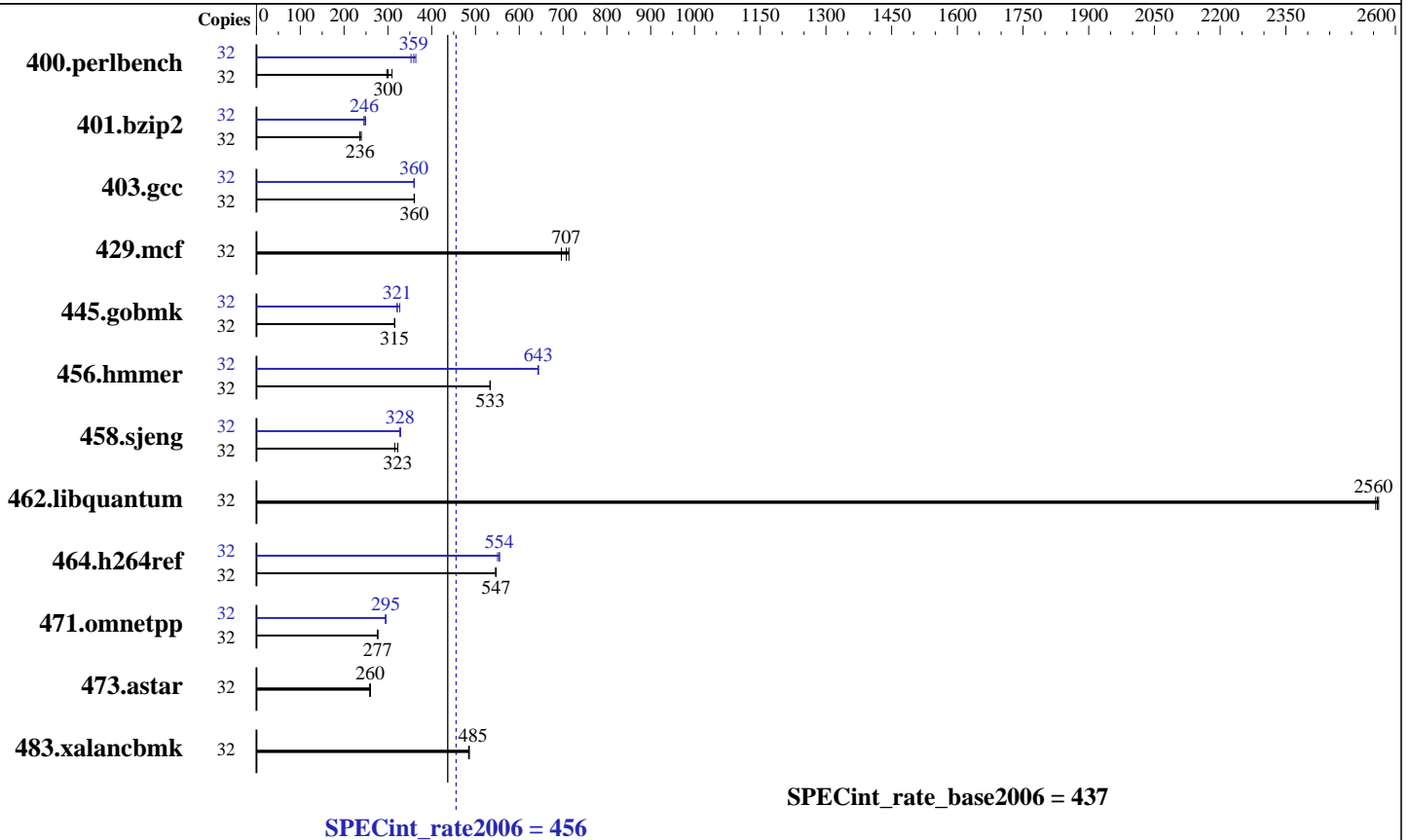
Test date: Dec-2011

Test sponsor: Itaotec

Hardware Availability: Jun-2012

Tested by: Itaotec

Software Availability: Dec-2011



## Hardware

CPU Name: Intel Xeon E5-2650L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.30 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (16 x 4 GB 1Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 500 GB, SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20111011  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 456

Servidor Itaotec MX225+ (Intel Xeon E5-2650L)

SPECint\_rate\_base2006 = 437

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1010	309	1050	298	<b>1041</b>	<b>300</b>	32	886	353	859	364	<b>871</b>	<b>359</b>
401.bzip2	32	1290	239	<b>1307</b>	<b>236</b>	1309	236	32	<b>1255</b>	<b>246</b>	1239	249	1257	246
403.gcc	32	716	360	714	361	<b>715</b>	<b>360</b>	32	<b>716</b>	<b>360</b>	714	361	716	360
429.mcf	32	409	713	419	696	<b>413</b>	<b>707</b>	32	409	713	419	696	<b>413</b>	<b>707</b>
445.gobmk	32	1063	316	<b>1064</b>	<b>315</b>	1064	315	32	1046	321	1026	327	<b>1044</b>	<b>321</b>
456.hammer	32	560	533	559	534	<b>560</b>	<b>533</b>	32	<b>464</b>	<b>643</b>	464	644	464	643
458.sjeng	32	<b>1200</b>	<b>323</b>	1200	323	1227	316	32	<b>1181</b>	<b>328</b>	1177	329	1182	328
462.libquantum	32	<b>259</b>	<b>2560</b>	259	2560	260	2560	32	<b>259</b>	<b>2560</b>	259	2560	260	2560
464.h264ref	32	1294	547	1297	546	<b>1295</b>	<b>547</b>	32	<b>1277</b>	<b>554</b>	1287	550	1276	555
471.omnetpp	32	723	277	<b>723</b>	<b>277</b>	722	277	32	679	295	<b>678</b>	<b>295</b>	677	295
473.astar	32	869	259	<b>864</b>	<b>260</b>	864	260	32	869	259	<b>864</b>	<b>260</b>	864	260
483.xalancbmk	32	456	484	455	486	<b>455</b>	<b>485</b>	32	456	484	455	486	<b>455</b>	<b>485</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Sysinfo program /home/rcaneca/cpu2006/Docs/sysinfo  
\$Rev: 6775 \$ \$Date:: 2011-08-16 #\$ 8787f7622badcf24e01c368b1db4377c  
running on localhost Sat Dec 31 17:23:37 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2650L 0 @ 1.80GHz  
2 "physical id"s (chips)  
32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 456

Servidor Itaotec MX225+ (Intel Xeon E5-2650L)

SPECint\_rate\_base2006 = 437

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011

## Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      65942288 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 31 17:21
```

```
SPEC is set to: /home/rcaneca/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_it5rh-lv_home
                ext4      193G  2.1G  181G   2% /home
```

(End of data from sysinfo program)

## General Notes

This result was measured on the Servidor Itaotec MX205.  
The Servidor Itaotec MX205, the Servidor Itaotec LX205 and the Servidor Itaotec MX225+ are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 456

Servidor Itautec MX225+ (Intel Xeon E5-2650L)

SPECint\_rate\_base2006 = 437

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Dec-2011  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64  
401.bzip2: icc -m64  
456.hmmmer: icc -m64  
458.sjeng: icc -m64  
C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 456

Servidor Itaotec MX225+ (Intel Xeon E5-2650L)

SPECint\_rate\_base2006 = 437

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011

## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmarheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 456

Servidor Itaotec MX225+ (Intel Xeon E5-2650L)

SPECint\_rate\_base2006 = 437

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Dec-2011  
Hardware Availability: Jun-2012  
Software Availability: Dec-2011

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.html](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.html)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2006/flags/Itaotec-Servidor\\_Itaotec-Intel-Linux-Platform.xml](http://www.spec.org/cpu2006/flags/Itaotec-Servidor_Itaotec-Intel-Linux-Platform.xml)  
<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 12:54:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 9 October 2012.