



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 49.9

PRIMERGY BX924 S3, Intel Xeon E5-2665, 2.40 GHz

SPECint_base2006 = 46.6

CPU2006 license: 19

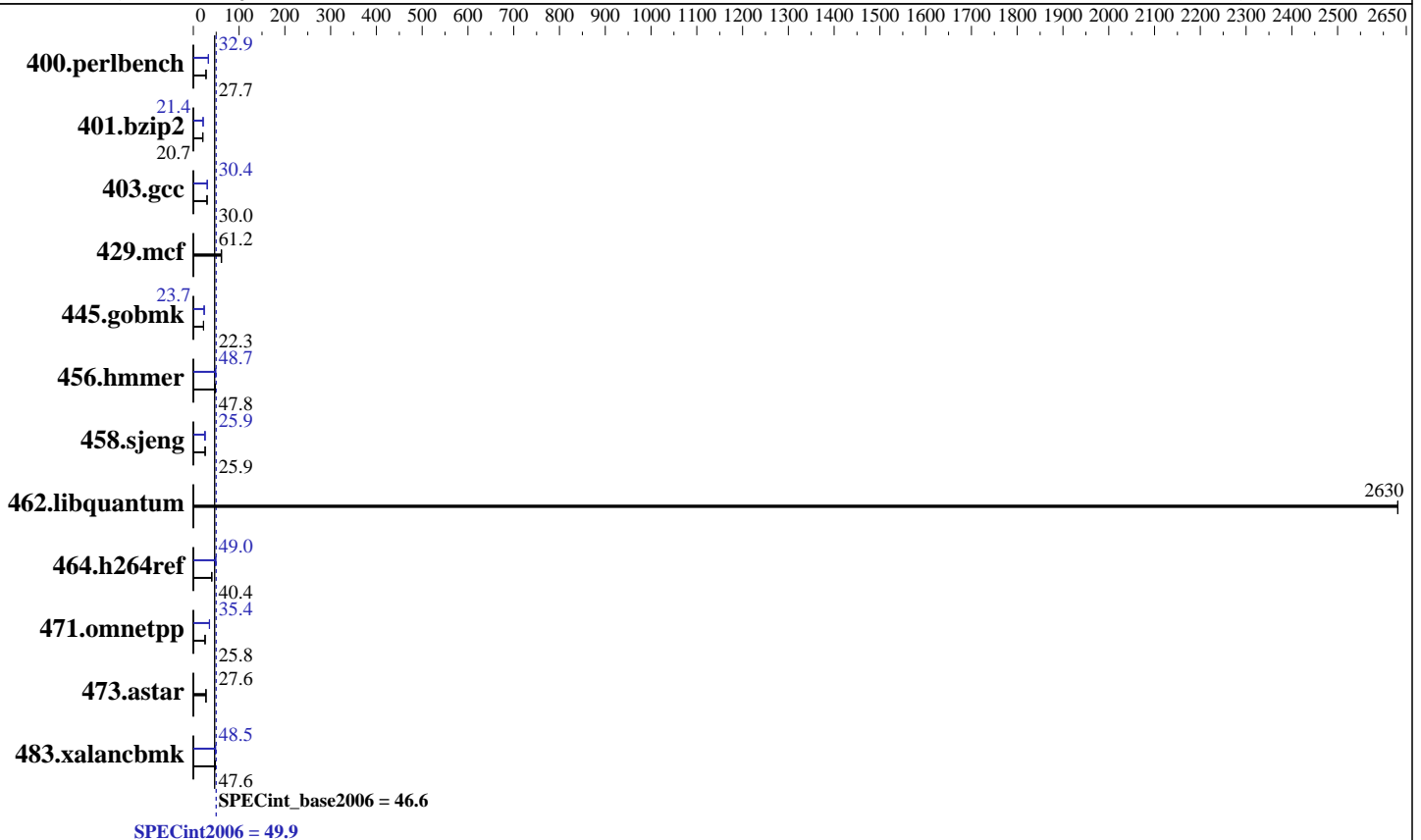
Test date: Jan-2012

Test sponsor: Fujitsu

Hardware Availability: Mar-2012

Tested by: Fujitsu

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2665
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: PRIMERGY SX940 S1 for hw_disk

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|-------------------|--------------------|--------------------|--------------------|-------------------|--------------------|--------------------|--------------------|--------------------|--------------------|-------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 353 | 27.7 | <u>352</u> | <u>27.7</u> | 352 | 27.7 | 297 | 32.9 | 297 | 32.9 | <u>297</u> | <u>32.9</u> |
| 401.bzip2 | 466 | 20.7 | <u>466</u> | <u>20.7</u> | 465 | 20.8 | 452 | 21.4 | 451 | 21.4 | <u>451</u> | <u>21.4</u> |
| 403.gcc | 268 | 30.0 | 268 | 30.0 | <u>268</u> | <u>30.0</u> | 264 | 30.5 | <u>264</u> | <u>30.4</u> | 264 | 30.4 |
| 429.mcf | 147 | 61.9 | 150 | 61.0 | <u>149</u> | <u>61.2</u> | 147 | 61.9 | 150 | 61.0 | <u>149</u> | <u>61.2</u> |
| 445.gobmk | <u>470</u> | <u>22.3</u> | 471 | 22.3 | 470 | 22.3 | 442 | 23.7 | 442 | 23.7 | <u>442</u> | <u>23.7</u> |
| 456.hammer | <u>195</u> | <u>47.8</u> | 195 | 47.8 | 195 | 47.7 | <u>192</u> | <u>48.7</u> | 192 | 48.7 | 192 | 48.7 |
| 458.sjeng | 468 | 25.9 | <u>468</u> | <u>25.9</u> | 468 | 25.9 | 490 | 24.7 | 468 | 25.9 | <u>468</u> | <u>25.9</u> |
| 462.libquantum | 7.87 | 2630 | <u>7.87</u> | <u>2630</u> | 7.87 | 2630 | <u>7.87</u> | <u>2630</u> | <u>7.87</u> | <u>2630</u> | 7.87 | 2630 |
| 464.h264ref | <u>548</u> | <u>40.4</u> | 554 | 39.9 | 547 | 40.4 | <u>452</u> | <u>49.0</u> | 460 | 48.1 | 449 | 49.3 |
| 471.omnetpp | <u>242</u> | <u>25.8</u> | 241 | 25.9 | 248 | 25.2 | 179 | 34.9 | 176 | 35.5 | <u>177</u> | <u>35.4</u> |
| 473.astar | <u>254</u> | <u>27.6</u> | 252 | 27.9 | 256 | 27.4 | <u>254</u> | <u>27.6</u> | 252 | 27.9 | 256 | 27.4 |
| 483.xalancbmk | 144 | 48.0 | <u>145</u> | <u>47.6</u> | 145 | 47.5 | <u>142</u> | <u>48.5</u> | 142 | 48.5 | 143 | 48.4 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Intel HT Technology = Disable
Frequency Floor Override = Enable

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
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Base Compiler Invocation

C benchmarks:
icc -m64

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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