



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®_rate2006 = 52.9

IBM System x3250 M3 (Intel Xeon L3406)

SPECint_rate_base2006 = 49.5

CPU2006 license: 11

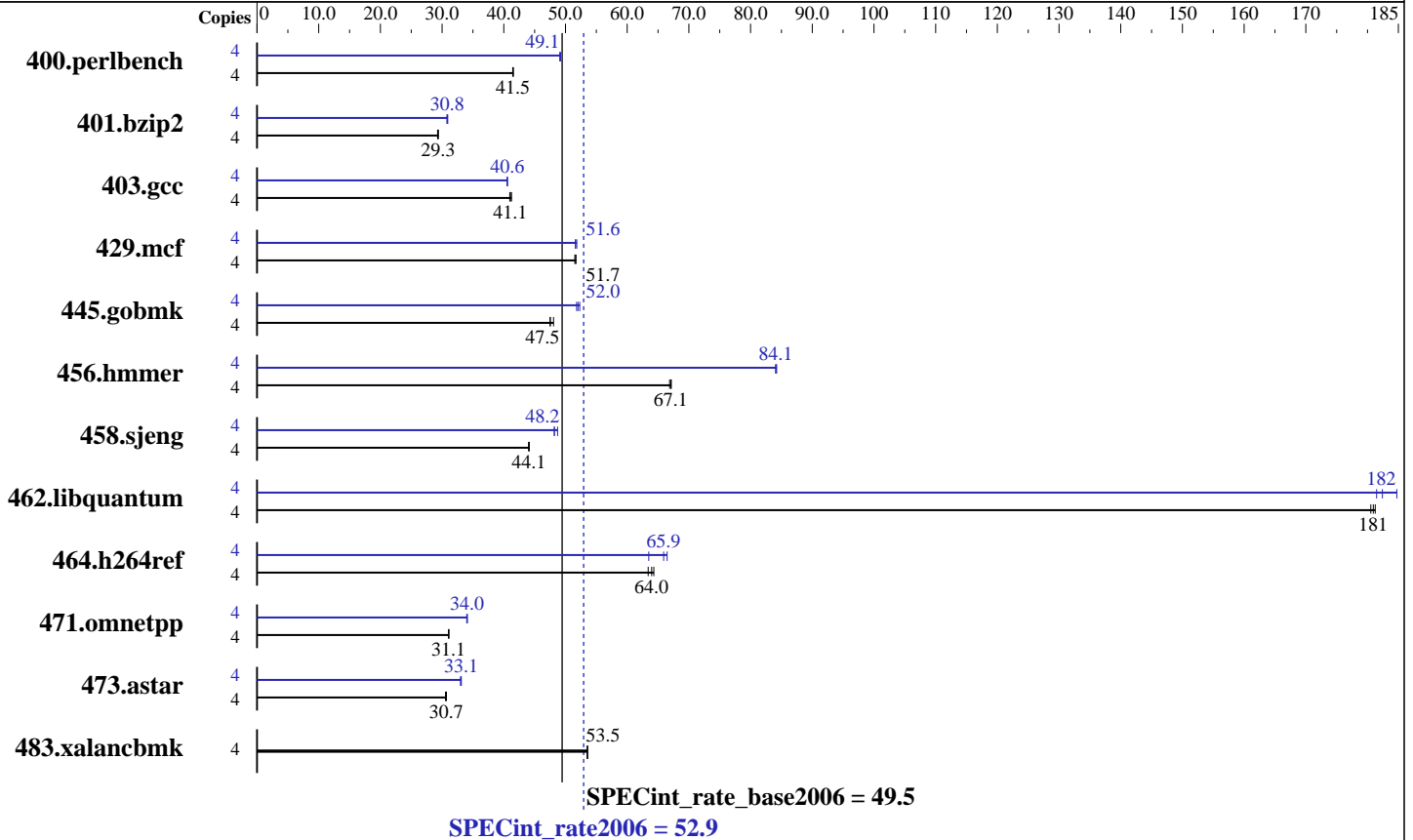
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2010

Hardware Availability: Nov-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L3406
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x 73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: 1_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 52.9

IBM System x3250 M3 (Intel Xeon L3406)

SPECint_rate_base2006 = 49.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Oct-2010
Hardware Availability: Nov-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	940	41.6	943	41.4	<u>942</u>	<u>41.5</u>	4	794	49.2	<u>796</u>	<u>49.1</u>	796	49.1
401.bzip2	4	1313	29.4	<u>1315</u>	<u>29.3</u>	1319	29.3	4	1248	30.9	<u>1252</u>	<u>30.8</u>	1254	30.8
403.gcc	4	786	41.0	781	41.3	<u>784</u>	<u>41.1</u>	4	<u>794</u>	<u>40.6</u>	793	40.6	794	40.5
429.mcf	4	708	51.5	<u>706</u>	<u>51.7</u>	706	51.7	4	704	51.8	707	51.6	<u>706</u>	<u>51.6</u>
445.gobmk	4	873	48.1	<u>882</u>	<u>47.5</u>	883	47.5	4	802	52.3	810	51.8	<u>807</u>	<u>52.0</u>
456.hammer	4	556	67.1	<u>556</u>	<u>67.1</u>	558	66.9	4	444	84.1	443	84.2	<u>444</u>	<u>84.1</u>
458.sjeng	4	1097	44.1	1099	44.0	<u>1098</u>	<u>44.1</u>	4	<u>1004</u>	<u>48.2</u>	993	48.7	1004	48.2
462.libquantum	4	457	181	<u>458</u>	<u>181</u>	459	181	4	457	181	<u>454</u>	<u>182</u>	449	185
464.h264ref	4	<u>1384</u>	<u>64.0</u>	1396	63.4	1376	64.3	4	1332	66.5	1394	63.5	<u>1343</u>	<u>65.9</u>
471.omnetpp	4	804	31.1	805	31.1	<u>805</u>	<u>31.1</u>	4	735	34.0	733	34.1	<u>734</u>	<u>34.0</u>
473.astar	4	919	30.5	<u>916</u>	<u>30.7</u>	914	30.7	4	<u>849</u>	<u>33.1</u>	848	33.1	852	33.0
483.xalancbmk	4	515	53.6	<u>516</u>	<u>53.5</u>	516	53.5	4	515	53.6	<u>516</u>	<u>53.5</u>	516	53.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Platform Notes

BIOS Settings:
Turbo Mode Enable
CPU C State Enable

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 52.9

IBM System x3250 M3 (Intel Xeon L3406)

SPECint_rate_base2006 = 49.5

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Oct-2010

Hardware Availability: Nov-2010

Software Availability: Jan-2010

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 52.9

IBM System x3250 M3 (Intel Xeon L3406)

SPECint_rate_base2006 = 49.5

CPU2006 license: 11

Test date: Oct-2010

Test sponsor: IBM Corporation

Hardware Availability: Nov-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint_rate2006 = 52.9

IBM System x3250 M3 (Intel Xeon L3406)

SPECint_rate_base2006 = 49.5

CPU2006 license: 11

Test date: Oct-2010

Test sponsor: IBM Corporation

Hardware Availability: Nov-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

473.astar (continued):

-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 15:01:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 20 January 2011.