



SPEC® CINT2006 Result

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NEC Corporation

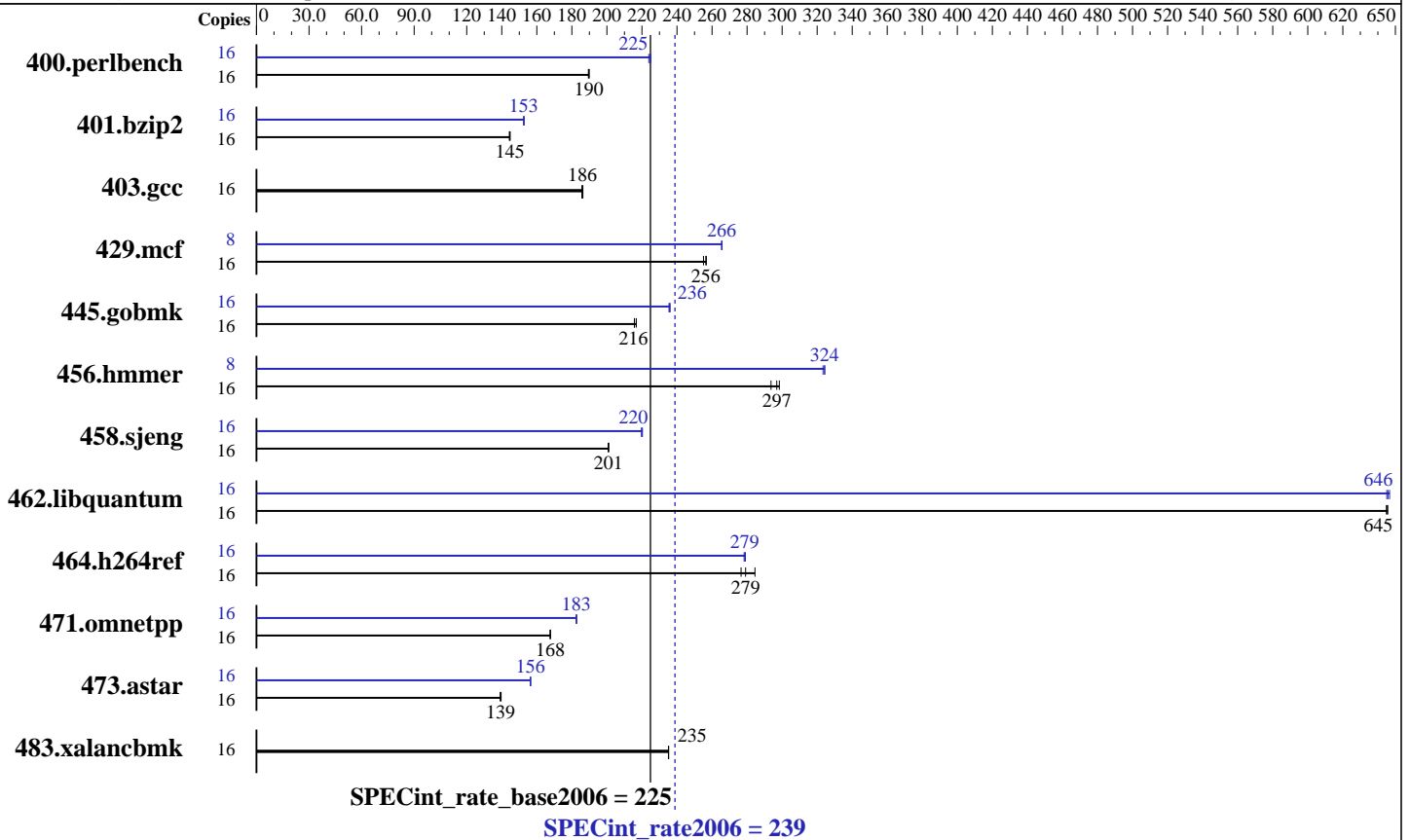
Express5800/R120b-1
(Intel Xeon E5640)

SPECint®_rate2006 = 239

SPECint_rate_base2006 = 225

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Aug-2010
Hardware Availability: Sep-2010
Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E5640
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB PC3L-10600R, 2 rank, CL9, ECC, running at 1066 MHz)
 Disk Subsystem: 1x160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	825	189	<u>824</u>	<u>190</u>	823	190	16	698	224	<u>695</u>	<u>225</u>	694	225
401.bzip2	16	<u>1067</u>	<u>145</u>	1067	145	1070	144	16	1011	153	1012	153	<u>1012</u>	<u>153</u>
403.gcc	16	<u>693</u>	<u>186</u>	693	186	691	186	16	<u>693</u>	<u>186</u>	693	186	691	186
429.mcf	16	572	255	<u>569</u>	<u>256</u>	568	257	8	275	266	<u>275</u>	<u>266</u>	275	265
445.gobmk	16	<u>778</u>	<u>216</u>	774	217	778	216	16	711	236	713	235	<u>712</u>	<u>236</u>
456.hammer	16	508	294	<u>503</u>	<u>297</u>	500	298	8	230	324	<u>230</u>	<u>324</u>	231	323
458.sjeng	16	964	201	<u>964</u>	<u>201</u>	963	201	16	881	220	879	220	<u>881</u>	<u>220</u>
462.libquantum	16	514	645	<u>514</u>	<u>645</u>	514	646	16	514	645	513	647	<u>513</u>	<u>646</u>
464.h264ref	16	1245	284	<u>1268</u>	<u>279</u>	1280	277	16	1272	278	<u>1270</u>	<u>279</u>	1269	279
471.omnetpp	16	596	168	<u>596</u>	<u>168</u>	596	168	16	548	183	547	183	<u>548</u>	<u>183</u>
473.astar	16	<u>805</u>	<u>139</u>	807	139	804	140	16	<u>718</u>	<u>156</u>	718	156	718	156
483.xalancbmk	16	<u>469</u>	<u>235</u>	469	235	470	235	16	<u>469</u>	<u>235</u>	469	235	470	235

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS setting:
Performance/Watt: Traditional
Server Class: Custom
Data Reuse Optimization: Disabled

General Notes

The Express5800/R120b-1 and the Express5800/R120b-2 models are electronically equivalent. The results have been measured on the Express5800/R120b-1 model.

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/opt/SmartHeap_8.1/lib -lsmarheap

```

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Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/opt/SmartHeap_8.1/lib64 -lsmartheap64
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-R120b.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-R120b.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 10:11:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 14 September 2010.