



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/E110b-1
(Intel Celeron G1101)

SPECfp®_rate2006 = 35.7

SPECfp_rate_base2006 = 34.4

CPU2006 license: 9006

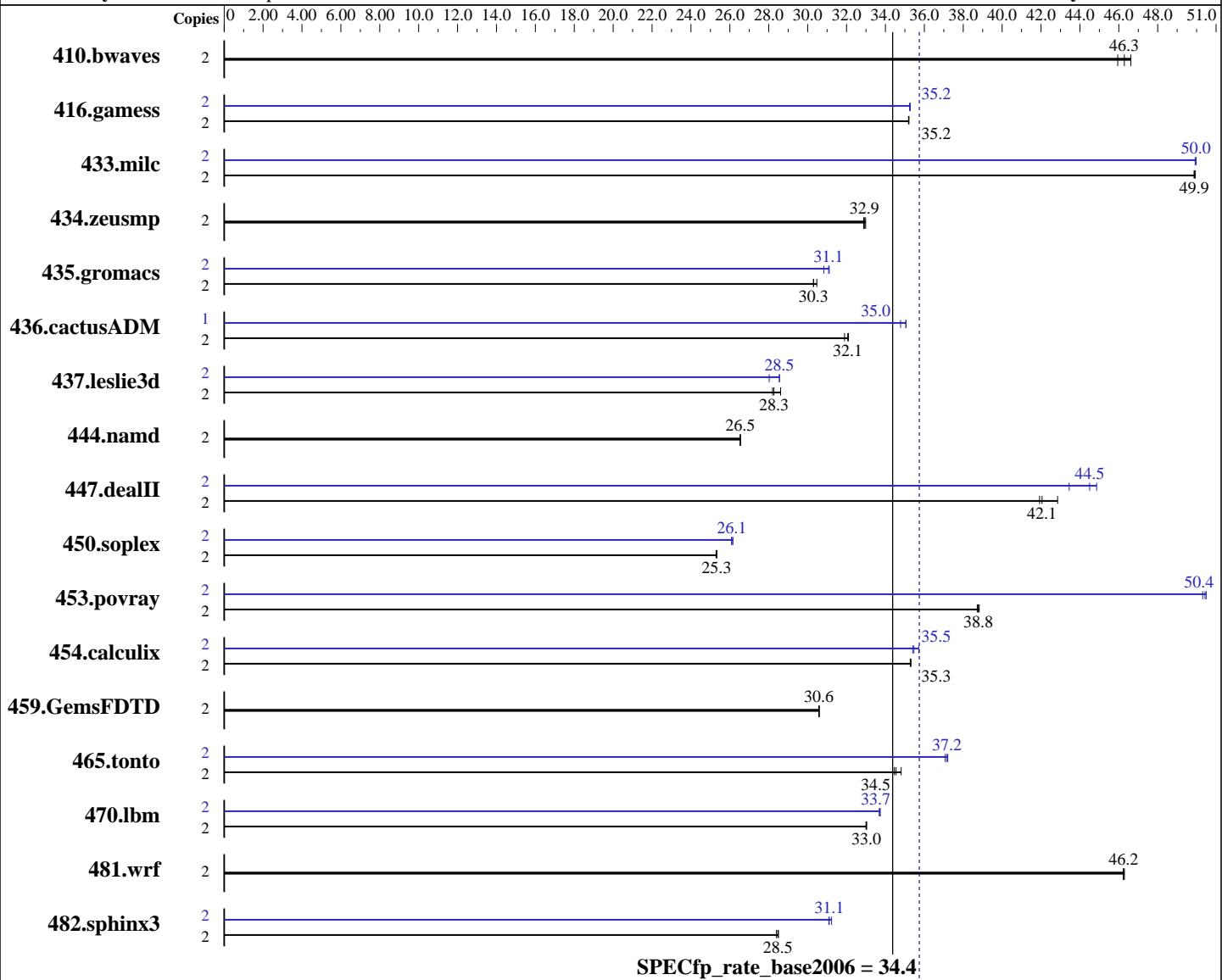
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2010

Hardware Availability: Apr-2010

Software Availability: Dec-2009



Hardware

CPU Name: Intel Celeron G1101
CPU Characteristics:
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4 GB PC3-10600E, 2 rank, CL9, ECC, running at 1066 MHz)
Disk Subsystem: 1x160 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|---------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|------------|-------------|-------------|-------------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 2 | 583 | 46.6 | 587 | 46.3 | 592 | 45.9 | 2 | 583 | 46.6 | 587 | 46.3 | 592 | 45.9 | | |
| 416.gamess | 2 | 1113 | 35.2 | 1112 | 35.2 | 1113 | 35.2 | 2 | 1111 | 35.2 | 1110 | 35.3 | 1111 | 35.2 | | |
| 433.milc | 2 | 368 | 49.9 | 368 | 49.9 | 368 | 49.9 | 2 | 367 | 50.0 | 368 | 49.9 | 367 | 50.0 | | |
| 434.zeusmp | 2 | 553 | 32.9 | 553 | 32.9 | 552 | 33.0 | 2 | 553 | 32.9 | 553 | 32.9 | 552 | 33.0 | | |
| 435.gromacs | 2 | 469 | 30.5 | 471 | 30.3 | 471 | 30.3 | 2 | 459 | 31.1 | 459 | 31.1 | 463 | 30.8 | | |
| 436.cactusADM | 2 | 749 | 31.9 | 745 | 32.1 | 745 | 32.1 | 1 | 341 | 35.1 | 344 | 34.8 | 341 | 35.0 | | |
| 437.leslie3d | 2 | 667 | 28.2 | 665 | 28.3 | 657 | 28.6 | 2 | 659 | 28.5 | 658 | 28.6 | 671 | 28.0 | | |
| 444.namd | 2 | 604 | 26.5 | 604 | 26.5 | 605 | 26.5 | 2 | 604 | 26.5 | 604 | 26.5 | 605 | 26.5 | | |
| 447.dealII | 2 | 534 | 42.9 | 544 | 42.1 | 546 | 41.9 | 2 | 514 | 44.5 | 510 | 44.9 | 527 | 43.4 | | |
| 450.soplex | 2 | 659 | 25.3 | 659 | 25.3 | 660 | 25.3 | 2 | 638 | 26.2 | 639 | 26.1 | 639 | 26.1 | | |
| 453.povray | 2 | 274 | 38.8 | 274 | 38.8 | 275 | 38.7 | 2 | 211 | 50.3 | 211 | 50.4 | 211 | 50.5 | | |
| 454.calculix | 2 | 467 | 35.3 | 468 | 35.3 | 468 | 35.3 | 2 | 462 | 35.7 | 465 | 35.5 | 466 | 35.4 | | |
| 459.GemsFDTD | 2 | 694 | 30.6 | 694 | 30.6 | 693 | 30.6 | 2 | 694 | 30.6 | 694 | 30.6 | 693 | 30.6 | | |
| 465.tonto | 2 | 565 | 34.8 | 571 | 34.5 | 570 | 34.5 | 2 | 530 | 37.2 | 529 | 37.2 | 531 | 37.1 | | |
| 470.lbm | 2 | 832 | 33.0 | 833 | 33.0 | 832 | 33.0 | 2 | 815 | 33.7 | 815 | 33.7 | 816 | 33.7 | | |
| 481.wrf | 2 | 483 | 46.2 | 483 | 46.2 | 483 | 46.3 | 2 | 483 | 46.2 | 483 | 46.2 | 483 | 46.3 | | |
| 482.sphinx3 | 2 | 1368 | 28.5 | 1369 | 28.5 | 1373 | 28.4 | 2 | 1248 | 31.2 | 1254 | 31.1 | 1253 | 31.1 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset has been used to bind processes to cores except
for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run



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Platform Notes

Default BIOS settings were used.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -Obo -ansi-alias
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSSE3 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll14 -auto

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100609.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100609.xml>

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For other inquiries, please contact webmaster@spec.org.

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