SPEC® CFP2006 Result

Intel Corporation

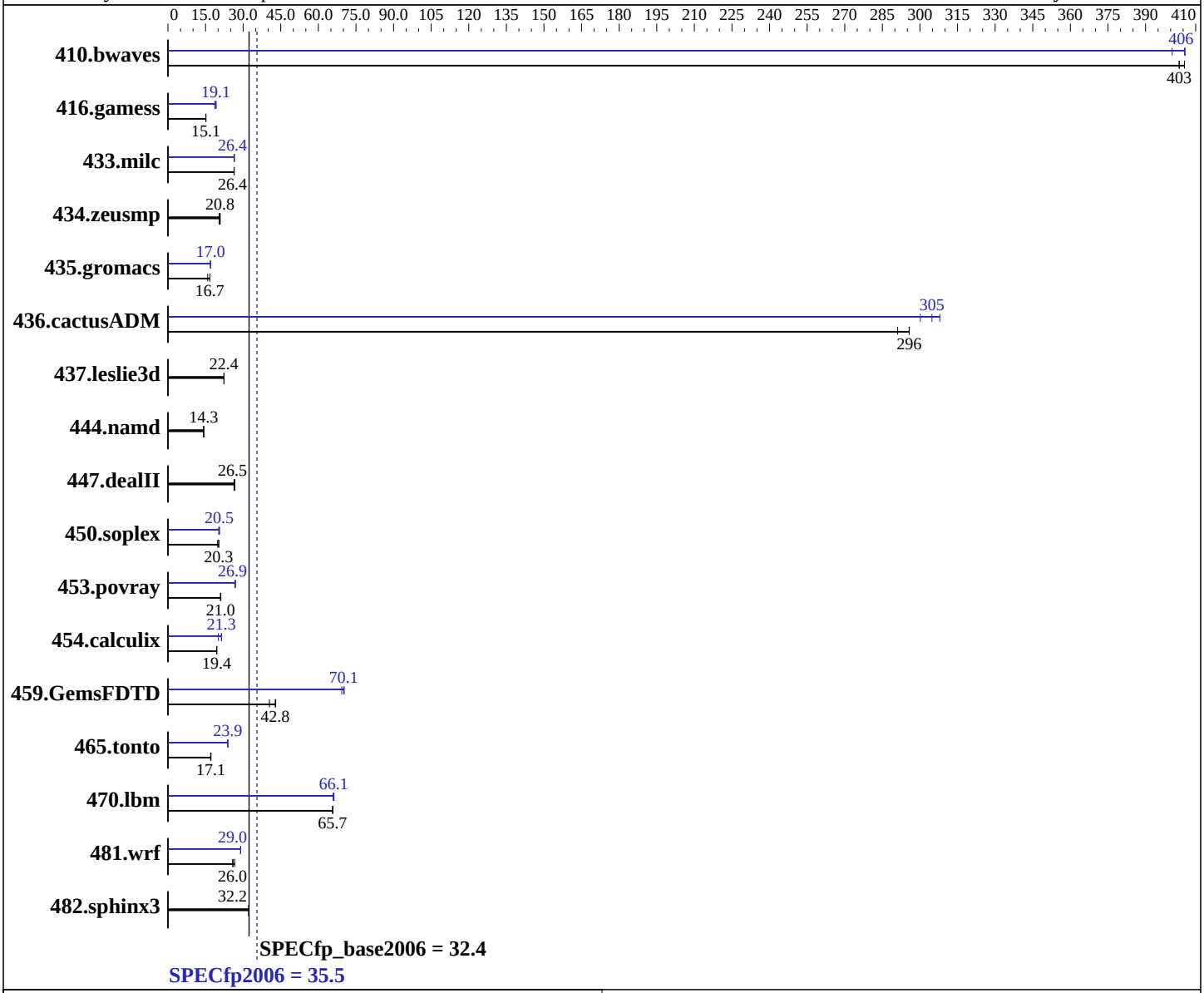
Intel QSSC-S4R (Intel Xeon X7550, 2.00 GHz)

SPECfp®2006 = 35.5

SPECfp_base2006 = 32.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon X7550
CPU Characteristics:	Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SuSe Linux Enterprise Server 11 Kernel 2.6.27.19-5 on x86_64
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (64x 4GB Quad-Rank DDR3-1066, ECC, CL9)
Disk Subsystem: 146 GB SAS, 10000RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.7	403	33.5	406	**33.7**	**403**	**33.5**	**406**	33.9	401	33.5	406
416.gamess	1293	15.1	1296	15.1	**1295**	**15.1**	1019	19.2	**1023**	**19.1**	1042	18.8
433.milc	348	26.4	347	26.4	**348**	**26.4**	**347**	**26.4**	346	26.5	347	26.4
434.zeusmp	**438**	**20.8**	437	20.8	446	20.4	**438**	**20.8**	437	20.8	446	20.4
435.gromacs	426	16.8	**426**	**16.7**	451	15.8	**421**	**17.0**	422	16.9	420	17.0
436.cactusADM	40.4	296	41.0	291	**40.4**	**296**	**39.2**	**305**	39.8	300	38.8	308
437.leslie3d	421	22.3	**420**	**22.4**	420	22.4	421	22.3	**420**	**22.4**	420	22.4
444.namd	563	14.2	**562**	**14.3**	562	14.3	563	14.2	**562**	**14.3**	562	14.3
447.dealII	434	26.3	428	26.7	**431**	**26.5**	434	26.3	428	26.7	**431**	**26.5**
450.soplex	412	20.3	420	19.8	**412**	**20.3**	406	20.6	412	20.2	**407**	**20.5**
453.povray	252	21.1	254	21.0	**253**	**21.0**	**198**	**26.9**	199	26.7	198	26.9
454.calculix	**424**	**19.4**	423	19.5	425	19.4	**387**	**21.3**	387	21.3	410	20.1
459.GemsFDTD	262	40.5	**248**	**42.8**	247	43.0	**151**	**70.1**	153	69.3	151	70.3
465.tonto	574	17.2	578	17.0	**574**	**17.1**	**412**	**23.9**	412	23.9	413	23.9
470.lbm	209	65.8	**209**	**65.7**	209	65.7	207	66.2	209	65.9	**208**	**66.1**
481.wrf	433	25.8	419	26.6	**430**	**26.0**	385	29.0	386	28.9	**385**	**29.0**
482.sphinx3	605	32.2	604	32.3	**605**	**32.2**	605	32.2	604	32.3	**605**	**32.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:

```
icc   -m64
```

C++ benchmarks:

```
icpc  -m64
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

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Peak Optimization Flags (Continued)

416.gamess: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -Ob0 -opt-prefetch -parallel`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-inline-calloc -opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32`

436.cactusADM: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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