# SPEC® CINT2006 Result
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## Fujitsu

PRIMERGY TX200 S5, Intel Xeon E5530, 2.40 GHz

$SPECint®\_rate2006 =$    208

$SPECint\_rate\_base2006 =$   193

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

Copies

| Benchmark | | |
|---|---|---|
| 400.perlbench | 16 | 210 |
| | 16 | 178 |
| 401.bzip2 | 16 | 135 |
| | 16 | 127 |
| 403.gcc | 16 | 167 |
| 429.mcf | 8 | 232 |
| | 16 | 219 |
| 445.gobmk | 16 | 214 |
| | 16 | 194 |
| 456.hmmer | 8 | 193 |
| | 16 | 146 |
| 458.sjeng | 16 | 202 |
| | 16 | 184 |
| 462.libquantum | 16 | 636 |
| | 16 | 636 |
| 464.h264ref | 16 | 264 |
| | 16 | 252 |
| 471.omnetpp | 16 | 136 |
| 473.astar | 16 | 142 |
| | 16 | 128 |
| 483.xalancbmk | 16 | 223 |

**SPECint_rate_base2006 = 193**

**SPECint_rate2006 = 208**

## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5530 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.67 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes) |
| Disk Subsystem: | 1 x SATA, 250 GB, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502 |

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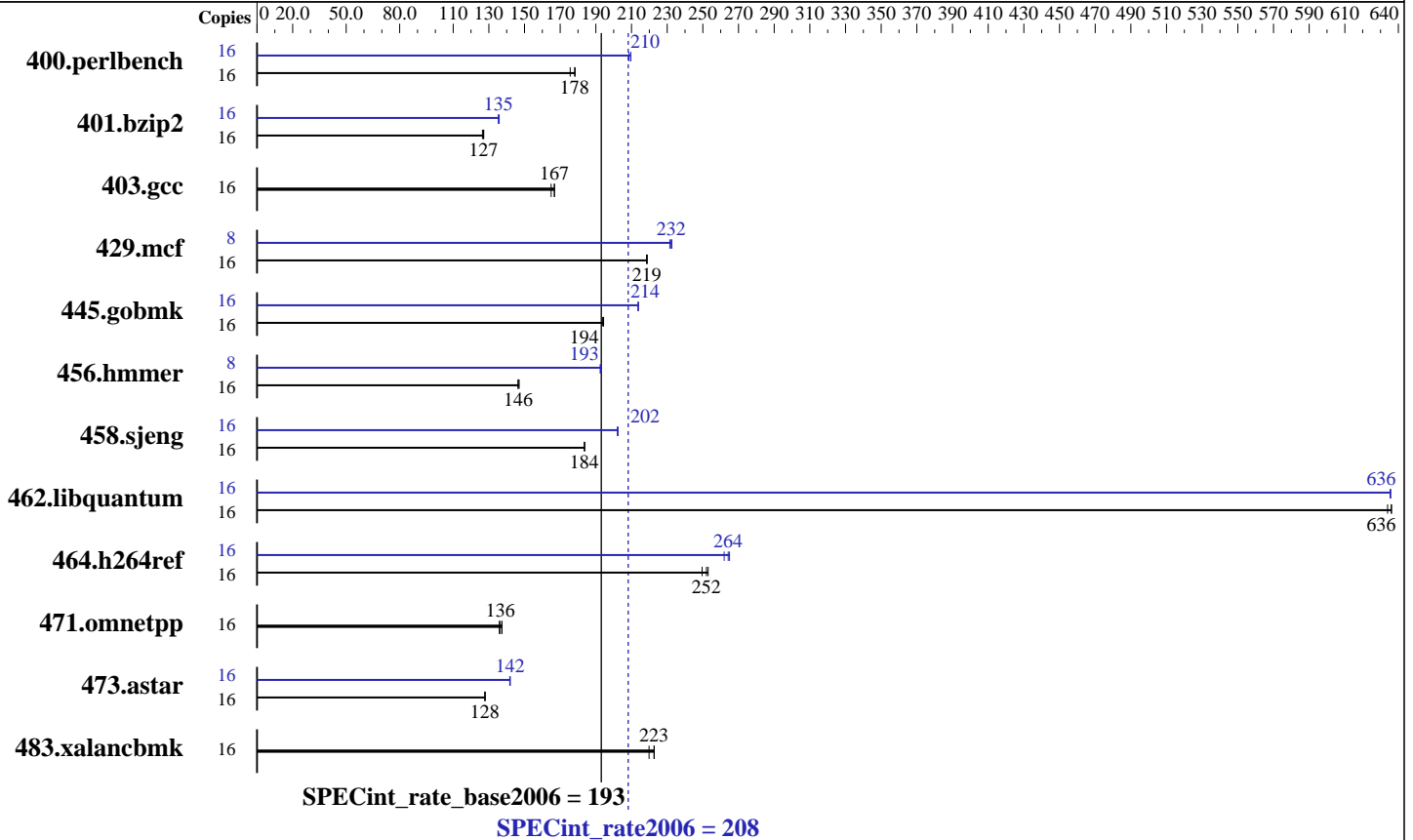
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**Test date:** Jul-2009
**Hardware Availability:** Jun-2009
**Software Availability:** Feb-2009

## Results Table

| Benchmark | Base Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Peak Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 400.perlbench | 16 | **878** | **178** | 890 | 176 | 876 | 178 | 16 | 746 | 210 | **746** | **210** | 749 | 209 |
| 401.bzip2 | 16 | 1221 | 126 | 1214 | 127 | **1216** | **127** | 16 | **1140** | **135** | 1140 | 135 | 1137 | 136 |
| 403.gcc | 16 | **773** | **167** | 781 | 165 | 772 | 167 | 16 | **773** | **167** | 781 | 165 | 772 | 167 |
| 429.mcf | 16 | 667 | 219 | 668 | 218 | **667** | **219** | 8 | 314 | 233 | **314** | **232** | 315 | 231 |
| 445.gobmk | 16 | **864** | **194** | 864 | 194 | 867 | 194 | 16 | 785 | 214 | **785** | **214** | 786 | 214 |
| 456.hmmer | 16 | 1016 | 147 | **1019** | **146** | 1020 | 146 | 8 | 387 | 193 | **388** | **193** | 388 | 192 |
| 458.sjeng | 16 | 1055 | 184 | **1054** | **184** | 1054 | 184 | 16 | 956 | 202 | 958 | 202 | **958** | **202** |
| 462.libquantum | 16 | 523 | 634 | **521** | **636** | 521 | 636 | 16 | 522 | 636 | 522 | 635 | **522** | **636** |
| 464.h264ref | 16 | **1405** | **252** | 1419 | 250 | 1400 | 253 | 16 | 1352 | 262 | **1340** | **264** | 1336 | 265 |
| 471.omnetpp | 16 | **733** | **136** | 736 | 136 | 728 | 137 | 16 | **733** | **136** | 736 | 136 | 728 | 137 |
| 473.astar | 16 | 878 | 128 | **879** | **128** | 879 | 128 | 16 | **791** | **142** | 791 | 142 | 793 | 142 |
| 483.xalancbmk | 16 | 496 | 223 | 502 | 220 | **496** | **223** | 16 | 496 | 223 | 502 | 220 | **496** | **223** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1066 MHz.

## General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

## Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

## Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX

Continued on next page

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## Base Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`

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## Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -ansi-alias -opt-prefetch`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: basepeak = yes

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static`
`-opt-malloc-options=3 -opt-prefetch`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.14.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.14.xml

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