



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

## SPECint®\_rate2006 = Not Run

### PRIMERGY BX620 S4, Intel Xeon L5430, 2.66 GHz

## SPECint\_rate\_base2006 = 121

CPU2006 license: 22

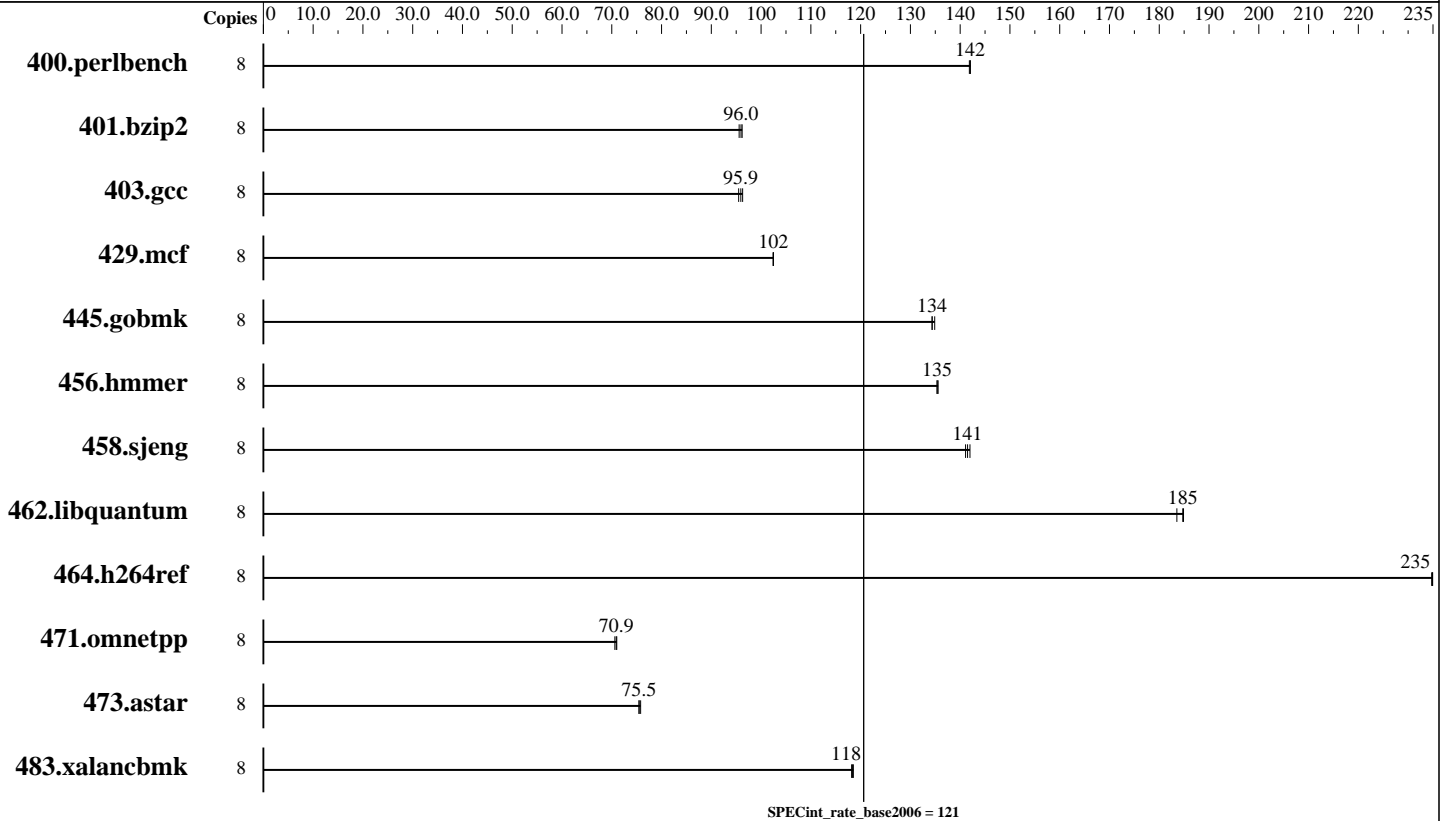
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Oct-2008

Hardware Availability: Oct-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon L5430  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x SATA, 120 GB, 5400 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	551	142	<u>550</u>	<u>142</u>	550	142							
401.bzip2	8	803	96.2	<u>804</u>	<u>96.0</u>	808	95.6							
403.gcc	8	669	96.3	674	95.5	<u>671</u>	<u>95.9</u>							
429.mcf	8	712	102	712	102	<u>712</u>	<u>102</u>							
445.gobmk	8	622	135	<u>625</u>	<u>134</u>	625	134							
456.hammer	8	551	135	<u>551</u>	<u>135</u>	551	136							
458.sjeng	8	682	142	<u>684</u>	<u>141</u>	686	141							
462.libquantum	8	903	184	<u>897</u>	<u>185</u>	897	185							
464.h264ref	8	754	235	754	235	<u>754</u>	<u>235</u>							
471.omnetpp	8	708	70.6	<u>705</u>	<u>70.9</u>	704	71.0							
473.astar	8	<u>744</u>	<u>75.5</u>	741	75.8	744	75.5							
483.xalancbmk	8	<u>466</u>	<u>118</u>	467	118	466	119							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset has been used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

## General Notes

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.16.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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