



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®\_rate2006 = 87.8

PowerEdge 1950 III (Intel Xeon X5260, 3.33 GHz)

SPECint\_rate\_base2006 = 82.7

CPU2006 license: 55

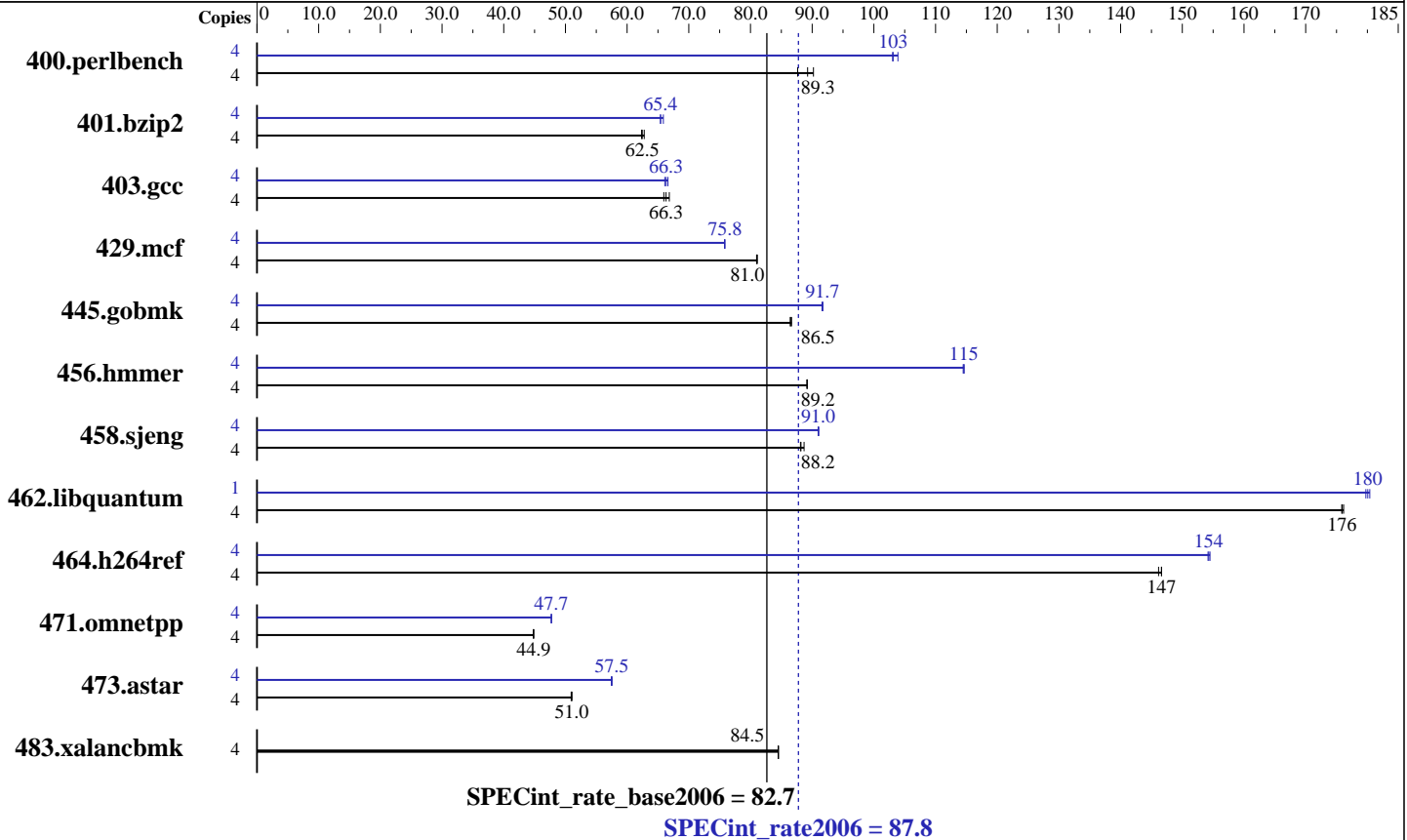
Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



## Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics:  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-667 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	446	87.6	<b>438</b>	<b>89.3</b>	433	90.2	4	376	104	379	103	<b>379</b>	<b>103</b>
401.bzip2	4	615	62.8	<b>618</b>	<b>62.5</b>	619	62.3	4	590	65.4	<b>590</b>	<b>65.4</b>	586	65.9
403.gcc	4	488	65.9	<b>486</b>	<b>66.3</b>	482	66.8	4	<b>485</b>	<b>66.3</b>	483	66.6	487	66.1
429.mcf	4	<b>450</b>	<b>81.0</b>	450	81.1	450	81.0	4	<b>481</b>	<b>75.8</b>	481	75.8	481	75.8
445.gobmk	4	<b>485</b>	<b>86.5</b>	484	86.7	485	86.4	4	457	91.7	<b>458</b>	<b>91.7</b>	458	91.6
456.hammer	4	419	89.1	<b>418</b>	<b>89.2</b>	418	89.2	4	326	114	326	115	<b>326</b>	<b>115</b>
458.sjeng	4	546	88.7	<b>549</b>	<b>88.2</b>	549	88.1	4	<b>532</b>	<b>91.0</b>	532	91.0	531	91.1
462.libquantum	4	<b>471</b>	<b>176</b>	471	176	471	176	1	115	180	115	180	<b>115</b>	<b>180</b>
464.h264ref	4	<b>604</b>	<b>147</b>	606	146	604	147	4	<b>574</b>	<b>154</b>	573	154	574	154
471.omnetpp	4	<b>557</b>	<b>44.9</b>	557	44.9	558	44.8	4	524	47.7	<b>524</b>	<b>47.7</b>	524	47.7
473.astar	4	551	50.9	550	51.1	<b>551</b>	<b>51.0</b>	4	489	57.4	<b>488</b>	<b>57.5</b>	488	57.6
483.xalancbmk	4	327	84.5	<b>327</b>	<b>84.5</b>	326	84.5	4	327	84.5	<b>327</b>	<b>84.5</b>	326	84.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

## General Notes

taskset was used to bind processes to cores except for 462.libquantum peak  
OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:  
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:  
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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
 -opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
 -no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static  
 -opt-malloc-options=3 -parallel -par-runtime-control  
 -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=block  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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