



SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

SPECint®2006 = 28.8

ASUS D5EB-DG Server Motherboard (Intel XEON X5482)

SPECint_base2006 = 25.3

CPU2006 license: 001034

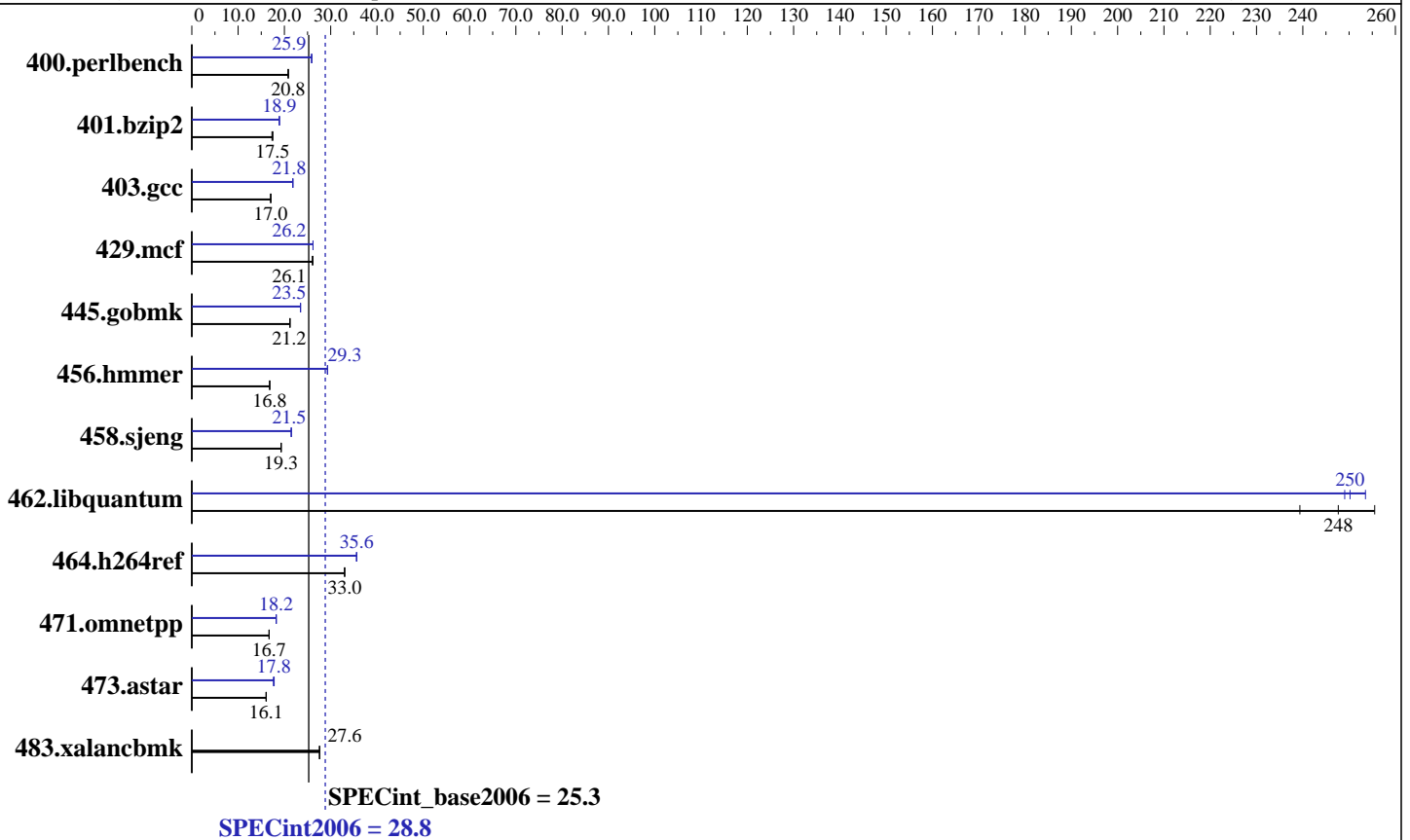
Test date: Aug-2008

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Feb-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Feb-2008



Hardware

CPU Name: Intel Xeon X5482
 CPU Characteristics: 1600 MHz Bus Speed
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 X 4GB PC2-6400F, CL5, FBDIMM)
 Disk Subsystem: Seagate ST3500830AS 500GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	468	20.9	469	20.8	470	20.8	377	25.9	378	25.9	378	25.9
401.bzip2	552	17.5	552	17.5	558	17.3	510	18.9	510	18.9	510	18.9
403.gcc	471	17.1	472	17.0	473	17.0	369	21.8	369	21.8	369	21.8
429.mcf	350	26.1	349	26.1	350	26.0	348	26.2	348	26.2	348	26.2
445.gobmk	495	21.2	495	21.2	495	21.2	447	23.5	447	23.5	447	23.5
456.hmmer	555	16.8	555	16.8	554	16.8	318	29.3	318	29.3	319	29.3
458.sjeng	627	19.3	628	19.3	627	19.3	564	21.5	563	21.5	563	21.5
462.libquantum	81.1	256	86.6	239	83.7	248	81.7	254	82.8	250	83.2	249
464.h264ref	670	33.0	670	33.0	670	33.0	622	35.6	622	35.6	622	35.6
471.omnetpp	374	16.7	374	16.7	375	16.7	343	18.2	343	18.2	343	18.2
473.astar	436	16.1	438	16.0	437	16.1	395	17.8	399	17.6	395	17.8
483.xalancbmk	250	27.6	250	27.6	250	27.6	250	27.6	250	27.6	250	27.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbenc: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 19:31:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 September 2008.