



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

CELSIUS R550, Intel Xeon X5470 processor

SPECint®2006 = 28.8

CPU2006 license: 22

Test date: Aug-2008

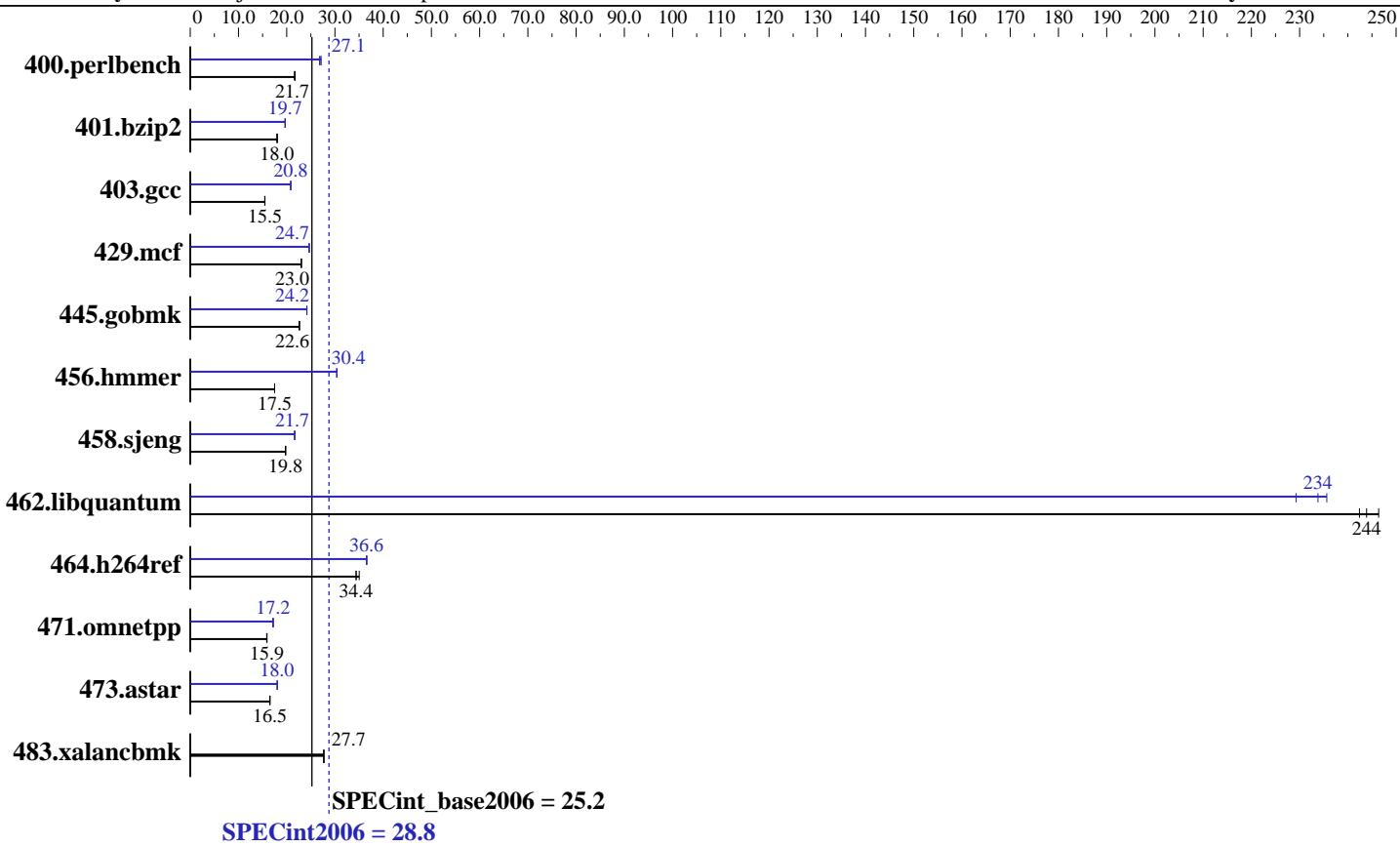
Hardware Availability: Sep-2008

Software Availability: Jun-2008

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

SPECint_base2006 = 25.2



Hardware

CPU Name:	Intel Xeon X5470
CPU Characteristics:	
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1 x SATA II, 400 GB, 7200 rpm
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20080602
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User, Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	451	21.7	449	21.8	452	21.6	360	27.1	361	27.1	365	26.8
401.bzip2	534	18.1	536	18.0	537	18.0	490	19.7	490	19.7	491	19.7
403.gcc	520	15.5	520	15.5	521	15.4	387	20.8	387	20.8	386	20.9
429.mcf	396	23.0	396	23.0	396	23.0	370	24.7	370	24.6	370	24.7
445.gobmk	464	22.6	464	22.6	463	22.6	434	24.2	434	24.2	434	24.2
456.hmmer	533	17.5	533	17.5	534	17.5	307	30.4	307	30.4	307	30.4
458.sjeng	613	19.7	612	19.8	611	19.8	559	21.7	560	21.6	558	21.7
462.libquantum	85.5	242	85.0	244	84.1	246	87.9	236	90.4	229	88.6	234
464.h264ref	643	34.4	644	34.4	632	35.0	606	36.5	604	36.6	605	36.6
471.omnetpp	394	15.8	393	15.9	393	15.9	363	17.2	363	17.2	365	17.1
473.astar	425	16.5	424	16.5	425	16.5	389	18.0	390	18.0	388	18.1
483.xalancbmk	249	27.7	249	27.7	249	27.7	249	27.7	249	27.7	249	27.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run OMP_NUM_THREADS set to number of cores (default).

Platform Notes

BIOS configuration:

Enhanced Speedstep Technology = Disable

C1 Enhanced Mode = Disable

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

SnoopFilter = Disable

General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

```
/opt/intel/cc/10.1.017/bin/icc -L/opt/intel/cc/10.1.017/lib
-I/opt/intel/cc/10.1.017/include
```

C++ benchmarks:

```
/opt/intel/cc/10.1.017/bin/icpc -L/opt/intel/cc/10.1.017/lib
-I/opt/intel/cc/10.1.017/include
```



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.017/bin/icc -L/opt/intel/cc/10.1.017/lib
-I/opt/intel/cc/10.1.017/include

401.bzip2: icc

456.hmmr: icc

C++ benchmarks:

/opt/intel/cc/10.1.017/bin/icpc -L/opt/intel/cc/10.1.017/lib
-I/opt/intel/cc/10.1.017/include

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
            -auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
            -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
            -auto-ilp32

458.jeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -O0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
              -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
              -no-prec-div -ansi-alias -opt-ra-region-strategy=block
              -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
            -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
            -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.20090714.xml>

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