



SPEC[®] CINT2006 Result

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IBM Corporation

SPECint[®]_rate2006 = 76.5

IBM System x3350 (Intel Xeon X3370)

SPECint_rate_base2006 = 66.3

CPU2006 license: 11

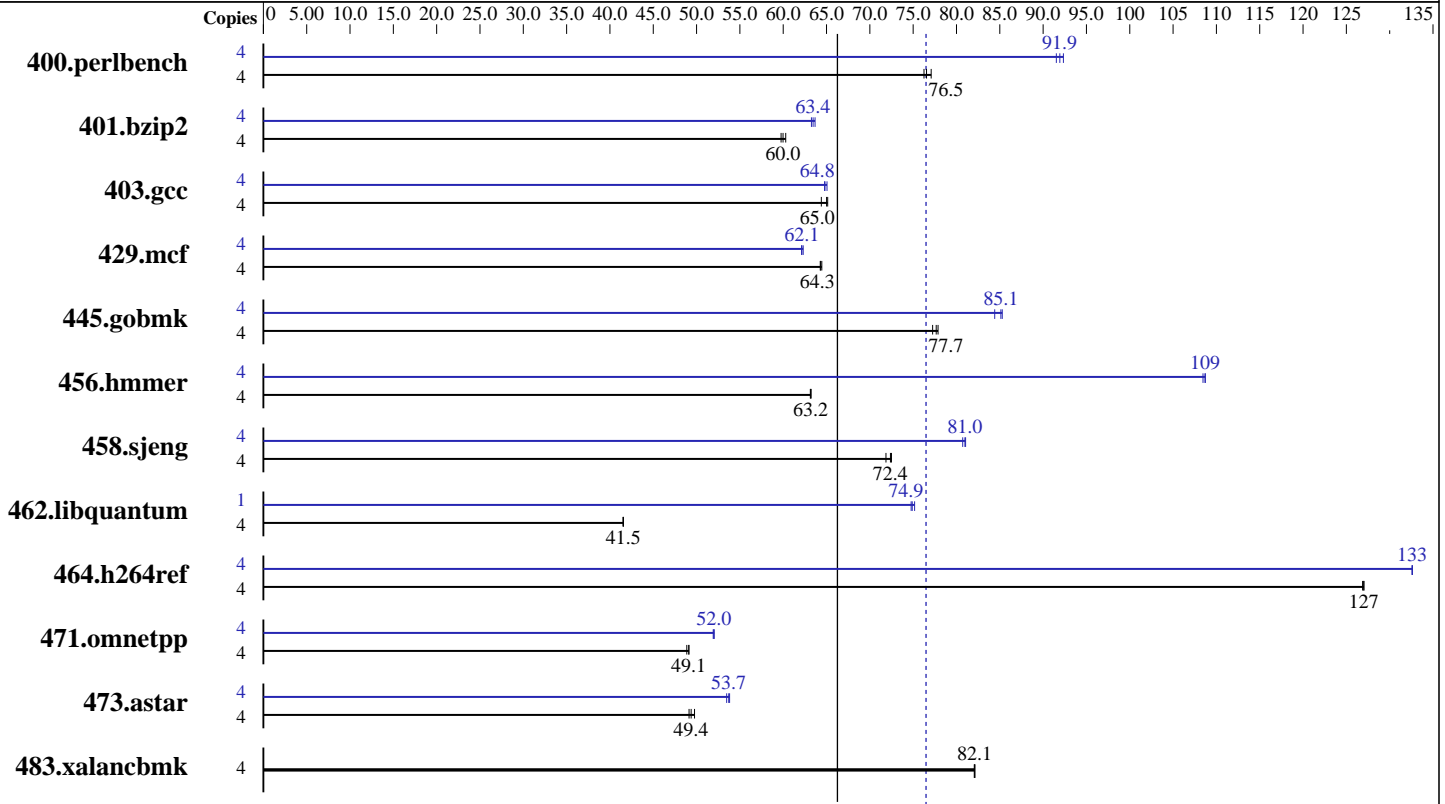
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jul-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2007



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Hardware

CPU Name: Intel Xeon X3370
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-6400E ECC)
 Disk Subsystem: 1 x 160 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	513	76.2	<u>511</u>	76.5	507	77.1	4	427	91.5	<u>425</u>	91.9	423	92.3
401.bzip2	4	640	60.3	646	59.8	644	60.0	4	610	63.3	608	63.4	606	63.7
403.gcc	4	495	65.0	494	65.1	500	64.4	4	497	64.8	497	64.8	495	65.1
429.mcf	4	567	64.3	568	64.3	566	64.5	4	585	62.3	587	62.1	587	62.1
445.gobmk	4	539	77.9	543	77.2	540	77.7	4	497	84.4	492	85.3	493	85.1
456.hmmmer	4	590	63.2	591	63.2	591	63.1	4	343	109	343	109	344	108
458.sjeng	4	668	72.5	674	71.9	669	72.4	4	598	81.0	597	81.1	600	80.7
462.libquantum	4	1996	41.5	1994	41.6	1996	41.5	1	276	75.2	277	74.9	277	74.8
464.h264ref	4	697	127	698	127	697	127	4	668	133	667	133	668	133
471.omnetpp	4	512	48.9	509	49.1	509	49.1	4	481	51.9	480	52.0	481	52.0
473.astar	4	564	49.8	572	49.1	569	49.4	4	523	53.7	522	53.8	525	53.5
483.xalancbmk	4	336	82.1	336	82.1	336	82.1	4	336	82.1	336	82.1	336	82.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to null
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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