



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

Express5800/i120Ra-e1  
(Intel Xeon L5240)

SPECfp<sup>®</sup>2006 = 22.4

SPECfp\_base2006 = 19.4

CPU2006 license: 9006

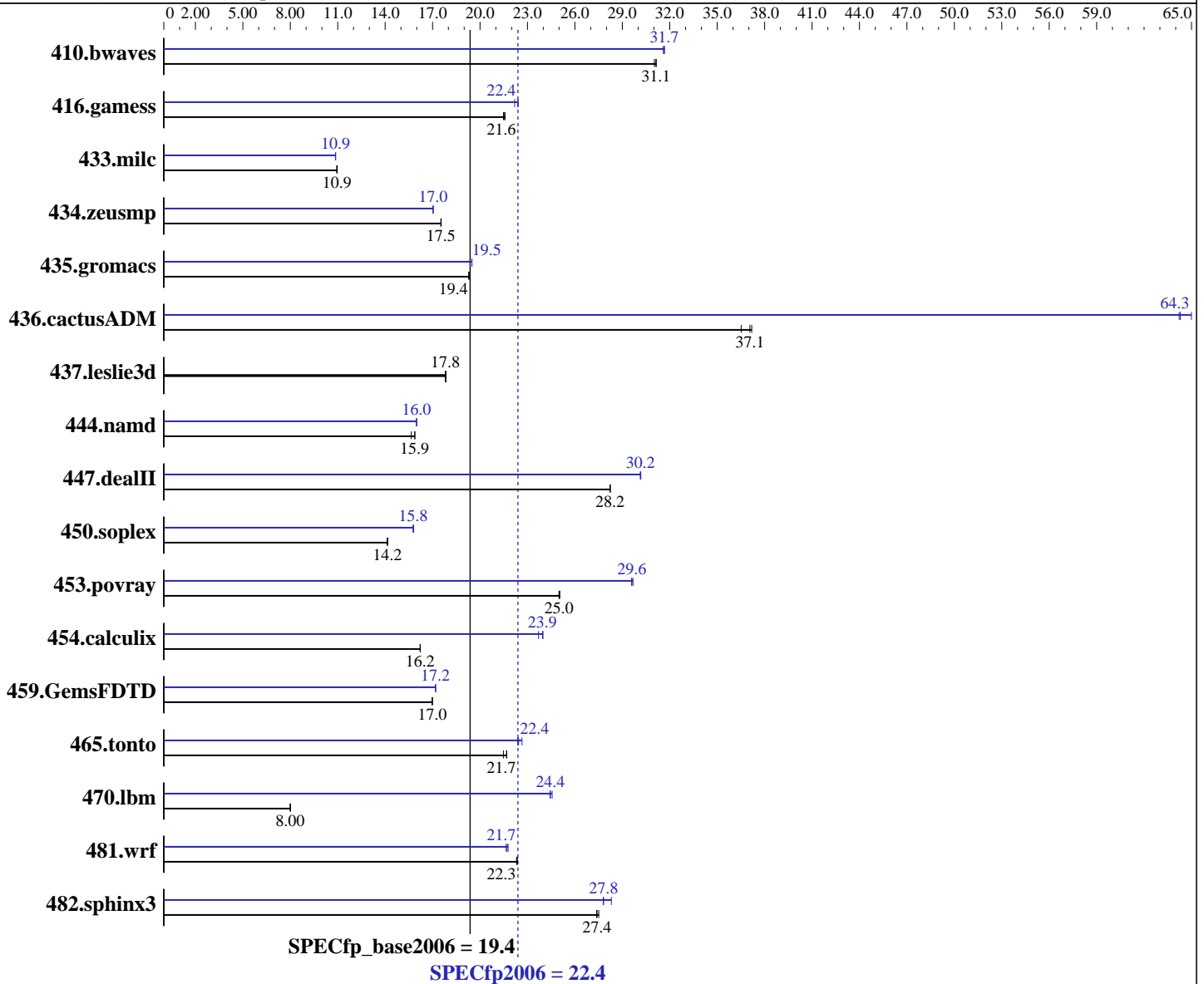
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2008

Hardware Availability: May-2008

Software Availability: Nov-2007



**Hardware**

CPU Name: Intel Xeon L5240  
 CPU Characteristics: 3.00 GHz, 6 MB L2, 1333 MHz bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip

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**Software**

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smpp  
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l\_cc\_p\_10.1.008, l\_fc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS

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L3 Cache: None  
Other Cache: None  
Memory: 16 GB (4x4 GB PC2-5300P, 2 rank, CL5-5-5, ECC)  
Disk Subsystem: 1x80.0 GB SATAII, 7200RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: binutils-2.17.tar.gz, Version 2.17

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	436	31.1	438	31.0	<b>437</b>	<b>31.1</b>	430	31.6	<b>429</b>	<b>31.7</b>	429	31.7
416.gamess	<b>909</b>	<b>21.6</b>	907	21.6	911	21.5	874	22.4	<b>874</b>	<b>22.4</b>	882	22.2
433.milc	839	10.9	<b>838</b>	<b>10.9</b>	837	11.0	844	10.9	<b>845</b>	<b>10.9</b>	845	10.9
434.zeusmp	<b>519</b>	<b>17.5</b>	519	17.5	519	17.5	534	17.0	534	17.0	<b>534</b>	<b>17.0</b>
435.gromacs	369	19.4	370	19.3	<b>369</b>	<b>19.4</b>	366	19.5	<b>366</b>	<b>19.5</b>	366	19.5
436.cactusADM	327	36.5	321	37.2	<b>322</b>	<b>37.1</b>	184	65.0	186	64.2	<b>186</b>	<b>64.3</b>
437.leslie3d	528	17.8	<b>528</b>	<b>17.8</b>	527	17.8	528	17.8	<b>528</b>	<b>17.8</b>	527	17.8
444.namd	512	15.7	<b>505</b>	<b>15.9</b>	505	15.9	<b>502</b>	<b>16.0</b>	502	16.0	502	16.0
447.dealII	405	28.2	405	28.2	<b>405</b>	<b>28.2</b>	379	30.2	<b>379</b>	<b>30.2</b>	380	30.1
450.soplex	<b>589</b>	<b>14.2</b>	589	14.2	590	14.1	528	15.8	529	15.8	<b>529</b>	<b>15.8</b>
453.povray	<b>213</b>	<b>25.0</b>	213	25.0	212	25.0	180	29.6	179	29.7	<b>180</b>	<b>29.6</b>
454.calculix	509	16.2	<b>509</b>	<b>16.2</b>	509	16.2	344	24.0	<b>344</b>	<b>23.9</b>	348	23.7
459.GemsFDTD	625	17.0	<b>625</b>	<b>17.0</b>	625	17.0	617	17.2	617	17.2	<b>617</b>	<b>17.2</b>
465.tonto	458	21.5	454	21.7	<b>454</b>	<b>21.7</b>	<b>438</b>	<b>22.4</b>	434	22.7	440	22.4
470.lbm	1715	8.01	<b>1717</b>	<b>8.00</b>	1718	8.00	559	24.6	<b>562</b>	<b>24.4</b>	563	24.4
481.wrf	500	22.4	501	22.3	<b>500</b>	<b>22.3</b>	513	21.8	516	21.6	<b>514</b>	<b>21.7</b>
482.sphinx3	<b>711</b>	<b>27.4</b>	712	27.4	708	27.5	689	28.3	701	27.8	<b>701</b>	<b>27.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores

## Platform Notes

Bios settings:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Enabled  
Intel SpeedStep Technology: Disabled



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### General Notes

All benchmarks compiled in 64-bit mode except 450.soplex, 470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode

### Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc  
  
Fortran benchmarks:  
ifort  
  
Benchmarks using both Fortran and C:  
icc ifort

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

### Base Optimization Flags

C benchmarks:  
-fast -parallel  
  
C++ benchmarks:  
-fast -parallel

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel

## Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast  
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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## Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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