



SPEC[®] CFP2006 Result

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Itautec

SPECfp[®]_rate2006 = 73.2

Servidor Itautec MX201 (Intel Xeon E5430)

SPECfp_rate_base2006 = 66.2

CPU2006 license: 9001

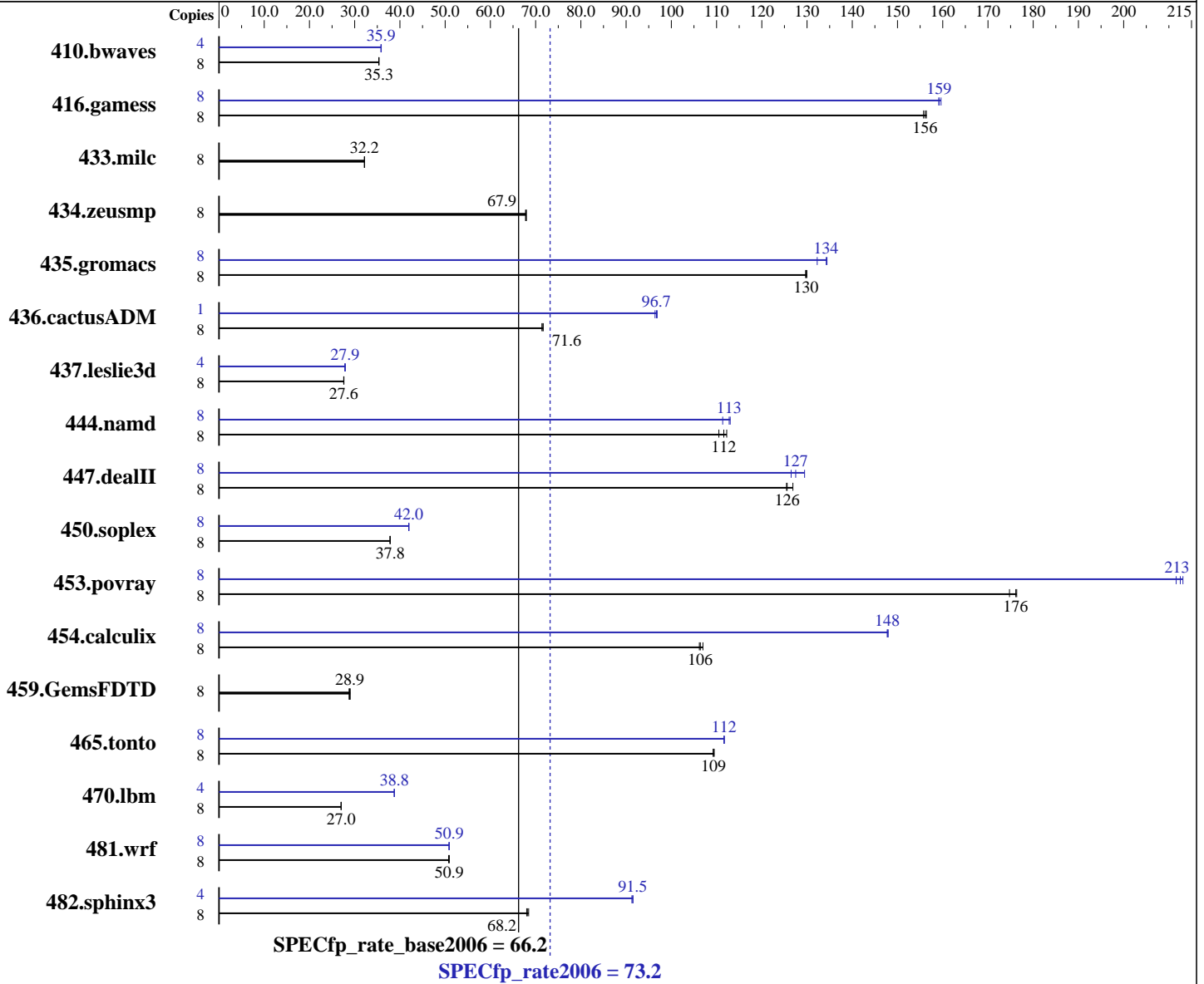
Test date: May-2008

Test sponsor: Itautec

Hardware Availability: Dec-2007

Tested by: Itautec

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics:
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler for Linux version 10.1
 Build 20080112 Package ID: l_cc_p_10.1.012, l_fc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem: 1 x SCSI, 73GB, 15000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.10.50

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3076	35.3	3075	35.4	3076	35.3	4	1515	35.9	1519	35.8	1516	35.9
416.gamess	8	1002	156	1003	156	1006	156	8	981	160	984	159	984	159
433.milc	8	2285	32.1	2284	32.2	2281	32.2	8	2285	32.1	2284	32.2	2281	32.2
434.zeusmp	8	1072	67.9	1071	67.9	1074	67.8	8	1072	67.9	1071	67.9	1074	67.8
435.gromacs	8	440	130	440	130	440	130	8	426	134	425	134	432	132
436.cactusADM	8	1339	71.4	1335	71.6	1334	71.7	1	123	96.9	124	96.7	124	96.4
437.leslie3d	8	2727	27.6	2726	27.6	2722	27.6	4	1352	27.8	1350	27.9	1346	27.9
444.namd	8	581	111	575	112	572	112	8	569	113	568	113	576	111
447.dealII	8	721	127	729	125	729	126	8	707	129	718	127	724	126
450.soplex	8	1766	37.8	1762	37.9	1763	37.8	8	1590	42.0	1589	42.0	1590	42.0
453.povray	8	242	176	244	175	241	176	8	200	213	201	212	200	213
454.calculix	8	621	106	617	107	620	106	8	447	148	446	148	447	148
459.GemsFDTD	8	2952	28.7	2939	28.9	2926	29.0	8	2952	28.7	2939	28.9	2926	29.0
465.tonto	8	719	109	721	109	720	109	8	705	112	705	112	705	112
470.lbm	8	4071	27.0	4070	27.0	4070	27.0	4	1416	38.8	1418	38.8	1419	38.7
481.wrf	8	1761	50.8	1756	50.9	1756	50.9	8	1755	50.9	1756	50.9	1758	50.8
482.sphinx3	8	2287	68.2	2277	68.5	2292	68.0	4	854	91.3	852	91.5	852	91.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' is used to bind benchmark copies to processor, except for 436.cactusADM at peak.



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Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

General Notes

This result was measured on the Servidor Itaotec MX201.
The Servidor Itaotec MX201 and the Servidor Itaotec MX221 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
/opt/intel/cc/10.1.012/bin/icc -L/opt/intel/cc/10.1.012/lib
-I/opt/intel/cc/10.1.012/include

433.milc: icc

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/cc/10.1.012/bin/icpc -L/opt/intel/cc/10.1.012/lib
-I/opt/intel/cc/10.1.012/include

Fortran benchmarks (except as noted below):
ifort

437.leslie3d: /opt/intel/fc/10.1.012/bin/ifort -L/opt/intel/fc/10.1.012/lib
-I/opt/intel/fc/10.1.012/include

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3
482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32
447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-opt-malloc-options=3
459.GemsFDTD: basepeak = yes
465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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