



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 66.5

Servidor Itautec LX211 (Intel Xeon E5440)

SPECint_rate_base2006 = 56.0

CPU2006 license: 9001

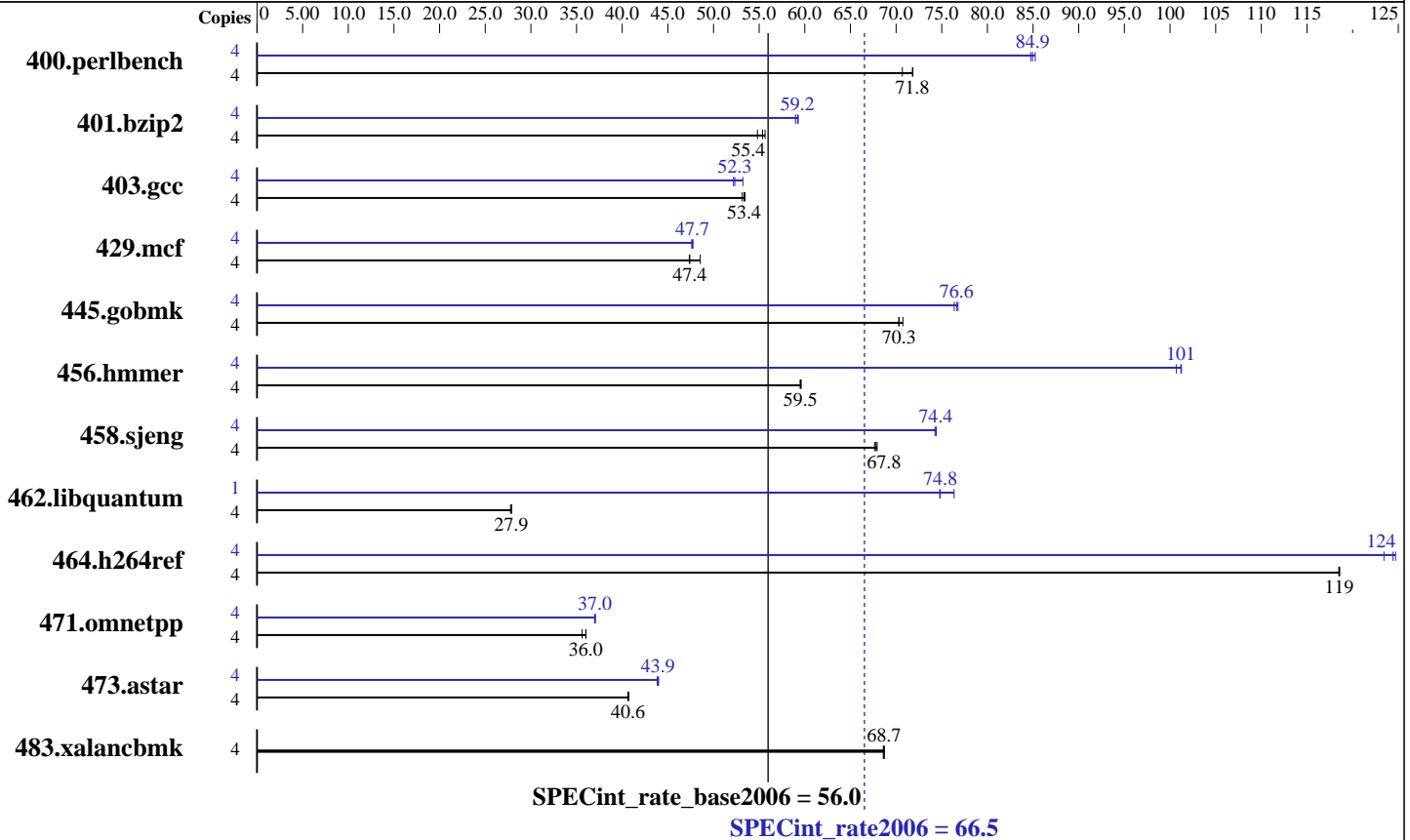
Test sponsor: Itautec

Tested by: Itautec

Test date: May-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics:
 CPU MHz: 2830
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (6 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: 1 x SCSI, 73GB, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux version 10.1 Build 20080112 Package ID: l_cc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run Level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.10.50
 MicroQuill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	553	70.7	<u>544</u>	<u>71.8</u>	544	71.8	4	458	85.2	<u>460</u>	<u>84.9</u>	461	84.7
401.bzip2	4	694	55.6	<u>697</u>	<u>55.4</u>	704	54.8	4	651	59.3	<u>652</u>	<u>59.2</u>	655	59.0
403.gcc	4	604	53.4	603	53.4	606	53.1	4	617	52.2	605	53.2	615	52.3
429.mcf	4	751	48.6	770	47.4	<u>770</u>	<u>47.4</u>	4	<u>765</u>	<u>47.7</u>	766	47.6	764	47.8
445.gobmk	4	<u>597</u>	<u>70.3</u>	597	70.3	593	70.7	4	<u>548</u>	<u>76.6</u>	546	76.8	549	76.4
456.hammer	4	<u>627</u>	<u>59.5</u>	627	59.5	626	59.6	4	371	101	<u>369</u>	<u>101</u>	369	101
458.sjeng	4	716	67.6	713	67.9	<u>714</u>	<u>67.8</u>	4	651	74.4	652	74.3	651	74.4
462.libquantum	4	2984	27.8	2974	27.9	<u>2975</u>	<u>27.9</u>	1	271	76.4	<u>277</u>	<u>74.8</u>	277	74.8
464.h264ref	4	747	118	<u>747</u>	<u>119</u>	746	119	4	<u>711</u>	<u>124</u>	710	125	717	123
471.omnetpp	4	694	36.0	<u>694</u>	<u>36.0</u>	702	35.6	4	676	37.0	<u>675</u>	<u>37.0</u>	674	37.1
473.astar	4	691	40.6	689	40.7	<u>691</u>	<u>40.6</u>	4	<u>639</u>	<u>43.9</u>	638	44.0	641	43.8
483.xalancbmk	4	<u>402</u>	<u>68.7</u>	402	68.7	402	68.6	4	<u>402</u>	<u>68.7</u>	402	68.7	402	68.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors, except for 462.libquantum at peak.

Platform Notes

BIOS configuration:
Hardware Prefetch Enabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

456.hmmmer: /opt/intel/cce/10.1.012/bin/icc
-L/opt/intel/cce/10.1.012/lib
-I/opt/intel/cce/10.1.012/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/sh/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itaotec-ic10.1-INT-intel64-linux-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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