



SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 26.7

Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECint_base2006 = 23.5

CPU2006 license: 55

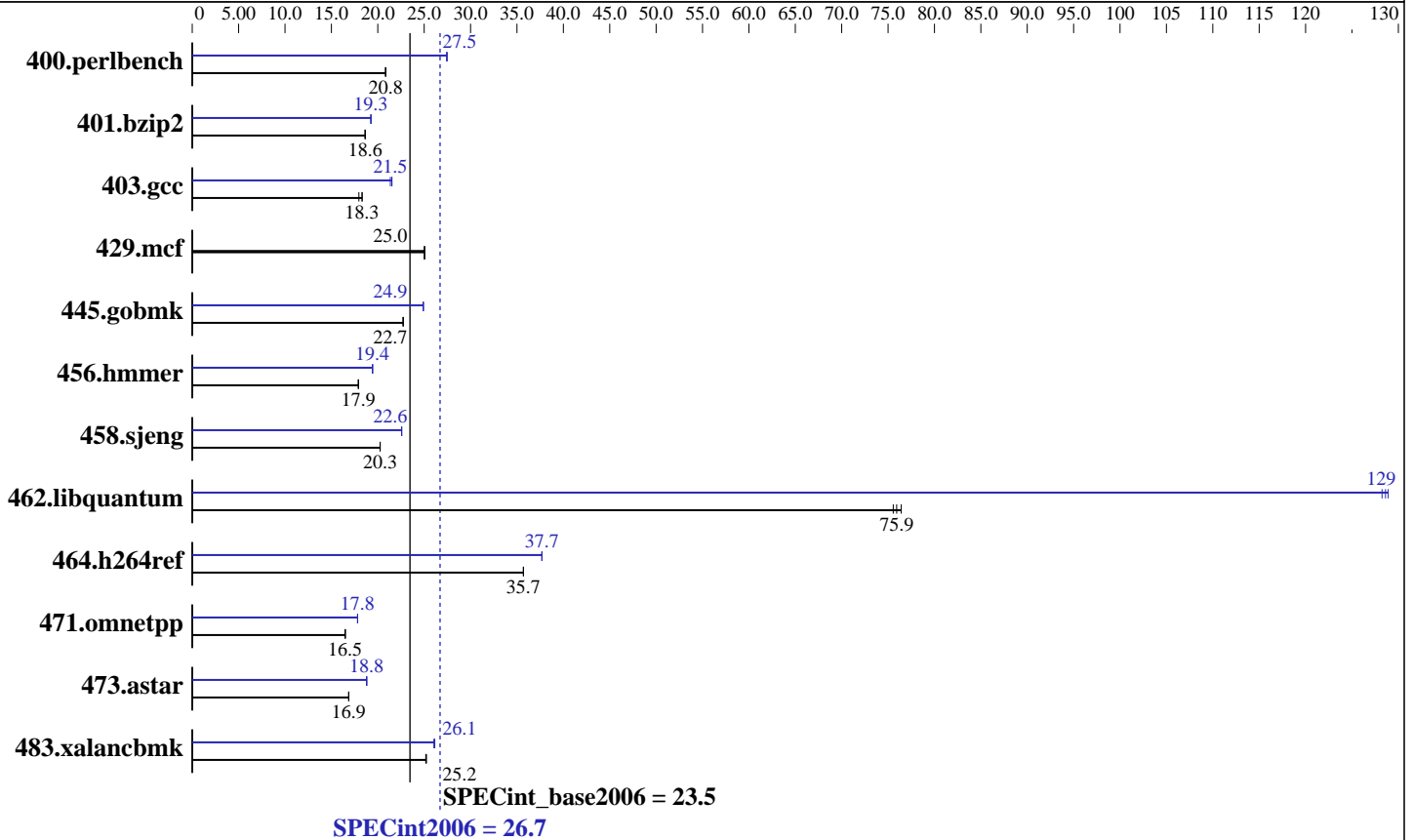
Test date: Apr-2008

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Mar-2008



Hardware

CPU Name: Intel Xeon X5272
 CPU Characteristics: 1600 MHz Bus Speed
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB 800 MHz CL5 FB-DIMM)
 Disk Subsystem: 1 x 160 GB SATA 7200 RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate (64-bit)
 Compiler: Intel C++ Compiler for IA-32, Version 10.1
 Build 20080312 Package ID: w_cc_p_10.1.021
 Microsoft Visual Studio 2005 SP1
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	469	20.8	<u>469</u>	<u>20.8</u>	468	20.9	356	27.4	356	27.5	<u>356</u>	<u>27.5</u>
401.bzip2	517	18.7	<u>518</u>	<u>18.6</u>	518	18.6	<u>501</u>	<u>19.3</u>	501	19.3	501	19.3
403.gcc	439	18.3	<u>440</u>	<u>18.3</u>	448	18.0	374	21.5	377	21.4	<u>374</u>	<u>21.5</u>
429.mcf	<u>364</u>	<u>25.0</u>	364	25.0	365	25.0	<u>364</u>	<u>25.0</u>	364	25.0	365	25.0
445.gobmk	462	22.7	<u>461</u>	<u>22.7</u>	461	22.7	<u>421</u>	<u>24.9</u>	421	24.9	421	24.9
456.hmmer	521	17.9	<u>521</u>	<u>17.9</u>	521	17.9	<u>480</u>	<u>19.4</u>	480	19.4	480	19.4
458.sjeng	597	20.3	598	20.2	<u>597</u>	<u>20.3</u>	536	22.6	536	22.6	<u>536</u>	<u>22.6</u>
462.libquantum	<u>273</u>	<u>75.9</u>	274	75.6	271	76.4	162	128	<u>161</u>	<u>129</u>	161	129
464.h264ref	<u>620</u>	<u>35.7</u>	620	35.7	620	35.7	587	37.7	587	37.7	<u>587</u>	<u>37.7</u>
471.omnetpp	<u>379</u>	<u>16.5</u>	379	16.5	379	16.5	351	17.8	<u>351</u>	<u>17.8</u>	351	17.8
473.astar	<u>416</u>	<u>16.9</u>	416	16.9	416	16.9	373	18.8	<u>373</u>	<u>18.8</u>	374	18.8
483.xalancbmk	<u>274</u>	<u>25.2</u>	274	25.2	273	25.2	265	26.1	<u>264</u>	<u>26.1</u>	264	26.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

BIOS Settings
Adjacent Cache Line Prefetch set to ON

Base Compiler Invocation

C benchmarks:
icl -Qstd=c99

C++ benchmarks:
icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qstd=c99
```

C++ benchmarks:

```
icl
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
/F512000000 libguide40.lib
```

```
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
libguide40.lib
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000
```

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Peak Optimization Flags (Continued)

456.hmmcr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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